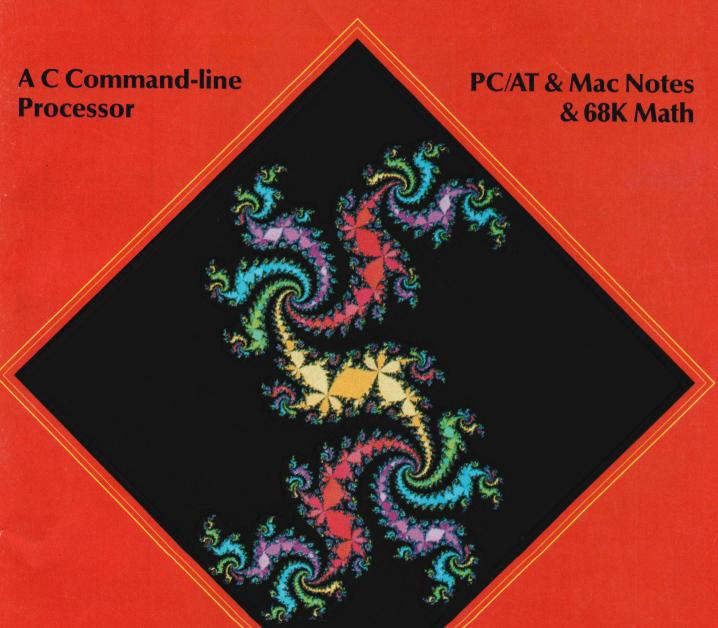
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Dr. Dobb's Journal (USPS 307690) is published monthly by M&T Publishing, Inc., 2464 Embarcadero Way, Palo Alto, CA 94303, (415) 424-0600. Second class postage paid at Palo Alto and at additional entry points.

Address correction requested. Postmaster: Send Form 3579 to *Dr. Dobb's Journal*, 2464 Embarcadero Way, Palo Alto, CA 94303. ISSN 0278-6508

Subscription Rates: \$25 per year within the United States, \$44 for first class to Canada and Mexico, \$62 for airmail to other countries. Payment must be in U.S. Dollars, drawn on a U.S. Bank. For subscription problems, call: outside of CA 1-800-321-3333; within CA 1-619-485-6535.

Foreign Distributors: ASCII Publishing, Inc. (Japan), Computer Services (Australia), Computer Store (New Zealand), Computercollectief (Nederland), Homecomputer Vertriebs GMBH (West Germany), International Presse (West Germany), La Nacelle Bookstore (France), McGill's News Agency PTY LTD (Australia), Progresco (France).

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May 1985 Volume 10, Issue 5

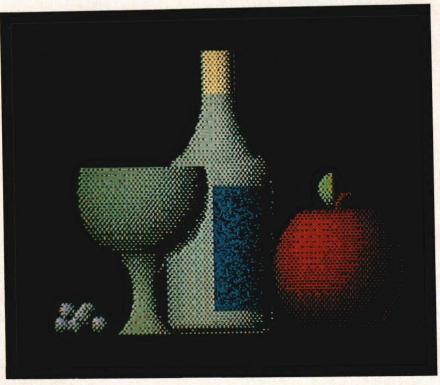
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Cover Artist

Our cover "Fractal Dragon," is the creation of Benoit Mandelbrot, Professor of the Practice of Mathematics at Harvard University. The most conspicuous characteristic of this dragon is that the eye and the imagination distinguish in it, in addition to the overall shape, an infinity of smaller features of every size down to the infinitesimal. For geometric shapes of this kind, Professor Mandelbrot coined the term "fractal." "Fractal Dragon" is a portion of "attractor" that occurs in a problem of dynamics. The cover illustration is a reproduction of Plate C5 of Mandelbrot's book *The Fractal Geometry of Nature* (Freeman 1982), drawn using computer programs by Mark R. Laff and V. Alan Norton. Copyright 1982 by B. B. Mandelbrot.

Referees Who Assisted With This Issue

Ted Carnevale, State University of New York at Stony Brook Allen Holub, *DDJ* Contributing Editor Robert Tripp, Editor-in-Chief of the late *MICRO* James Woomer, Commodore-64 Guru and Gazebo Builder



See Richard Rylander's "Solid Shape Drawing on the Commodore 64" page 50.

Dr. Dobb's Journal

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want to comment on what others have been saying about two subjects germane to this or any issue of *DDJ*: graphics and copy-protected disks.

Frank Gaude regards the iconic interface of the Mac and its imitators as a regression to preliterate cave-scratchings. In his *Z-News* he says, "Long ago we used hieroglyphs to communicate. Then after a struggle an alphabet developed. Now we go back to pictures: icons. How many glyphs can we remember?"

In the macaphony of the current iconolatry this cautionary message is worthy of attention, but I suggest that it's not the whole story. The function of software is to realize metaphors. Icons are capable of doing that well but are no help in stepping outside existing metaphors to invent new ones; no help, that is, in developing new software. I would agree that icons are mnemonic for users and superfluous for programmers and let it go at that, but for the fact that I have recently seen a prototype of a system that takes icons a step further, a system in which actions can be assigned user-created visual tokens, and can be combined in ways that visually and kinaesthetically convey the form of the combination. Active, growing icons.

Furthermore, while the Mac icons are static tokens rather than elements of a true language, that is not true of all computer graphic aids to communication. French researchers are currently developing the means for transmitting sign language over telephone lines at commercial data rates. Their idea is to extract from a sequence of video frames just the essential components of a gesture and pass those along. This does not, I should make clear, mean matching the gesture to some template from a database of acceptable gestures; rather it means producing a real-time cartoon version of the gesturing party. The cartoons, subject to idiomatic and dialectical variations, are truly linguistic graphic elements, which is just what icons aren't.

As for copy-protected disks

"... imagine yourself buying a new car. You have narrowed your choice to two cars, identical in all respects except that one will travel 100 mph and the other has a governor on it and cannot go past 55. Which car would you buy?"—Michael D. Brown, Central Point Software, producer of Copy II.

"People can rest assured that...we will always offer versions of our products that are not copy-protected."—Philippe Kahn, Borland International.

"Companies like Multimate and Borland have the right idea. Lotus, Ashton-Tate and MicroPro are the bad guys."—Edward Messerly, SF regional administrator of the GSA.

"... a growing number of publishers are dropping traditional copy-protection methods, which they conclude have created ill-will and inconvenience for users.... Among the publishers who have abandoned copy-protection schemes is MicroPro.... Many software publishers, however, hope to see new protection methods...."—Time/Life Access:Apple.

"These people are nutty."—Edward Messerly.

Yes. Something is nutty when a vendor can brag about also selling non-broken goods. It's really simple, isn't it? Vendors of software who want to stay vendors of software must find ways to profit from their efforts without breaking the tools they sell. The ability to copy files and disks is a facility I bought with my hardware and operating system. To try to sell me a program designed to subvert my system and hamper its operation is to try to enlist me in vandalism against my own property. Even granting that copy-crippling is a morally defensible policy, how could anyone believe that it was commercially viable in the long run? Nutty.

Michael Swaine

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Anorexia Cured

Dear DDJ:

I read with some interest the article 'Fatten Your Mac' that appeared in your January [1985, #99] issue and forthwith did a fattening on my Macintosh. There are a number of points that I would like to add to the article, however. Firstly, a fine supplier of the 256K DRAM chips was omitted. I refer to Microprocessors Unlimited, of Beggs, Oklahoma, (918) 267-4961. I was able to phone them late Friday, and the chips they sent out on Monday arrived here (in Seattle) on Tuesday. I was quite pleased with the price and the service. They accept Visa, and they even sent a brief note explaining proper handling of the chips.

Secondly, it is not necessary, though it may be desirable, to destroy the 64K DRAM chips. I desoldered the original chips with an Ungar model 2000 desoldering tool, and was able to remove the solder, let the board cool, then pry the chips from the motherboard, all without clipping a single lead. This damaged two printed circuit traces (pulled them off in the prying process); as the article recommends, there is a safer way to proceed. My technique cannot be advised in the absence of an excellent desoldering tool, or for those not confident in their abilitites to make printed circuit repairs. The article had so fired my enthusiasm that I yanked out the 64K DRAMs days before the 256K DRAM circuits arrived. I was able to verify the repairs, and the removed memory chips, by installing sockets, plugging the 64K DRAMs into those sockets, and using the Macintosh. The nature of the lifted printed circuit traces (they showed copper color on the ICs to which the traces stuck) made detection easy,

and no faults were missed. The board worked on the first try.

Thirdly, the multiplexer can be installed either piggybacked on another chip, or can be placed on a separate board, attached on the seven solder pads referred to in the article. Those seven pads include all logic signals and power required for the multiplexer. I installed wire-wrap pins in those pads, made a small board for the multiplexer from .100-inch hole-grid breadboard material, and pushed the small board over the wire-wrap pins, wire-wrapping the connections atop it to hold it in place. I found this less tricky than the piggyback soldering job would have been, and no leads of the multiplexer needed to be bent.

Fourthly, the multiplexer recommended is 74F253 or 74AS253; while these will do quite well, so will the 74F153 or 74AS153; which are slightly less expensive; the only difference is the nature of the DISABLE function, which (in this application) is always unused. All pin connections are identical to the '253.

I found that the multiple-layer board, having extra copper in the interior layers, was rather more difficult than most to desolder; I had to apply more heat (use a higher temperature on the desoldering iron) than usual, to complete the solder removal within a safe time. I did not notice any fragility of the printed circuit (no more than most, at any rate), and my procedure was, as I have mentioned, rather stressful. At one point, I fumbled, and dropped the board onto the concrete floor—there was no damage.

Prices were better than I expected, about \$220 total cost for the chips, and that figure is dropping almost daily. Time required was almost exactly four hours. Since Apple charges

about six hundred dollars more for the 512K Mac, it hardly pays to buy one; the fine folks in Cupertino are willing, in effect, to pay me handsomely for those hours, if I convert my 128K instead.

Instead of 'Macintosh' and 'Fat Mac', I will be using the terms 'Macintosh' and 'Anorexic Mac' in the future.

Sincerely grateful, John Whitmore FM-15 Physics Dept., University of Washington Seattle, WA 98195

Going PUBlic

Dear DDJ:

PUBlic files have been used successfully on a wide variety of CP/M computers since the publication of our November 1984 [DDJ, #97] article "CP/M 2.2 goes PUBlic." However, we want again to caution readers that PUBPATCH must be installed on an unmodified CP/M 2.2 BDOS!

Several users have discovered that their Heath Co. CP/M 2.2.03 has been altered with a patch at 06E1 and 0DEE-0DF3 (relative to the start of the BDOS). The Heath patch causes the BDOS to stop building the disk group-allocation vector when it encounters the first directory entry having deleted data (E5) codes in both bytes 0 and 1. If PUBPATCH is then installed in this BDOS, the symptoms will be files that disappear from the end of the disk directory!

Heath users with this problem should patch location 06E2h back to its original value '06D2h' (relative) before installing PUBPATCH.

RELATIVE ADDRESS 06E1'
ORIGINAL 2.2 BDOS JZ 06D2'
HEATH 2.2.03 BDOS JZ 0EEE'

Two readers have reported problems with PUBLIC.ASM, used to set or reset the PUBlic file attribute bit. Updated code can be found in [Listing One (page 13)]. The first fix corrects a bug in v. 1.0 that prevents PUBLIC from setting the attribute for a very large file. The second takes care of a bug in the standard CCP (but not in ZCPR), which fails to restore the default dma address before continuing to execute a SUBMIT file.

No changes are required to PUB-PATCH itself.

> Sincerely, Bridger Mitchell, Derek McKay Plu*Perfect Systems Box 1494 Idyllwild, CA 92349

Turbo Bug

Dear DDJ:

I have been using Borland International's excellent Turbo Pascal compiler for several months now (I started with version 1) and I think it is by far the best implementation of any language I have every seen. The quick compiler and built-in editor have reduced the pain of correcting typos far enough to let me appreciate the virtues of Pascal. In the process I have discovered two things about the language that may be of interest to others. I am including listings of three short demo programs.

First, the "initialized constants" mentioned briefly in both the reference manual and the Turbo Tutor manual are really initialized static variables. This is clear from a close reading of either manual, but it is never stated in so many words, nor do any of the example programs demonstrate it. Compiling and running the [program in Listing Two (page 13)] will demonstrate that the typed constant I in the procedure ShowStatic behaves exactly like a static variable in C.

Compiling and running the program [in Listing Three (page 14)] will demonstrate a problem I discovered while trying to process the files produced by a linker cross reference utility. The Trunc and Round functions in both the MSDOS and CP/M versions of Turbo Pascal produce a run time

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error if you attempt to convert a real value of -32768 (8000H). Since this is a legal integer value, and right in the middle of the range of hex numbers, I think this has to be called a bug.

Listing Four (page 14) shows one way of programming around this problem. It works just fine when using real numbers and integers to represent hex numbers between 0 and FFFFH, but it would not be very useful in an application that requires both positive and negative real numbers.

Sincerely, William Ames 8720 Topanga Cyn. Blvd. #8 Canoga Park, CA 91304

Borland assures us that this bug will be fixed in Version 3.0 of Turbo Pascal.—Ed.

Fast Hartley Transform

Dear DDJ,

We were pleased to see Ron Ullmans's letter in the December 1984, issue of *Dr.Dobb's Journal* regarding the Hartley Transform and the accompanying listing of a software program. We, too, consider its advantage to be great.

Your readers may wish to know Stanford University has proprietary rights on this technology, developed by Professor Ronald N. Bracewell. Patent rights are pending.

Use of the software without a license may be an infringement of those proprietary rights. However, a license under reasonable terms to the technology is available from Stanford.

Incidentally, we have called this development the "Bracewell" transform, in recognition of Professor Bracewell's fundamental contribution.

Sincerely,
Lisa Kuuttila
Consulting Associate
Office of Technology
Licensing
Stanford University

DDJ

Letters (Text begins on page 8) Listing One

```
; UPDATE PATCHES FOR PUBLIC.ASM
vers
                                ;update PUBLIC.ASM version number
; fix 1. Insert the following code in the 'SAMEXT' routine
          immediately preceeding its 'ret' instruction.
        rnz
                        ; v 1.1
        inx
                h
                        ; extent is 0, check overflow (s2) ext.
        inx
                h
        mov
                a.m
        ani
                7Fh
        ret
                        ; end of SAMEXT routine
; fix 2. Insert the following macro statement at the label 'xit:'
xit:
        dobdos dmafn,80h
                                ;restore default dma for SUBMIT under ccp.
        lspd
                ustack
        ret.
                                                                                                 End Listing One
```

Listing Two

Listing of: STATIC.PAS

```
program TestStaticVariables;
{ This program demonatrates that the "typed constants" of Turbo Pascal }
{ are really static variables. Each time the main loop calls ShowStatic }
{ the typed constant I is incremented by 1. When ShowStatic is called }
{ again the incremented value is written to the CRT. }
var
    Ch : Char;
    I : integer:

procedure ShowStatic;
{ This prints and then increments the "constant" I }
const
    I : integer = 0;
```

TURBO PASCAL Program Lister, Copyright 1983 Borland International

```
begin
  Writeln(I);
  I := I + 1;
  end;
function UseHeap(L : integer) : integer;
{ This is included to demonstrate that the typed constant above is not }
{ distroyed when another procedure uses the heap or stack. }
var
  I : integer;
  J : ^integer;
begin
  I := -L;
  riew(J);
  J^ := L;
 L := L + 1;
  if L (5 then
   L := UseHeap(L);
  Dispose(J);
 UseHeap := L
 end;
```

(Continued on next page)

Letters (Listing Continued, text begins on page 8) Listing Two

```
begin
  repeat
  read(Kbd, Ch);
  I := UseHeap(0);
  ShowStatic;
  Until Ch = #13;
end.
```

End Listing Two

Listing Three

```
TURBO PASCAL Program Lister, Copyright 1983 Borland International
Listing of: CNVRT1. PAS
program RealToInteger1;
{ This program can be used to demonstrate a problem in }
{ converting real numbers to integers. }
var
 Nmbr : real;
      : integer:
  Readln(Nmbr);
   { if Nmbr = 32768.0 the following produces a runtime error }
    if (Nmbr ) 65535.0) or (Nmbr (0) then
    { I am only interested in the range of 4 digit Hex numbers }
     Writeln(' overflow error')
    else begin
     if Nmbr ( 32768.0 then
       I := Trunc(Nmbr)
      PISP
       I := Trunc (Nmbr - 65536.0);
     Writeln(I);
     erid;
    prid:
```

End Listing Three

Listing Four

```
TURBO PASCAL Program Lister, Copyright 1983 Borland International
Listing of: CNVRT2. PAS
program RealToInteger2;
{ this shows one way around the bug demostrated in version 1 }
  Nmbr : real;
       : integer:
begin
  Readin(Nmbr);
   { 32768.0 is converted correctly by the following code }
    if (Nmbr ) 65535.0) or (Nmbr (0) then
    { I am only interested in the range of 4 digit Hex numbers }
      Writelm(' overflow error')
    else begin
      if Nmbr ( 32768.0 then
        I := Trunc (Nmbr)
      else
          I := Trunc (Nmbr - 65535.0):
          I := I - 1;
        erid:
      Writeln(I);
      erid;
    end.
```

End Listing Four

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DR. DOBB'S CLINIC



by D. E. Cortesi

Welcome, Microans

It seems that *DDJ* has picked up a batch of new readers, the erstwhile subscribers to the late, lamented *Micro*. Let's recap the purpose of this column for their benefit. It's supposed to be "a place for the display of techniques and discoveries." That's what we said the first time it appeared, in May 1981. Also, "We'd like to tell of unobvious uses for standard utilities. We want to uncover errors in documentation, to warn people away from pitfalls, and to show off those *'Eureka!'* moments that make systems work rewarding."

For a long time, most of the contributions to the Clinic involved CP/M and the Z80. Nowadays, we hear more from MSDOS/8086 users. Well, stand back, Zilog lovers; make room, Intel-lects! Now we have among us people who understand real computers like the 6502, the 6809, and the 68000 and real operating systems like OS9, SOS, and PRODOS. We can barely wait for the neat contributions they'll be sending in. Soon.

Wood Blocks

As bait for the *Micro* readers (hey, folks, we really like reader contributions here), we present our wood blocks program for the Atari. It's a little thing (see Listing at right) that we ran up one day while experimenting with the start, select, and option keys. These three keys control the three lowest bits of a byte in the Atari address space. The program samples that byte, and each time the combination of operated keys changes, it emits a heavily damped *klonk* at a different pitch. Play it by mashing down all three keys and then sort of

working your fingers around.

The simulation of damped tones is pretty good. Experiment with the damping factor and with the expression that generates a note value from the key combination.

Roll Over, Keyboard

We've an interesting letter here from Charles Davis, who lives in a town with the delightful name of Flower Mound, Texas. Besides submitting some throughput timings for an Atari system (at last!), he's been studying keyboards:

"Perhaps you could survey another area of concern. I am either a very good typist or a very sloppy one! I need a keyboard with what used to be called 'N-key rollover.' These are hard to find. The current generation of computers and terminals does not seem to supply this feature. Most marketing and sales people ... are unaware of the feature and therefore the sales literature does not mention it, even when it is present.

"A survey of the popular computers and terminals with reference to rollover would be interesting to me and, I think, to many of your readers. My procedure for checking rollover is to depress several keys in sequence, holding each until all are depressed, then I lift them all in the same order. I observe what happens on the screen. There seem to be three types of response:

- the first N keys (type 1)
- the last N keys (type 2)
- only the first and last keys (type 3)

The major variable is how big N is. Rarely must I find a helper with more fingers. One terminal manufacturer admitted that their terminals had N-key rollover and that N was supposed to be 5, but some key sequences did not work right so they would only guarantee N to be 3. This points up the weakness of my procedure: I do not do an exhaustive check and some problem characters may be missed. Without knowledge of the key matrix and the scanning algorithm, it would take too long to find the problems."

Davis sent along the results from some terminals he'd tested (see Table 1 on page 17). However, our first at-

- 10 REM WOODBLOX FOR ATARI, D.E. CORTESI
- 20 KEYBYTE=53279
- : DAMPING=0.29
- 30 SAMPLE=PEEK (KEYBYTE)
 - : IF SAMPLE=BUTTONS THEN GOTO 30
- 40 BUTTONS = SAMPLE
 - : PI TCH=12*(8-SAMPLE)
 - : VOLUME=15
- 50 SOUND O, PITCH, 12, VOLUME
 - : VOLUME = VOLUME VOLUME *DAMPING
 - : IF VOLUME >= 1 THEN GOTO 50
- 60 SOUND 0,0,0,0
 - : GOTO 30

Terminal	Response	Rollover
ADDS Regent 20	type 1	N > 10
ADDS Viewpoint	type 1	N > 10
Appollo DN600 Workstation	type 1	N > 10
Atari computers	type 3	n.a.
CIT 101E	type 1	N = 2
Daisy Logician Workstation	type 1	N > 10
Hazeltine 1410	type 3	n.a.
Heath/Zenith 19 and 29	type 4	N=2
HP-85	type 3	n.a.
HP-9816	type 1	N > 10
IBM PC/XT	type 1	N > 10
Perkin-Elmer Bantam 550	type 1	N = 3
Soroc IQ-120	type 3	n.a.
Televideo 950	type 1	N = 2
Wyse 50	type 1	N = 3

Table 1

Charles Davis' survey of the number of keys that can be "rolled over" on different terminals. Types are: 1, first N keys; 2, last N keys; 3, first and last only; 4, first N keys then garbage.

Command	Type of	Operation
Code	Driver	
0	either	initialize
1.	block	media (sic) check
1 2 3	block	build Bios Parameter Block
3	either	IOCTL input call
4	either	input
5	character	nondestructive input no wait
6	character	input status check
7:	character	input flush
8	either	output
9	either	output with verify
10	character	output status
11-	character	output flush
12	either	IOCTL output call
13	either	notification of file-open
14	either	notification of file-close
15	either	IOCTL query: removable disk?

Table 3

The command codes to which an MSDOS device driver must respond, omitted from IBM's DOS *Technical Reference* manuals. Codes 13, 14 and 15 are new with PCDOS 3.0.—Codes 3, 12 and 15 are passed only to drivers that indicate IOCTL support in their Device Header Attribute word.



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D&W DIGITAL, INC. 20655 Hathaway Avenue Hayward, California 94541 (415) 887-5711 tempt to test a keyboard produced a new result that you should watch out for:

• the first N keys plus a garbage key (type 4)

This is the H19 problem we reported here in August 1982; it results from a lack of blocking diodes in the key matrix. Heath hasn't learned anything since, either. On our Heath/Zenith 29, holding L, I, and then S produces "LIOS" on the screen. We have to be very precise typing the word "list" or it comes out "ligst." Worse, some combinations produce false function keys. The combination E, I, then P generates a false down-arrow (escape-B) sequence, for instance. So the H/Z29 is an N=2 machine, even though for some key combinations it will pass Davis' test as a type 1 with much higher N. Incidentally, the good combinations occur on the home row. That suggests that you might apply Davis' test on the top or bottom rows.

Charting DOS

We've been boning up on MSDOS and PCDOS lately, in preparation for writing a book and we've compiled the chart of DOS services that appears in Table 2 (page 19) as a study aid. For each possible Int 21 service it shows the request number in decimal and hex, the releases that support it, and a capsule description of the service. The services are organized into groups by function. The groups and the services within the groups are generally in ascending order by request number, but functional relationships are given precedence.

The idea is that you can look for the service you need by scanning a small group of related ones. The capsule descriptions are just enough to differentiate between similar functions. Having found the service you need, you can access its detailed description in your DOS manual through its request number.

Our study of PCDOS has been impeded no small amount by the poor quality of the IBM DOS manuals, especially those for DOS 3.0. For one

example, the documentation on service 56/38h (get or set country-dependent information) is so badly done that we honestly cannot make sense of it. If you are a reader who can write a comprehensible, accurate description of the DOS 3 version of service 38h, please do so and send it to the Clinic.

For another example, the chapter on device drivers omits any specification of the command codes to which a device driver is supposed to respond. In the DOS 2.1 manual you could still cull the essential information from the comments of a sample program. In DOS 3.0, the sample program has been dropped from the manual. The book says just enough that you can infer that command codes 3, 4, 8, 9, and 12 specify input or output, but there is no way you can tell which code calls for what action!

Fortunately, the missing information can be found in Microsoft's version of the technical manual, from which we constructed Table 3 (page 17). Although some command codes are specific to character or block devices, it seems wisest for all drivers to expect all command codes. They can treat the unwanted ones as null operations, or they can reject them with the "unknown command" error code.

DDI

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			Functional group	
19/31 −/2/3 program cntrl end, stay resident, AL=ret, DX=size program cntrl end program, AL=ret make daughter PSP at (DX:DO)				
	0/00	1/2/3	program cntrl	end program (requires PSP *CS)
18/26 1/2/3 program cntrl make daughter PSP at (DX-OD)	19/31	-/2/3	program cntrl	end, stay resident, AL=ret, DX=size
	76/4C	-/2/3	program cntrl	end program, AL=ret
	88/26	1/2/3	program cntrl	make daughter PSP at (DX:00)
	75/4B	-/2/3	program cntrl	(AL=0) load, run daughter from path *DS:DX
18/62	7/4D	-/2/3	program entrl	get AH=term code, AL=ret of daughter
18/30	5/4B	-/2/3	program cntrl	(AL=3) load DS:DX —>path as overlay
12/2A	8/62	-/-/3	program cntrl	get BX = paraddr of my PSP
12/2A	8/30	-/2/3	system info	get DOS version number
14/2C				
Saly				
33/35 -/2/3 system info get int(AL) vector to ES+BX 36/38 -/2/4 system info (AL=00) get country-dependent info 32/48 -/2/3 system info (BX=FFFF) get size available RAM to BX 39/59 -/-3 system control set int(AL) vector to (DS+DX) 37/25 1/2/3 system control set int(AL) vector to (DS+DX) 33/2B 1/2/3 system control set int(AL) vector to (DS+DX) 33/2B 1/2/3 system control set int(AL) vector to (DS+DX) 33/2B 1/2/3 system control set int(AL) vector to (DS+DX) 33/2B 1/2/3 system control set int(AL) vector to (DS+DX) 33/2B 1/2/3 system control set int(AL) vector to (DS+DX) 33/2B 1/2/3 system control set time from CH:CL:DH.DL 34/49 -/2/3 system control system control system control resize ES=paraddr to BX paras 33/49 -/2/3 system control resize ES=paraddr to BX paras 33/49 -/2/3 system control resize ES=paraddr to BX paras 34/40 1/2/3 kbd/handle 0 get key: wait, necho, nowait, nobreak 34/60 1/2/3 kbd/handle 0 get key: wait, noecho, nobreak 34/80 1/2/3 kbd/handle 0 get key: wait, noecho, break 34/80 1/2/3 kbd/handle 0 get key: wait, noecho, break 34/90 1/2/3 scrn/handle 1 test if key available (break) 34/90 1/2/3 scrn/handle 1 gut Dk. with editing and break-check 34/90 1/2/3 scrn/handle 1 gut Dk. with editing and break-check 34/90 1/2/3 aux/handle 3 get byte from COM1 34/90 1/2/3 aux/handle 3 get byte from COM1 34/90 1/2/3 disk info get AL = default drive 34/90 1/2/3 disk info get AL = default drive 34/90 1/2/3 disk info get AL = default drive 34/90 1/2/3 disk info get FAT * DS:BX (2,3: only 1st byte of FAT) and CX = bytes/Sec AL = sec/unit DX = units/disk				
22/48				
1/2 3 3 3 3 3 3 3 3 3				
39/59			4	
83/2B 1/2/3 system control set date from CX, DX 45/2D 1/2/3 system control set time from CH:CL:DH.DL 56/38 -/2/3 system control (AL=O1) set Break switch from DL.O 56/38 -/2/3 system control system control 72/48 -/2/3 system control system control 8/24/49 -/2/3 system control return RAM from ES= paraddr 7/4/4A -/2/3 system control resize ES= paraddr to BX paras 1/01 1/2/3 kbd/handle O get key: wait, echo, break-check 6/06 1/2/3 kbd/handle O get key: wait, noecho, nobreak 8/08 1/2/3 kbd/handle O get key: wait, noecho, break 10/0A 1/2/3 kbd/handle O buffered line input with editing 11/0B 1/2/3 kbd/handle O flush buffered keys, then do (AL) 2/02 1/2/3 scrn/handle 1 put DL with editing and break-check 6/06 1/2/3 scrn/handle 1 put DL noeclit, nobreak 9/09 1/2/3 scrn/				
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Sef-3/8				
2/48				
73/49				
1/01				
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6/06 1/2/3 kbd/handle 0 (DL=FF) get key: noecho, nowait, nobreak 7/07 1/2/3 kbd/handle 0 get key: wait, noecho, nobreak 8/08 1/2/3 kbd/handle 0 get key: wait, noecho, break 10/0A 1/2/3 kbd/handle 0 buffered line input with editing 11/0B 1/2/3 kbd/handle 0 flush buffered keys, then do (AL) 12/0C 1/2/3 scrn/handle 1 put DL with editing and break-check 6/06 1/2/3 scrn/handle 1 (DL <> FF) put DL, noedit, nobreak 9/09 1/2/3 scrn/handle 1 iterate DOA 2 over *DS:DX to \$ 3/03 1/2/3 aux/handle 3 get byte from COM1 4/04 1/2/3 aux/handle 3 put byte to COM1 5/05 1/2/3 disk info get AL = default drive 47/2F -/2/3 disk info get FAT *DS:BX (2,3: only 1st byte of FAT) 28/1C -/2/3 disk info . and CX = bytes/sec AL = sec/unit DX = units/disk 54/36 -/2/3 disk info BX = free units, CX/DX/AL as for 1C	1/01		khd/handle 0	get key; wait echo break-check
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54/36 — -/2/3 disk info BX=free units, CX/DX/AL as for 1C	27/1B	1/+/3	disk info	
			disk info	
84/54 —/2/3 disk info get write-verify switch to AL.0				
	84/54	-/2/3	disk info	get write-verify switch to AL.0
Table 2				Chart of DOS Services (Continued on next pa

(Continued)		
13/0D 1/2/3	disk control	reset disk system
14/0E 1/2/3	disk control	select drive, return count of drives
26/1A 1/2/3	disk control	set Disk Transfer Address = DS:DX
46/2E -/2/3	disk control	set write-verify switch from AL.0
15/0F 1/2/3	fcb control	open file from FCB
16/10 1/2/3	fcb control	close file from FCB
17/11 1/2/3	fcb control	scan current directory for first match
18/12 1/2/3	fcb control	scan current directory for next match
19/13 1/2/3	fcb control	erase files matching FCB
22/16 1/2/3	fcb control	make new file from FCB
23/17 1/2/3	fcb control	rename file from FCB
35/23 1/2/3	fcb control	get size of file to fcb
36/24 1/2/3	fcb control	set relrecno from current file position
40/28 1/2/3	fcb control	(CX = 0) trunc or stretch file to size
41/29 1/+/3	fcb control	parse filespec *DS:SI into fcb *ES:DI
20/14 1/2/3	fcb i/o	read next record to DTA
21/15 1/2/3	fcb i/o	write DTA to next record
33/21 1/2/3	fcb i/o	read record at direct address
34/22 1/2/3	fcb i/o	write record to direct address
39/27 1/2/3	fcb i/o	block read CX records to DTA
40/28 1/2/3	fcb i/o	(CX $>$ 0) block write CX records from DTA
57/39 -/2/3	directories	make directory for path *DS:DX
58/3A -/2/3	directories	erase directory at end of path *DS:DX
59/3B -/2/3	directories	set current path as path *DS:DX
71/47 = /2/3	directories	get current path of DL=drv to DS:DI
63/3F -/2/3	file i/o	read CX bytes from BX = handle to DS:DX
64/40 -/2/3	file i/o	write CX bytes to BX = handle from DS:DX
60/3C -/2/3	file control	create/trunc file (path *DS:DX, CX=attr)
91/5B -/-/3	file control	create new or fail (path *DS:DX, CX=attr)
90/5A -/-/3	file control	create unique file (path *DS:DX, CX=attr)
61/3D -/2/+	file control	open file (path *DS:DX, AL= access type)
62/3E -/2/3	file control	close file (BX=handle)
65/41 -/2/3	file control	erase file (path *DS:DX)
66/42 -/2/3	file control	seek (BX=handle, AL=method, CX+DX=offset)
67/43 -/2/3	file control	(AL=0) get CX=attr of file path *DS:DX
67/43 -/2/3	file control	(AL=1) set attr of file path *DS:DX to CX
69/45 -/2/3	file control	create a duplicate file handle
70/46 -/2/3	file control	force a handle to be a duplicate
78/4E -/2/3	file control	search 1st match to path *DS:DX
79/4F -/2/3	file control	search next match to path *DS:DX
86/56 -/2/3	file control	rename/move path *DS:DX to path *ES:DI
87/57 -/2/3	file control	(AL=0) get CX = time, DX = date of path *DS:DX
87/57 -/2/3	file control	(AL=1) set CX =time, DX =date of path *DS:DX
92/5C -/-/3	file control	(AL=0) at $CX+DX$ in $BX=hdl$, lock $SI+DI$ bytes
92/5C -/-/3	file control	(AL=1) release bytes locked by prior call

Table 2 Chart of DOS Services

helps compare, evaluate, find products. Straight answers for serious programmers.

Programmer's Referral List • Dealer's Inquire **Compare Products**

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Free Literature - Compare Products

Evaluate products Compare competitors Learn about new alternatives. One free call □ FORTH □ FORTRAN □ PASCAL □ UNIX/PC or □ Debuggers, Linkers, etc

RECENT DISCOVERIES

FASTER C - Lattice users eliminate Link step. Normal 27 seconds, Faster C in 13 secs. MSDOS

ARTIFICIAL INTELLIGENCE

EXSYS - Expert System building tool. Full RAM, Probability, Why PCDOS \$275 Intriguing, serious.

GC LISP - "COMMON LISP", Help. tutorial, co-routines, compiled functions, thorough. PCDOS \$455

IQ LISP - MACLISP & INTERLISP Full RAM. Liked. PCDOS \$155

TLC LISP - "LISP-machine"-like, all RAM, classes, turtle graphics 8087. CP/M-86, MSDOS \$235

INSIGHT 1 - Expert Sys. Dev't, PCDOS \$95 decent

PROLOG-86 - Learn fast, Standard, tutorials, samples of Natural Language, Exp. Sys. MSDOS \$125

Expert System front-ends for PROLOG: APES (\$275), ES/P (\$1895)

Other solid alternatives include: MuLISP-86 (\$189), WALTZ LISP for CPM (\$159), MicroPROLOG (\$275)

EDITORS FOR PROGRAMMING

BRIEF Programmer's Editor - undo, windows, reconfigurable, macro programs, powerful. PCDOS \$195

VEDIT - well liked, macros, buffers, CPM-80-86, MSDOS, PCDOS \$119

MACINTOSH

We evaluate, carry every available programmers product. Ask.

CLANGUAGE

INSTANT C - Interactive development - Edit, Source Debug, run. Edit to Run - 3 Secs. MSDOS \$495

"INTRODUCING C" - Interactive C to learn fast, 500 page tutorial, examples, graphics. PCDOS \$95

MEGAMAX C - native Macintosh has fast compile, tight code, K&R. toolkit, .OBJ, DisASM MAC \$275

Audio-based C tutorials. Overview \$95. Full \$295

CLIBRARIES

COMMUNICATIONS by Greenleaf (\$159) or Software horizons (\$139) includes Modem7, interrupts, etc. Source. Ask for Greenleaf demo.

C SHARP Realtime Toolkit - well supported, thorough, portable, objects, state sys. Source MANY

APPLICATION TOOLKIT by Shaw . Complete: ISAM, Screen, Overlay mgnt, report gen, Strings, String math. Source. CPM, MSDOS \$495

ROMPack - special \$Main .EXE editor, source, tech support, 8086. \$185

DEBUGGERS

PERISCOPE DEBUGGER - load after "bombs", symbolic, "Reset box" 2 Screen, own 16K. PCDOS

SOURCE PROBE by Atron for Lattice, MSC, Pascal. Windows single step, 2 screen, log file. \$395 | code

FORTRAN LANGUAGE

MacFORTRAN - full '77, '66 option, toolbox, debugger, 128K or 512K, ASM-out option MAC \$375

DR/Fortran-77 - full ANSI 77, 8087, overlay, full RAM, big arrays, complex NUMS., CPM86, MSDOS \$249

Ask about Microsoft, Supersoft, others.

OTHER LANGUAGES

ASSEMBLER - ask about FASM-86 (\$95), ED/ASM (\$100) - both are fast, compatible, or MASM (\$125), improvements.

BetterBASIC all RAM, modules, \$185 structure. BASICA - like

HS/FORTH - '79 & '83 Standards, full RAM, ASM, BIOS, interrupts, graph. multi-task, optimizer MSDOS \$250

MBP COBOL has screen control, strong doc, '74 interm., fast. MSDOS \$680

SUPPORT PRODUCTS

BASIC DEVELOPMENT SYSTEM -(BDS) for BASICA; Adds Renum, crossref, compress. PCDOS \$115 PLINK-86 for Overlays, most lang. segment control. MSDOS \$325 ProYAM Communications Package -

All a programmer'd want. TTY, VT 100, 3101, MODEM7, BBS. Remote. macros, windows MSDOS \$139

CODESMITH - visual, interactive debugger. Symbolize, modify

FORTRAN

MS FORTRAN-86 - Impr.

DR Fortran-86 - full '77' PolyFORTRAN-XREF, Xtract

\$129

RUNS ON PRICE

8086 249

PCDOS 165

MSD0S \$ 239

DUR

"C" LANGUAGE PRICE MSDOS: C86-8087, reliable call Instant C - Inter., fast, full Lattice 2.1 - improved 495 call Microsoft C 2.x 279 Williams, debugger, fast call 175 C Systems & debugger CPM80: EcoPlus C - faster, SLR BDS C - solid value 275 125 MACINTOSH: Softworks 365 Megamax-object, full

Consulair's MAC C

BASIC	RUNS ON	
Active Trace-debug	86/80	75
BASCOM-86 - MicroSoft	8086	279
BASIC Dev't System	PCDOS	115
BetterBASIC - 640K	PCDOS	185
CB-86 - DRI	CPM86	419
Prof. BASIC Compiler	PCDOS	89
Databurst - screens	MSDOS	215
SCREEN SCULPTOR	PCDOS	115
Ask about ISAM, other a	ddons for BA	SIC

Compare, evaluate, consider other Cs

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EDITORS Programming

		OUR
	RUNS ON	PRICE
BRIEF - Intuitive, flexible	PCDOS	195
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Epsilon - like EMACS	PCDOS	195
FINAL WORD-for manuals	s 86 80	215
MINCE-like EMACS	PC 80	149
PMATE-powerful	8086	185
VEDIT-full, liked	86/80	119

UNIX PC

COHERENT - for "C" users	PClike	475	
COHERENT-NCI-Realtime	PClike	call	
XENIX - plus C to MSDOS	PC	1275	
Ask about run-times, applications,	DOS com	npatibil-	
ity, other alternatives. UNIX is a trace	demark of	Bell Lab	S

LANGUAGE LIBRARIES

GRAPHICS: GraphiC-source in C	MSDOS	219
GRAPHMATIC-3D. FTN, PAS	PCDOS	125
HALO-fast, full-all lang.	PCDOS	139
FILE MGNT: BTrieve-all lang.	MSDOS	215
CIndex + -source, no royal.	86/80	369
CTree-source, no royal.	ALL	369
dBC ISAM by Lattice	8086	229
dB VISTA-"Network" Structure	MSDOS	465
PHACT-up under UNIX, addons	MSDOS	225
OTHER: CUtil by Essential	MSDOS	129
Greenleaf - 200 +	MSDOS	159
CSharp - Real-Time	MSDOS	600
PORTABLE C to PC, Mac, II	Many	125
SOFT Horizons - Blocks I	PCDOS	139
SCREEN: CURSES by Lattice	PCDOS	125
CView - input, validate	PCDOS	195
MetaWINDOW - icons, clip	PCDOS	139
PANEL - many lang, term	MSDOS	249
ProScreen - windows, source	PCDOS	415
Windows for C	MSDOS	175

OTHER PRODUCTS

Assembler & Tools - DRI	8086	159
Atron Debugger for Lattice	PCDOS	395
cEnglish - dBase to C	MSDOS	750
C Helper: DIFF, xref, more	86/80	135
CODESMITH-86 - debug	PCDOS	129
MacASM-full, fast, tools	MAC	115
MBP Cobol-86 - fast	8086	680
Modula 2 for MAC.	PCDOS	90
Micro: SubMATH-FORTRAN full	86:80	250
Microsoft MASM-86	MSDOS	125
MSD Debugger	PCDOS	119
Multilink - Multitasking	PCDOS	265
PC FORTH + -well liked	MSDOS	219
PFIX-86 Debugger	MSDOS	169
PL 1-86	8086	495
Polylibrarian - thorough	MSDOS	95
PolyMAKE	PCDOS	95
PROFILER by DWB - flexible	MSDOS	109
Prolog-86-Learn, Experiment	MSDOS	125
SLK F - Copy Protection	PCDOS	145
SYMD debugger-symbols	PCD0S	119
TRACE86 debugger ASM	MSDOS	115

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REALIZABLE FANTASIES

Dr. Dobb is a Subversive

by D. E. Cortesi

The publishers of DDJ recently threw a party to celebrate the 100th issue of the magazine. It was a friendly affair, with lots of sincere speeches about how important DDJ has been and about how it still has a great role to play. There was a lot of talk about "hackers:" what they accomplished; where DDJ stood in relation to them; where they might have gone now.

they were expressed, the sentiments seemed incomplete to me. Something was being overlooked, but I couldn't figure out what it was until I was halfway home. Then I lay awake working out the speech that ought to have been given, but wasn't. And this is it....

We've heard a lot about "hackers" in the last few minutes. I would like to protest the use of that word. I understand why it's being used. It's because nobody can think of the right word to describe the people who founded DDJ and the people who read and profited from it in its earliest days. We know they weren't part of the establishment; we know they didn't play a conventional role in the society of their day. We have, perhaps, an uneasy feeling that we aren't quite like them and that they wouldn't find us very interestingeven though perhaps we were them just a few years ago.

So we scratch around in our vocabularies, searching for the right word to describe these departed people, and the best we can come up with is "hacker." Well, it's the wrong word.

The right word is ... revolutionary. Subversive. Bomb-throwing anarchist.

To me, the hacker state is a frame of mind in which one is turned inward on oneself and the machine: one's spiritual third eye rolls up into the head. The people who birthed *DDJ* and personal computing in general were quite the opposite. Their eyes were turned outward on society—and twinkled with mischief.

They were bent, quite consciously, on overthrowing the established order. It was to be a nonviolent overthrow, one conducted if possible with whimsy and good humor, but it was to be an overthrow, a toppling, nonetheless.

The central tenet of this jolly revolution was (and still is) the belief that if we put enough computers in the hands of enough people, wonderful changes will take place. What the wonderful changes are . . . well, that varies with the year and the person you're talking to. But that they will be wonderful, as well as radical in the deepest and best sense of the word, no computer subversive doubts. I certainly don't.

The founder of the movement and its only theorist, its Marx or Che Guevara if you like, is Ted Nelson. His book, Computer Lib, was written in reaction to computers, their employers, and their professional priesthood as those things existed in the early 70s. It was a protest: "Either computer systems are going to go on inconveniencing our lives, or they are going to be turned around to make life better," and then, in caps, a battle cry: "COMPUTER POWER TO THE PEOPLE! DOWN WITH CYBERCRUD!"

And to make sure we didn't miss the point, there was the symbol of the clenched fist on the cover. I hope I'm not the only one here who remembers when the word *Lib* and the upraised fist were potent, dangerous symbols. That Nelson borrowed both for his manifesto indicates how serious he was.

I don't know if the People's Com-

puter Company admitted to subversive intentions in public or not. Unfortunately for me, I was out of the country during its glory days. I was aware of micros and an early subscriber to *DDJ*, but I was only a fellow traveler to the revolution, not a revolutionary myself.

But whether the intentions were admitted or not, I'm positive that they were there. No one who's spent any time with Bob Albrecht, or with Ramon Zamorra, who founded ComputerTown USA!, could have any doubt that these people are motivated by a deep love of mischief. Nothing pleases them better than a well-toppled apple cart or a well-rocked boat.

Another revolution that I missed out on was the birth of the acid culture in the early 60s. But I suspect that the spirit of People's Computer Company had a lot in common with the spirit of Ken Kesey's Merry Pranksters, and with that of the Diggers and the various communards of the 60s. They all felt that they'd stolen the fire of the gods and that they'd damn well better set as many blazes as they could before the gods came to take it back.

After all, what could possibly be more subversive than training children to operate the tools of the establishment, without at the same time inculcating the establishment's line of thinking? But that was what Albrecht, Allison, et al., were doing. I don't know if this is what was specifically in their minds or not. But consider. You have all had the following experience. You program something in BASIC; you find that the program doesn't work because of the inevitable bugs; then, after you pick them off, you find that it still doesn't work because the limited accuracy of binary floating point has turned your numerical results into oatmeal. Nobody who has been through that can ever look at their bank statement in quite the same way again. Or their phone bill. Or, if there is a live connection between one side of their brain and the other, can they ever feel quite the same about a proposal for an antiballistic missile system as they might once have done before their personal encounter with computers in the raw.

Albrecht and company used timesharing terminals to start indoctrinating children and susceptible adults, but then the micro came along. The earliest micros were pitiful, but to the eyes of a computer subversive they appeared to be the sword of liberation. A computer you could hold in your hand!

For a parallel, try to imagine that Gutenberg had never invented his printing press; that the only way for books to be reproduced was for them to be handwritten by scribes. Of course, only the wealthy and the government can afford to pay scribes and buy vellum, so the only thoughts that circulate in book form are the ones acceptable to the powers that be. Then somebody invents the typewriter: a cheaper, faster way of being a scribe, one that anybody can learn. So you start a counterculture book factory, teaching people to use typewriters to duplicate unapproved liter-

And then somebody walks in the door with the latest invention, the Xerox copier. And just that simply, your revolution has been won; now it's just a matter of mopping up.

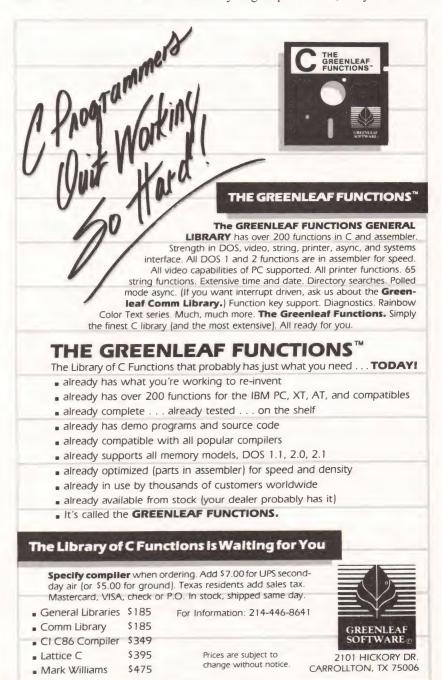
In the case of the computer revolution, the mopping up is still going on. The micro has, in fact, hurt the computer bureaucrats, the people who would try to make you swallow an inconvenience on the grounds that the computer wouldn't let them be helpful. These days they find it a lot harder to say "the computer won't let me" because so many clients are likely to answer "Why not? My computer would." There really is a lot less these days of what Nelson called cybercrud, and we can credit that to the wide popular exposure to micros.

But there is a lot more to be done. We've spread computers all over the place and still have no millenium, nor anything remotely like it. There's been no great liberation of minds or spirits. The revolution has been assimilated, just as the automotive, sexual, drug, and free speech revolutions were assimilated before it, and the feminist revolution is being assimilated now: they come, they pass, things are different but not very much so. Surely by now the wonderful thing should have happened. Surely by now somebody should have written the program that will set us all free? But no.

One reason for this failure is that

the computer did not turn out to be a sword that could be placed in anyone's hand. It isn't a sword at all, nor anything like a simple edged or pointed weapon. For complexity it's more like a whole battalion of soldiers. If you tell some poor shmuck to start wielding one, you put him in the position of a private promoted instantly to general and asked to take over management of a battle.

To change metaphors nimbly in midstream, we have made the belated discovery that the computer priesthood that we deposed was not entirely a group of fakes; they had more



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than mumbo jumbo going for them. True, they'd made public access to the machines difficult, or allowed it to be difficult. But now that we have our own machines, we find out that there were good reasons for that. It turns out that quite possibly the most difficult thing you can do with a computer is to make it be truly helpful. If you are in a hurry for an answer, you will find it a lot easier to program the machine to take its input in rigid, unforgiving formats, and to give cryptic, insulting error messages, than to

make it flexible and friendly and forgiving.

In fact, we find that in putting on its happy face, the machine uses up so much capacity that it has precious little left in which to do any useful work.

Partly this is a real effect. It truly is difficult to make a computer be helpful, and there are deep, unsolved problems in the way of making that happen. But in part, I think that the revolutionaries were a bit hasty in exiling those priests. They have a genuine science, computer science, that

deals with the elegant and efficient application of machine power. All too little of that science is being applied today. The result is that we have machines with 512K of storage that can't keep up with the demands of a single user. Damn it, we used to support twenty APL users on a single 360 model 40 with 128K; we considered a 370/158 with 512K a big machine, suitable for use by a middle-sized company with half a dozen programmers online to TSO or VM/370. The big macro assembler for the IBM 370 needs a 128K partition to run in. That's the assembler program plus all the tables and work areas and buffers it uses. The macro assembler for the IBM PC can barely load itself into 128K, and it can't assemble anything of useful size until you give it 256K. In short, there has been a major loss of efficiency in moving from the software that the priests built for the corporate mainframes to the software that we are putting on personal computers today.

As a good revolutionary, I must believe that computer *use* is for everybody, and I really do. But programming the computers so they can *be used* is almost certainly a job for specialists.

The question that confronts us now is: whose specialists are they going to be? Will they be the tools of the new computing establishment, working for the fiscal advantage of a company like IBM or AT&T or Microsoft, or for the political advantage of a nation like Japan or France?

Or will they be grass-roots benefactors like so many who supplied the software we have now-and who often supplied it through the pages of DDJ? I would like it to be the latter. In fact, I propose to you that what DDJ has always done is to support and train and encourage our grass-roots systems programmers. And that is still the best, and most truly radical, thing that it can do in the future. I'm delighted to see that its editors agree with me. Proof that they do is in their publishing Richard Stallman's "GNU Manifesto" (DDJ #101, March 1985), an outrageous, revolutionary challenge quite in the old vein.

My own best writing for DDJ has



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been inspired by a species of the Robin Hood ethic: a desire to steal ideas from the well-funded and put them into the heads of the unfunded. I enjoyed writing my recent article on Prolog for just that reason. Prolog and the ideas behind it had been the property of a few academics, and these ideas were rapidly being co-opted by the Japanese for their Fifth Generation. But DDJ gave me a chance to snatch those same notions and spread them around everywhere, making them accessible to everybody who can grasp them. Of course the real revolutionaries are the people in London who publish Micro-PROLOG and the ones in this country who are selling a version of DEC-10 Prolog for the IBM PC. The ideas wouldn't be much use without the cheap software that implements them, and that wouldn't be of any use without the cheap micros to run it on.

So these processes all feed back on themselves and get richer and deeper. But the continuation of this revolution, at least its continuation as a revolution and not as a captive toy of the corporations and the advertisers, depends on getting ever more sophisticated ideas and tools into the hands of creative people, so that they can keep putting ever more sophisticated programs back into ours.

Notice how the ante goes up on every round. The level of sophistication is escalated every time through the loop. The Mac's internal toolkit, and its competitive followers Windows and GEM, are vastly harder to use well than the old CP/M BDOS functions. Graphics, AI, and speech recognition, in their different ways, require the exercise of all the algorithmic tools of computer science. And it takes very abstruse, very mathematical tools to analyze these massive, sophisticated programs and prevent them from overflowing the capacities of personal hardware, which really are quite limited. The experience of mainframers has been that your hardware is always an order of magnitude too small for the software you'd like to run. There's no indication that this law has been repealed for micros. We will be squeezing quarts into pint bottles forever,

and it takes sound theoretical insight to do that well.

I should add that the rule that the capacity of the hardware always falls short of the needs of the software just might be repealed by highly parallel, multiple-micro systems. Two things about such systems: first, they are right on the fringe of computer science and the academics haven't the foggiest idea how to build good software for them; and second, they are ideally suited for hobbyist-level, garage experiments.

But this is why the face and contents of *DDJ* have been changing. Whether the editors and contributors knew it or not, they've been trying to prepare themselves to discuss and understand computers at the more sophisticated level permitted by the second generation of micros. But there's been no change in who is doing the discussing, nor in their essentially subversive motive, which is to snatch the tools of the establishment and apply them in the public domain.

It's that mischievous, subversive motive that has kept me writing for *DDJ*. That, and the belief I still hold that, somewhere out there, fingers on the keyboard of an Apple or Osborne, is a fifteen-year-old kid who is capable of writing the program that will turn the world upside down. The proper role of *DDJ* is to make sure that that kid has free, unfettered access to the ideas and the software tools that she needs in order to write it.

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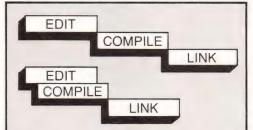
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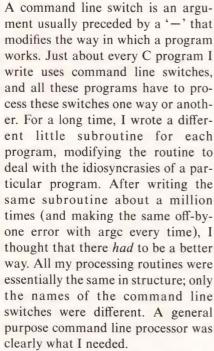
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Command Line Processing

by Allen Holub



Looking around in the literature for something that did the job, I found the "Argum" package, which appeared in Anthony Skjellum's C/Unix column in DDJ (#70, August 1982). The program was well written and did what I needed, but it had several problems. First of all, it did a lot more than I needed and the extra functions added extra code. Because this package was going to be included in every program I wrote, a reduction in scope seemed to be a good idea. Another, related. problem was the internal tables used by Argum. They were created at runtime (as compared to compile time). This added both extra code and extra execution time to any program that used Argum. Finally, Argum had no convenient way to deal with errors on the command line. In view of these problems, I decided to write my own processor, a description of which follows. This is by far the most frequently used subroutine in my standard library.

Getargs()

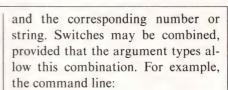
The command line processor is actually a package of subroutines. Access to these routines is through a single procedure, called getargs(). The routines are table driven. In every program you have to declare a table in which the various command line switches are described. Getargs() removes the switches from the command line as it works. This means that position-sensitive arguments (e.g., a switch that applies to all files that follow it on the command line, but not to any files that precede it) are not supported. I don't use this sort of switch very often, and I got tired of having to skip past arguments that had already been processed to get to a filename. The arguments are evaluated left to right, so interaction between PROC type switches (see below) is possible.

When getargs() finds an error on the command line, it prints to stderr a list of all legal switches, along with a brief description of what those switches do and their default values. After it prints the error message, getargs() terminates the program with an exit(1) call. This closes any open files and returns to the operating system.

Command Line Switch Formats

Command line switches all must take the form:

That is, all arguments start with a minus sign; each switch is identified by a single letter that follows the minus sign immediately (no intervening spaces); the letter may be followed by an optional number or string, again with no spaces between the character that serves as the switch identifier



can also be written as:

However, if getargs() expects a string to follow the switch identifier character, then it assumes that the rest of the argument is part of the string. You have to be careful when combining string type command line switches with other types.

Using Getargs()

To use getargs() you must do two things: (1) set up a table to tell the routine what kind of command line switches to expect; (2) call the routine itself somewhere early in the main() module. The file getargs.h (Listing One, page 32) contains the #defines and typedefs needed to create the command switch descriptor table. This table is an array of structures:

```
typedef struct
{
  unsigned arg:7 ;
  unsigned type:4 ;
  int *variable ;
  char *errmsg ;
}
ARG;
```

The arg field is a single character that identifies the switch on the command line. In the following descriptions this character is represented as <switch>. The type field may take any one of five values, all of which are

#defined in getargs.h. The behavior of getargs() will vary according to the value.

INTEGER switches take the form

-<switch> < number>

Numbers preceded by 0x are hex, by 0 without the x, octal. All others are decimal. The number is terminated by any character not legal in the indicated radix. Any characters that follow are assumed to be additional switches. The int sized variable pointed to by the variable field of the ARG structure will be set to the value of the <number>. If the <number> isn't typed on the command line, *variable will be set to 0.

BOOLEAN switches will cause some action based on the presence or absence of the indicated switch on the command line. If the switch is present, then the int pointed to by the variable field is set to 1, otherwise *variable is not modified.

CHARACTER switches take the form

-<switch> <character>

When the switch is found on the command line, then *variable is set to the character immediately following the <switch> character.

STRING switches take the form

-<switch> <string>

When the switch is found on the command line, the character pointer pointed to by the variable field is set to point at a string consisting of all characters following (but not including) the <switch> character up to the end of the current argument (not to the end of the command line). In the case of combined switches in a single argument, the STRING argument must be the last one because all following characters will be considered part of the <string>. When defining a STRING switch in the table, be sure to cast the variable into an integer pointer. See Listing Three (page 38), line 15, for an example.

PROC switches take the form

-<switch> <anything>

This works like the STRING switch in that all characters following <switch> up to the end of the current argument will be part of the <anything>. However, the variable field is a pointer to a subroutine that is called indirectly as soon as the switch is encountered on the command line. A pointer to <anything> is passed to this subroutine as a single argument. An example of such a subroutine is given in Listing Three, line 19. It is the responsibility of the called subroutine to parse <any-

thing> as appropriate.

The errmsg field is used to print an error message if an undefined switch is found. An example of the format is shown in the Figure (page 30). This message was generated when the command line "argtest -x" was given to argtest.

Listing Three (if you haven't realized it by now) is an example of how to use getargs(). Lines 5-9 are declarations of the objects that are used in the variable fields of the switch descriptor table. The initial values of

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these variables remain unchanged if the switch is not encountered on the command line at runtime. The table itself, Argtab, is declared on lines 10-17. The main() routine calls getargs() on line 35. The rest of main() just prints the command line, both before and after the getargs() call, so you can see how getargs() strips processed switches out of argv.

Getargs() itself starts on line 98 of Listing Two (page 32). It is passed four arguments: argv, argc, a pointer to the switch descriptor table (tabp). and the size of this table in elements (not bytes). The routine returns a new value of argc, and argv is compressed, i.e., all entries containing command line switches are removed from it. The for loop on line 112 processes argy one element at a time. Nargy points into argy and strips processed switches. Nargy is initialized to point at argv[1] because argv[0] can't possibly contain a command line switch (it holds the program name). If no leading minus sign is found in the argument, *argv is copied to *nargy and both pointers are advanced. In addition, narge (new argc) is incremented. If the argument does begin with a minus sign, it is processed as a command line switch. In this case *argv is not copied to *nargy, and argy, but not nargy, is advanced. This effectively eliminates the argument containing the switch from the argy array.

Findarg() (Listing Two, line 44) is used by getargs() to see if an argument is in the table. It performs a linear search. Because the table is usually fairly small, it seemed as if the extra code needed for a binary search wasn't justified. Findarg() takes three arguments: c is the switch identifier character being searched for; tabp and tabsize are the same parameters as were passed to getargs().

Setarg() (Listing Two, line 9) is called when a command line switch is found in the table. Argp is a pointer into the table, as returned from findarg(). Linep is a pointer into the argy entry that is being processed. Casts were used to make this routine as transportable as possible.

Pr_usage() (Listing Two, line 57) is used to print the error message shown in the Figure when an illegal command line switch is found. It just spins through the table, printing the current contents of the object pointed to by the variable field as well as the message given in the errmsg field.

The final routine in the package is stoi() (for string to integer, Listing Four, page 38), which is used to process INTEGER switches. Stoi() is a fancy version of the standard library routine atoi() (described on page 58 of Kernighan & Ritchie). Stoi() can handle numbers in base 8, 10, or 16. If code size is a real issue, you may want to remove the code that processes octal numbers (Listing Four, lines 45-53). Another major difference between stoi() and atoi() is that stoi() is passed a pointer to a character pointer. That is, it is passed the address of the pointer, which in turn points into the string to be processed. This extra level of indirection lets you modify the character pointer itself to point past the end of the number being processed. If you use atoi(), you have to go through the string twice, once to extract the number and once

Illegal argument <x>. Legal arguments are:

-bboolean argument -c < c >

character argument -n < num >integer argument

-s < str >string argument procedure argument -p < str >

(value is FALSE)

(value is .) (value is 0)

(value is <doo wha>)

Figure Error Output from Program in Listing Three

Let Us Mind Your Q's and T's

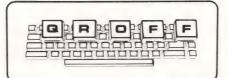
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more to skip past the characters that represent that number.

Conclusion

Several additions could be made to getargs(), at the cost of an increase in complexity and code size.

- A common command line function is to open or close a file in a particular mode and to abort if the file can't be opened. At present I'm doing this with a PROC type command line switch, but you could add another argument type, whose variable field points to a FILE pointer. Alternately, variable could point to a structure that included
- the FILE pointer, an open mode, a pointer to an error processing routine, and a default file name.
- Another nice feature would be to mark an argument if its presence is required on the command line. You could accomplish this by adding additional types (i.e., REQ _INTEGER), or by adding another field to the ARG structure. An error message would be printed if the argument wasn't found on the command line.
- Getargs() depends on initializers to set default argument values. If your compiler doesn't support initializers, you can add a default value field to the ARG structure.
- by using stoi() to process numeric arguments. If you expect only decimal numbers, you may want to replace stoi() with atoi(). However, stoi() is a useful routine in its own right.

I'm sure that you can think of other bells and whistles. I've been using getargs() for a of couple years now and am satisfied with it in its existing state. Defining the way the command line looks and then getting switches from it is now a painless process. DDI

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C Chest (Text begins on page 28) Listing One

```
1:
              Getargs.h
                               Typedefs and defines needed for getargs
 2:
3:
     #define INTEGER
                               0
     #define BOOLEAN
 4:
                               1
 5:
     #define CHARACTER
                               2
 6:
     #define STRING
                               3
     #define PROC
 7:
8:
    typedef struct
9:
10:
                               arg : 7
              unsigned
                                                 /* Command line switch
                                          ;
                               type: 4
11:
              unsigned
                                                 /* variable type
                                                 /* pointer to variable
12:
              int
                               *variable :
13:
              char
                               *errmsg
                                                   pointer to error message
14:
     ARG;
15:
                                                                                          End Listing One
```

Listing Two

```
1:
     /*
              GETARGS.C
                               Command line argument processor for C programs
      *
 2:
 3:
                 (C) Copyright 1985, Allen I. Holub. All rights reserved.
 4:
                This program may be copied for personal, non-profit use only.
 5:
 6:
     #include <stdio.h>
     #include <getargs.h>
 8:
     typedef int
                      (*PFI)();
 9:
     static char
                      *setarg( argp, linep )
10:
     ARG
                      *argp;
                      *linep;
11:
     char
12:
13:
              /*
                      Set an argument. argp points at the argument table entry
14:
               *
                      corresponding to *linep. Return linep, updated to point
15:
                      past the argument being set.
16:
               */
17:
             ++linep;
18:
             switch( argp->type )
```

```
19:
20:
             case INTEGER:
21:
                     *argp->variable = stoi( &linep );
22:
                     break:
23:
             case BOOLEAN:
24:
                     *argp->variable = 1;
25:
                     break:
26:
             case CHARACTER:
27:
                     *argp->variable = *linep++ ;
28:
                     break;
29:
             case STRING:
30:
                     *(char **)argp->variable = linep;
                     linep = "";
31:
32:
                     break;
33:
             case PROC:
                     (* (PFI)(argp->variable) )( linep ); linep = "";
34:
35:
36:
                     break:
37:
             default:
38:
                     fprintf(stderr, "INTERNAL ERROR: BAD ARGUMENT TYPE\n"):
39:
                     break:
40:
41:
             return( linep );
42:
43:
     /*_____
44:
                     *findarg( c, tabp, tabsize )
     static ARG
45:
    int
                     c, tabsize;
46:
    ARG
                     *tabp;
47:
             /*
48:
                     Return pointer to argument table entry corresponding
49:
                     to c (or 0 if c isn't in table).
              */
50:
51:
             for(; --tabsize >= 0 ; tabp++ )
52:
                     if( tabp->arg == c )
53:
54:
                             return tabp;
55:
             return 0;
56:
     }
57:
     static pr_usage( tabp, tabsize )
58:
     ARG
             *tabp;
59:
     int
             tabsize;
60:
                      Print the argtab in the form:
61:
62:
                              -<arg> <errmsg>
                                                  (value is <*variable>)
              */
63:
64:
             for(; --tabsize >= 0 ; tabp++ )
65:
66:
                      switch( tabp->type )
67:
68:
                      case INTEGER:
69:
                              fprintf(stderr, "-%c<num> %-40s (value is ",
70:
                                                       tabp->arg, tabp->errmsg);
71:
                              fprintf(stderr, "%-5d)\n", *(tabp->variable) );
72:
                              break;
73:
                     case BOOLEAN:
74:
                              fprintf(stderr,"-%c
                                                       %-40s (value is ".
75:
                                                       tabp->arg, tabp->errmsg);
                              76:
77:
78:
                              break:
79:
                     case CHARACTER:
80:
                              fprintf(stderr, "-%c<c>
                                                       %-40s (value is ",
81:
                                                      tabp->arg, tabp->errmsg);
82:
                              fprintf(stderr, "%-5c)\n", *(tabp->variable) );
83:
                              break:
84:
                      case STRING:
85:
                              fprintf(stderr, "-%c<str> %-40s (value is ",
86:
                                                       tabp->arg, tabp->errmsg);
                                                                               (Continued on page 36)
```

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C Chest (Listing Continued, text begins on page 28)

Listing Two

```
87:
88:
                               break;
89:
90:
                      case PROC:
                               fprintf(stderr, "-%c(str> %-40s\n",
91:
                                                         tabp->arg, tabp->errmsg);
92:
93:
94:
                      }
95:
96:
      #define ERRMSG "Illegal argument <%c>. Legal arguments are:\n\n"
97:
              getargs(argc, argv, tabp, tabsize)
98:
99:
              argc, tabsize;
      int
               **argv ;
100:
     char
              *tabp
101:
     ARG
102:
      {
               /* Process command line arguments. Stripping all command line
103:
                * switches out of argv. Return a new argc. If an error is found * exit(1) is called (getargs won't return) and a usage message
104:
105:
                * is printed showing all arguments in the table.
106:
                */
107:
108:
               register int
                                nargc
109:
               register char
                                **nargv, *p;
                                *argp
110:
               register ARG
               nargc = 1;
111:
               for(nargv = ++argv ; --argc > 0 ; argv++ )
112:
113:
                       if( **argv != '-' )
114:
115:
                                *nargv++ = *argv;
116:
117:
                                nargc++;
118:
                       else
119:
120:
                                p = (*argv) + 1;
121:
122:
                                while( *p )
123:
                                         if(argp = findarg(*p, tabp, tabsize))
124:
                                                 p = setarg( argp, p );
125:
                                         else
126:
127:
                                                 fprintf(stderr, ERRMSG, *p );
128:
                                                 pr_usage( tabp, tabsize );
129:
                                                 exit( 1 );
130:
                                         }
131:
132:
133:
134:
135:
               return nargc ;
                                                                                        End Listing Two
136:
```

Listing Three

```
Test program for getargs.
                ARGTEST.C
 1:
 2:
    #include <stdio.h>
#include "getargs.h"
 3:
 4:
                boolarg = 0;
chararg = '.';
 5:
      int
                                                         /* Variables used by argtab */
 6:
      int
                intarg = 0;
*strarg = "doo wha";
 7:
      int
 8:
 9:
      extern
                proc();
10:
      ARG
                Argtab[]=
```

(Continued on page 38)

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C Chest (Listing Continued, text begins on page 28)

Listing Three

```
11:
     {
               { 'b',
{ 'c',
{ 'n',
{ 's',
12:
                                                       "boolean argument"
                        BOOLEAN,
                                    &boolarg.
                        CHARACTER, &chararg,
                                                       "character argument"
13:
                                                       "integer argument"
                        INTEGER,
                                    &intarg,
                                                                                         },
14:
                                     dintarg, (int *)&strarg, "string argument"
/:= *\&nroc. "procedure argument"
15:
                        STRING,
                 's',
16:
                        PROC,
17:
     };
18:
     #define TABSIZE ( sizeof(Argtab) / sizeof(ARG) )
19:
     proc( str )
20:
     char
               *str;
21:
               printf("Inside procedure called by -p command line switch, ");
22:
23:
               printf("string = \langle %s \rangle \ n", str );
24:
     }
25:
     main(argc, argv)
26:
     int
               argc;
27:
               **argv;
     char
28:
29:
               register int
                                 i:
               printf("Argc == %d. ", argc);
30:
               printf("Cmd line: argtest");
31:
               for( i = 1 ; i < argc ; printf("%s ", argv[i++]) )
32:
33:
               printf("\n"):
34:
35:
               argc = getargs( argc, argv, Argtab, TABSIZE);
               printf("Argc == %d. ", argc);
36:
               printf("Cmd line: argtest");
37:
               for( i = 1 ; i < argc ; printf("%s ", argv[i++]) )
38:
39:
               printf("\n");
40:
                                                                                             End Listing Three
41: }
```

Listing Four

```
1:
     /* STOI.C
                      More powerful version of atoi.
2:
 3:
              Copyright (C) 1985 by Allen Holub. All rights reserved.
 4:
              This program may be copied for personal, non-profit use only.
 5:
                                ( 'a' <= (c) && (c) <= 'z' )
 6:
     #define islower(c)
                                ( islower(c) ? (c) - ('a' - 'A') : (c) )
 7:
     #define toupper(c)
8:
                       stoi(instr)
                       **instr;
9:
     register char
10:
                       Convert string to integer. If string starts with 0x it is interpreted as a hex number, else if it starts with a 0 it
11:
12:
13:
                       is octal, else it is decimal. Conversion stops on encounteria
14:
                       the first character which is not a digit in the indicated
15:
                       radix. *instr is updated to point past the end of the number
16:
17:
              register int
                                num = 0;
18:
              register char
                                *str
19:
              int
                                sign = 1;
20:
              str = *instr:
21:
              while(*str == ' ' |  *str == '\t' |  *str == '\n' )
22:
                       str++ ;
23:
              if( *str == '-' )
24:
25:
                       sign = -1;
26:
                       str++;
```

```
27:
28:
             if(*str == '0')
29:
30:
                     ++str;
31:
                     if (*str == 'x'
                                     *str == 'X')
32:
33:
                             str++:
                                     ('0'<= *str && *str <= '9')
34:
                             while(
                                     ('a'<= *str && *str <= 'f')
35:
                                     ('A'<= *str && *str <= 'F')
36:
37:
                                    38:
39:
40:
                                             toupper(*str) - 'A' + 10
41:
42:
                                     str++;
43:
44:
45:
                     else
46:
47:
                             while( '0' <= *str
                                                && *str <= '7' )
48:
49:
                                     num *= 8:
50:
                                     num += *str++ - '0';
51:
52:
53:
54:
             else
55:
56:
                     while( '0' <= *str &&
                                            *str <= '9' )
57:
58:
                             num *= 10;
59:
                             num += *str++ - '0';
60:
61:
62:
             *instr = str;
63:
             return( num * sign );
64:
```

End Listings

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Using Decision Variables in Graphics Primitives

by Tom Hogan

"Graphics algorithms" is not a listing you'll find in the Collected Algorithms of the ACM, but graphics programming presents unique problems to the software developer.

Picture a vast subterranean cavern packed with robed, grey-bearded men. A small platform stands at one end. Billows of purple smoke appear and out of the mist steps a tall man with white hair. He speaks and his voice fills the cavern.

"Welcome, brethren. Tonight we shall consider how to guarde our Realme of Graphix from the uninitiated who would learn our Mysteries and divulge them. We will speake of Master Bresenham, who revealed to us the Secrete of ye Mystik Circle."

Does the idea of graphics wizards jealously guarding their secrets seem strange? I can tell you that it didn't seem so to me when I searched the literature for a routine that would map ellipses and couldn't find one.

Light in the Tunnel

Thanks to a tip, I learned about Fundamentals of Interactive Computer Graphics by James D. Foley and Andries Van Dam (Addison Wesley, 1982). It contains an algorithm for plotting a circle using a decision variable (based on Bresenham's algorithm). I used the decision variable method (hereafter referred to as the DV method) to derive the algorithm for plotting ellipses that is found in Listing One (page 45).

The DV Method Generalized

Although the DV method was used only to plot an ellipse, it can be used to plot other conic sections as well. In fact, the DV method can be used to generate any well-behaved curve that can be expressed as G(x, y) = 0.

The DV Method Compared

Speed is important in graphics applications. The DV method is fast because it adds, shifts, subtracts, and multiplies integers, taking much less time than would be required by floating-point calculations.

An obvious method for plotting an ellipse is to calculate each point independently by incrementing x in unit steps and calculating y using square roots. This method is slow and generates a curve that is nonuniform when the slope of its tangent line falls below -1 (see Figure 1, page 41).

In contrast to the preceding method, the DV method does not calculate points independently. Instead, each succeeding point is mapped with reference to the point previously mapped. Furthermore, this method guarantees that succeeding points are adjacent. Therefore, the generated curve is more uniform than the curve generated by calculating points independently (see Figure 2, page 41).

Reduce the Number of Sample Points

Eight adjacent points surround any previously mapped point. In order to use the DV method to map the curve, you must reduce the number of sample points from eight to two. The sign of the decision variable can only be posi-

Tom Hogan, C Source, 12801 Frost Road, Kansas City, MO 64138.

tive or negative. The two possibilities correspond to the two sample points.

Take the case of an ellipse mapped only in the first quadrant. Points 1, 2, 3, 4, and 6 (see Figure 3, page 42) can be discarded immediately when mapping from Point A to C (see Figure 4, page 42), because x never decreases and y never increases as the ellipse is mapped in that direction. Points 1 through 3 would require y to increase, while points 1, 4, and 6 would require x to decrease.

This reduces the choice to three points: 5, 7, and 8. Which two are chosen as the set of sample points depends on the slopes of the line segments joining them and the last point plotted: the slope of the tangent line must fall within the range of the slopes of the two line segments for the region plotted. The slopes of the line segments of any two points must approach the slope of the tangent line as closely as possible. Thus, a set comprising Points 5 and 7 would be excluded because it would violate this requirement in the region close to B in Figure 4. There remain the possible combinations 5 and 8 and 7 and 8. Points 5 and 8, corresponding to Points S and T in Figure 5 (page 42), can be said to comprise set A. Points 7 and 8, corresponding to Points T and U in Figure 5, can be said to comprise set B.

Changing Sets of Sample Points

From Point A to B (Figure 4), set A is used because the slope of the tangent falls within the range delimited by the slopes of PS and PT (Figure 5). The decision variable is initialized to its value at Point A (see Equation 9 or 10 in the Table on page 44). The quantities to be added to it are based on the sample points in set A (Equations 6 and 8).

When the slope of the tangent line reaches -1 (at Point B in Figure 4), set B is used (see Figure 6, page 42). The value of the decision variable is recalculated (Equation 11) at Point B for set B. The quantities that are added to the decision variable are now based on set B (Equations 13 and 15).

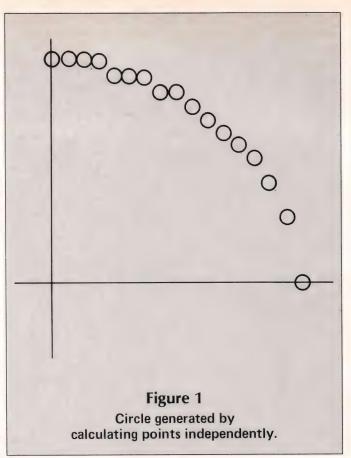
Expansion on General Applications

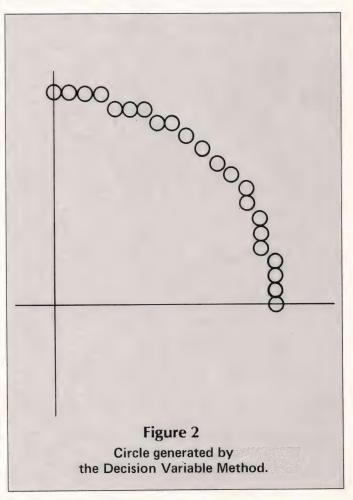
Please note a key feature of the DV method: the choice of the sample points depends on the slope of the tangent line and the direction of its change. A curve can be broken up into sections for plotting, based on the range of the slope of the tangent line in each section. The method is useful for a broad spectrum of curves.

Example: The Ellipse

The initial value of y depends on the position of the major axis. If it is vertical, y is R_m , half the length of the major axis. If horizontal, y equals R_m multiplied by the aspect ratio. The positions of the major and minor axes depend on the aspect ratio. If it is less than one, then the major axis is horizontal. If greater than one, it is vertical.

Long variables were used in this routine instead of integers or doubles in order to provide the widest range of values for the aspect ratio and R_m while retaining reasonable plotting speed. Integers limited the ellipse's size too much, while doubles were too slow.





The lower limit for the aspect ratio is 0.004 and R_m must not exceed 154. Otherwise, the values of some of the long variables will overflow.

This algorithm takes advantage of the symmetry of an ellipse. The ellipse is symmetric about the x and y axes, and about its center. Therefore, an ellipse need only be mapped in the first quadrant. The other three quadrants

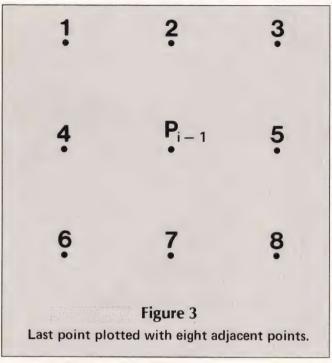
are easily plotted.

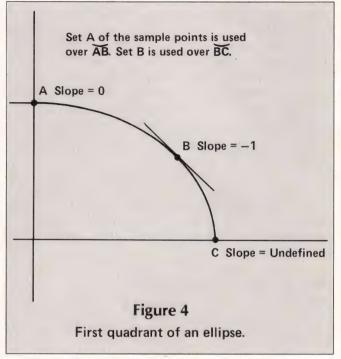
Clipping is done by the routine in Listing Two (page 48). The algorithm assumes medium resolution.

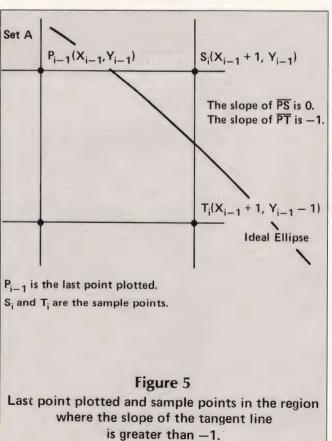
(Listings begin on page 45)

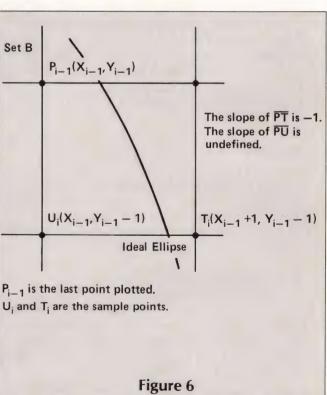
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Last point plotted and sample points in the region

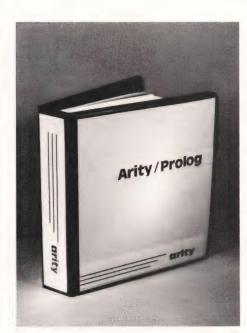
where the slope of the tangent line is less than

or equal to -1.

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The well known equation of an ellipse is:

$$\alpha Y^2 + \beta X^2 - \alpha \beta = 0.$$
 Eq. 1.

Therefore define an error term D(P_i) such that at any sample point P_i along the curve:

$$D(P_i) = \alpha Y^2 + \beta X^2 - \alpha \beta.$$
 Eq. 2

Define two points S_i and T_i relative to P_{i-1} such that P_i will be selected from one of these two points (see Figure 5). Define a decision variable d_i such that:

$$d_i = D(S_i) + D(T_i).$$
 Eq. 3

Notice that if the curve passes midway between S_i and T_i , $d_i = 0$. Further notice that:

- 1) if the curve passes above the midpoint, $d_i < 0$, and
- 2) if the curve passes below the midpoint, $d_i > 0$.

Therefore:

1) if
$$d_i < 0$$
, we choose S_i , and

2) if
$$d_i > = 0$$
, we choose T_i .

The quantity to add to d_i, depending on whether S_i or T_i is chosen, must be determined. The value of d_i for S_i and T_i must first be determined:

$$\begin{split} d_i &= D(S_i) + D(T_i), \\ d_i &= \alpha Y_{i-1}^2 + \beta (X_{i-1} + 1)^2 - \alpha \beta + \alpha (Y_{i-1} - 1)^2 \\ &+ \beta (X_{i-1} + 1)^2 - \alpha \beta, \\ d_i &= \alpha Y_{i-1}^2 + \alpha (Y_{i-1} - 1)^2 + 2\beta (X_{i-1} + 1)^2 \\ &- 2\alpha \beta. \end{split}$$
 Eq. 4

Determine di+1 if Si is chosen:

$$\begin{aligned} \mathbf{d}_{i+1} &= \alpha \mathbf{Y}_{i-1}^2 + \beta (\mathbf{X}_{i-1} + 2)^2 - \alpha \beta + \alpha (\mathbf{Y}_{i-1} - 1)^2 \\ &+ \beta (\mathbf{X}_{i-1} + 2)^2 - \alpha \beta. \\ \mathbf{d}_{i+1} &= \alpha \mathbf{Y}_{i-1}^2 + \alpha (\mathbf{Y}_{i-1} - 1)^2 + 2\beta (\mathbf{X}_{i-1} + 2)^2 \\ &- 2\alpha \beta. \end{aligned}$$
 Eq. 5

Define Δ_s , the difference between d_{i+1} and d_i if S_i is chosen:

$$\begin{split} &\Delta_s = d_{i+1} - d_i. \\ &\Delta_s = 2\beta (X_{i-1} + 2)^2 - 2\beta (X_{i-1} + 1)^2. \\ &\Delta_s = 2\beta (2X_{i-1} + 3). \\ &\Delta_s = 4\beta X_{i-1} + 6\beta. \end{split}$$
 Eq. 6

Now calculate the quantity to be added to d_i if T_i is chosen:

$$d_{i+1} = \alpha(Y_{i-1} - 1)^2 + \beta(X_{i-1} + 2)^2 - \alpha\beta$$

$$\begin{aligned} d_{i+1} &= \alpha (Y_{i-1} - 1)^2 + \beta (X_{i-1} + 2)^2 - \alpha \beta \\ &+ \alpha (Y_{i-1} - 2)^2 + \beta (X_{i-1} + 2)^2 - \alpha \beta. \\ d_{i+1} &= \alpha (Y_{i-1} - 1)^2 + \alpha (Y_{i-1} - 2)^2 \\ &+ 2\beta (X_{i-1} + 2)^2 - 2\alpha \beta. \end{aligned} \qquad \text{Eq. 7}$$

Define Δ_T , the difference between d_{i+1} and d_i when T_i is chosen:

$$\begin{split} & \Delta_T = d_{i+1} - d_i. \\ & \Delta_T = 2\beta (X_{i-1} + 2)^2 - 2\beta (X_{i-1} + 1)^2 \\ & + \alpha (Y_{i-1} - 2)^2 - \alpha Y_{i-1}^2. \\ & \Delta_T = 4\beta X_{i-1} + 6\beta - 4\alpha Y_{i-1} + 4\alpha. \end{split} \tag{Eq. 8}$$

Now the value of d_i for the initial point, which is located on the Y-axis, must be determined. R_m is half the length of the major axis. If the Y-axis is the major axis, the initial point is the point P(0, R_m). Therefore:

$$d_{i} = 2\alpha R_{m}^{2} - 2\alpha R_{m} + \alpha + 2\beta - 2\alpha\beta.$$
 Eq. 9

If the X-axis is the major axis, the initial point is the point P(0, $(\beta/\alpha)\times R_m$). Therefore:

$$d_i = (2\beta^2/\alpha) \times R_m^2 - 2\beta R_m + \alpha + 2\beta - 2\alpha\beta$$
. Eq. 10

Consider now the slope of a line tangent to the ellipse. Beginning with the original point, it can be guaranteed that the slope of the tangent line will always decrease while the ellipse is being mapped in the first quadrant. Therefore, once the slope of the tangent line reaches -1, it will never be greater than -1. Also, it may be seen that from the point where the slope of the tangent line reaches -1 throughout the rest of the first quadrant, for any point P_{i-1} mapped, T_i will be closer than S_i . At some point, the ellipse will diverge enough from the tangent line of slope -1 for a point U_i (see Figure 6) to be closer to the ellipse than T_i . It will be sufficient to check the new set of points $(T_i$ and $U_i)$ from the point where the tangent line's slope becomes -1. d_i must be recalculated, as must Δ_T and Δ_U . Calculate d_i :

$$\begin{split} d_i &= D(T_i) + D(U_i), \\ d_i &= \alpha (Y_{i-1} - 1)^2 + \beta (X_{i-1} + 1)^2 - \alpha \beta \\ &+ \alpha (Y_{i-1} - 1)^2 + \beta X_{i-1}^2 - \alpha \beta, \\ d_i &= 2\alpha (Y_{i-1} - 1)^2 + \beta X_{i-1}^2 \\ &+ \beta (X_{i-1} + 1)^2 - 2\alpha \beta. \end{split}$$
 Eq. 11

Determine d_{i+1} if T_i is chosen:

$$\begin{aligned} \mathbf{d}_{i+1} &= 2\alpha (\mathbf{Y}_{i+1} - 2)^2 + \beta (\mathbf{X}_{i-1} + 1)^2 \\ &+ \beta (\mathbf{X}_{i-1} + 2)^2 - 2\alpha \beta. \end{aligned} \quad \text{Eq. 12}$$

Define Δ_T as the difference between d_i and d_{i+1} if T_i is chosen:

Table Derivation of the algorithm to map an ellipse.

$$\Delta_T = d_{i+1} - d_i.$$

= $2\alpha(-2Y_{i-1} + 3) + \beta(4X_{i-1} + 4)$. Eq. 13

Determine di+1 if Ui is chosen:

$$d_{i+1} = 2\alpha(Y_{i-1} - 2)^2 + \beta X_{i-1}^2 + \beta(X_{i-1} + 1)^2 - 2\alpha\beta.$$
 Eq. 14

Define Δ_U as the difference between d_{i+1} and d_i if U_i is chosen:

$$\Delta_U = d_{i+1} - d_i.$$

= $2\alpha(-2Y_{i-1} + 3).$

Eq.15

The point where the slope becomes less than -1 must be determined. Finding the first derivative of the ellipse produces the equation:

$$\alpha YY' = -\beta X$$
. Eq. 16

When the slope is required to be less than -1, the relation

$$\alpha Y < \beta X$$
 Eq. 17

determines the point at which this occurs. This concludes the derivation of the algorithm for mapping an ellipse.

Decision Variables (Text begins on page 40) **Listing One**

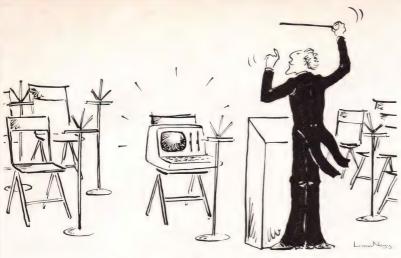
```
compiled 11/28/84 under Lattice C Compiler ver. 2.13
/*
    ellipse.c
      by Tom Hogan
    Version 2.0
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    Permission is granted
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     be returned on a collect basis.
#define ERROR O
#define SUCCESS 1
#define MAX_COL 319
#define MAX ROW 199
#define MIN_COL 0
#define MIN ROW
             /* a macro used in the error-checking code */
 #define inrange(a,x,b) ((a) <= (x) && (x) <= (b))
      /* the bounds of the following array can be set for different
       * graphics modes; this array is used in the assembly algorithm *
       * Listing 2 to write individual pixels.
```

(Continued on next page)

Decision Variables (Listing Continued, text begins on page 40) Listing One

```
int CRT BNDS[] = { MAX COL, MAX ROW, MIN COL, MIN ROW };
          /* r sub m is half the length of the major axis */
ellipse(x, y, r_sub_m, color, aspect)
     int x, y, r_sub_m, color;
     double aspect;
€
int two_x, two_y, or_color;
int col, row, rel_x, rel_y; /* rel_x and rel_y are relative */
double square_aspect; /* to the center */
long temp, beta, alpha, two_alpha, four_alpha, two_beta, four_beta, d;
                     /* d is the decision variable */
                     /* error checking follows */
if ((aspect < 0.004) || !inrange(0, color, 15) || !inrange(1, r_sub_m,
     154)) {
     printf("ELLIPSE(%d, %d, %d, %d, %f) - BAD ARG\n", x, y, r sub m,
          color, aspect);
     return ERROR; }
square aspect = aspect * aspect;
                                       /* initialize the beginning row */
if (aspect < 1.0) {
                                       /* and set the values of
                                                                         */
     alpha = r_sub_m * r_sub_m;
                                      /* constants
     beta = alpha * square aspect;
     row = y + r sub m * aspect; }
else {
     beta = r sub m * r sub m;
     alpha = beta / square aspect;
     row = y + r_sub_m; }
if (alpha == OL) alpha = 1L;
                                /* compensates for very small ellipses */
if (beta == OL) beta = 1L;
or_color = 0xc00 | color; /* sets the high byte for a screen interrupt */
col = x;
                          /*
                                initializes the beginning column
two x = x << 1;
two_y = y << 1;
rel_y = row - y;
two alpha = alpha << 1;
four_alpha = alpha << 2;
four beta = beta << 2;
two beta = beta << 1;
            /* initialize the decision variable -- Eq. 9 or 10 */
d = two_alpha * ((rel_y - 1) * rel_y) + alpha + two_beta * (1 - alpha);
          /* when the slope <= -1, choose the new set of points */
                -- Eq. 17
while (alpha * (rel_y = row - y) > beta * (rel_x = col - x)) {
      write_pix(col, row, two_x - col, two_y - row, or_color);
      if (d >= 0) {
           d += four_alpha * (1 - rel_y);
                                              /* Eq. 6; also first
           row--; }
                                               /* half of Eq. 8;
      d += two_beta * (3 + (rel x << 1));</pre>
                                              /* second half of Eq. 8 */
      col++; }
 /* initialize the decision variable for the rest of the ellipse */
      -- Eq. 11
d = two_beta * (rel_x * (rel_x + 1) + two alpha * (rel_y * (rel_y - 2))
        + 1) + (1 - two_alpha) * beta;
while ((rel y = row - y) + 1)
      write_pix(col, row, two_x - col, two_y - row, or_color);
      if (d <= 0) {
                                               /* col - x == rel
           d += four_beta * (1 + col - x);
                                              /* Eq. 15; also first
           col++; }
                                               /* half of Eq. 13;
     row--;
```

(Continued on page 48)



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XASM51	8051	200.00	250.00
XASM65	6502/65C02	200.00	250.00
XASM68	6800/01, 6301	200.00	250.00
XASM75	NEC 7500	500.00	500.00
XASM85	8085	250.00	250.00
XASM400	COP400	300.00	300.00
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Decision Variables (Listing Continued, text begins on page 40) Listing One

```
d += two alpha * (3 - (rel y << 1)); } /* second half of Eq. 13 */
return SUCCESS; }
            /* write_pix writes four points to the screen, taking *
                advantage of the ellipse's four-way symmetry
write pix(col, row, neg col, neg row, or color)
      int col, row, neg_col, neg_row, or_color;
_pixasm(col, neg_row, or_color);
                                               /* symmetry -- y axis */
_pixasm(neg_col, neg_row, or_color);

_pixasm(neg_col, row, or_color);

_pixasm(col, row, or_color); }
                                               /* symmetry -- center */
                                              /* symmetry -- x axis */
                                               /* mapped point
```

End Listing One

Listing Two

8086 Assembly function that clips against the screen bounds

```
_pixasm(x, y, type_color)
                                    int x, y, type_color
                               type - get or read pix in hi byte
                               color - if set pix, in lo byte
DGROUP GROUP
                DATA
                WORD PUBLIC 'DATA'
DATA
       SEGMENT
       ASSUME
                DS:DGROUP
       EXTRN CRT BNDS:WORD
PGROUP GROUP
                 PROG
PROG
       SEGMENT
                 BYTE PUBLIC 'PROG'
       ASSUME
                 CS: PGROUP
       PUBLIC _pixasm
```

_pixasm	proc near		
	push	bp	; set return address
	mov	bp, sp	
	mov	cx, [bp+4]	; set x (column)
	cmp		; check for max x value
	jg	BYE	
	cmp	CX, CRT BNDS+4	; check for min x value
	jl	BYE	•
	mov	dx, [bp+6]	; set y (row)
	cmp	dx, CRT BNDS+2	; check for max y value
	jg	BYE	
	cmp	dx, CRT BNDS+6	; check for min y value
	jl	BYE	
	MOV	ax, [bp+8]	; set color (al) / fcn number (ah)
	int	10h	; do video interrupt
	sub	ah, ah	
BYE:	pop	bp	; reset frame pointer
	ret		; bye!

pixasm endp PROG ends DATA ends end

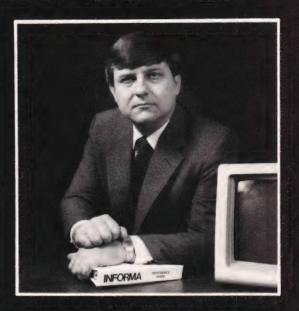
End Listings

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Solid Shape Drawing on the Commodore 64

by Richard Rylander

omputer graphics is one of the fastest growing areas of computer software and hardware development. The old saying that "one picture is worth a thousand words" couldn't be truer when you consider the effectiveness of a pie chart or bar graph versus columns of numbers. Even the simplest of home computers is capable of at least this basic form of computer graphics. But the term computer graphics usually brings to mind more exciting images mundane business than just applications.

Special visual effects for motion pictures and some television advertisements are now being done with computers to create scenes of "enhanced reality." We see spacecraft making impossible manuevers or shimmering corporate logos "flying easy-to-use graphics capabilities to the masses. The "masses," however, may still find the Macintosh pricey, especially if they just want to play around a bit with MacPaint. Lowcost computers, such as the Commodore 64, while lacking the slick graphic interaction of the Mac, are selling for such ridiculously low prices (\$150 at the time of writing this article) that they are hard to pass up. They have the potential for some fairly sophisticated graphics, but so far most of the commercially available software has done little more than add the usual "point plotting" and "line drawing" commands.

This article describes a software package that will allow you to create your own computer-generated scenes, such as "Coffee and Donuts" in Figure 1 (page 55) and "Goblets"

in Figure 2 (page 55). You can con-

struct images from combinations of

elementary shapes (spheres, cylin-

ders, and toroids in various orienta-

tions) or by the "polygon mesh" tech-

nique typically used to render more

complex objects. You can produce

Pumping pixels requires tight, efficient code. Here are halftone shading, backlighting and other visual effects in a tight little package.

by" in ways that would be difficult to simulate with conventional photographic techniques. The enormous amount of data and processing necessary to produce such images has made the use of "super computers" essential. Even with the largest computers working full time on the task, only a few feet of film are produced per day. For home computer owners who want to create their own computer-generated masterpiece, however, the task is not hopeless.

The Apple Macintosh represents a major step in bringing powerful,

drawings with realistic shading effects and in a variety of styles to make some surprisingly detailed pictures with a minimum of effort. The package includes demonstration programs to illustrate how you use each graphic function and style option.

In keeping with the "running light without overbyte" theme of *DDJ*, the entire graphics package fits in 3K of RAM. The program sits in an area of

Richard Rylander, 179 N. McKnight Rd. Apt. 203, St. Paul, MN 55119.

RAM that is inaccessible to BASIC (a 4K block following the BASIC ROM) so that you don't lose any useful program space. Commodore supplies a "DOS Wedge" program that occupies the last 1K of this block, and one of my objectives in the design of the graphics package was to maintain compatibility with this useful utility.

The compactness of the code required me to leave out some "bells and whistles," but I've made no sacrifices to execution speed or user-interface ease. The main omission is error trapping—the software performs no checking to ensure that you use only "legal" point coordinates and so on. This may make program development a little more difficult, but once you have written and debugged a program properly, error checking becomes extraneous and only slows program execution.

We must address several general tasks in developing a shape-drawing program. The first is, of course, how to calculate the apparent brightness for all points on visible surfaces of objects. Next, how do we display these different brightness levels on a high-resolution display that is basically on or off? Finally, a "general" task, but detailed in nature, is creating the specific software tools that will let us do the required calculations and bit-map manipulations.

The program itself is written in machine language, because even with our simplifying restrictions, the plotting of each pixel still involves considerable computation. The program uses integer arithmetic throughout, and we utilize all the symmetry available to eliminate redundant calculations. The main program actually consists of five separate subprograms to break the process into manageable pieces and to provide a library of separate utilities that you may find useful in other programs. Before getting into the problem of shade calculation, I'll present the first subprogram, which is a collection of integer arithmetic utilities that all the later subprograms need.

Integer Arithmetic Utilities

Listing One (page 61) provides the source code for a set of four integer

arithmetic routines. Because we will draw our shaded shapes on a high-resolution display of 320 x 200 points, we need only single-precision arguments for most functions and double-precision results for some intermediate values.

The first two routines, multiplying two single-precision numbers to yield a double-precision product and dividing a double-precision dividend by a double-precision divisor to yield a double-precision quotient, are fairly standard routines that should need no description other than the comments

in the code. The multiplication routine also contains a special case to treat a single-precision number as a signed integer, which is then squared.

The integer-square-root routine deserves some special comment. Have you ever tried the BASIC square-root function? If you have, you know it takes about 52 milliseconds. At this rate, plotting the 64,000 pixels of a 320 x 200 screen would take about 3,328 seconds, or nearly an hour, if we need a square root per point! Of course, we don't need a square root for each point, but it is an

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essential part of many shade calculations. Obviously, anything we can do to speed up this particular function will have a great effect on the overall program speed.

The BASIC square-root routine is slow because BASIC is written to save space, not time. Virtually all small BASICs find a square root by first taking the log of the argument, dividing that result by 2, and then exponentiating. Because LOG and EXP functions are already available, why use any more space than necessary to derive SQR?

One way to speed up this function is to use Newton's method, an iterative procedure that can give full floating-point precision in less time. Because we are interested only in integer results, we can do much better yet, though.

The method we actually use is essentially as fast as doing a single-divide operation. We construct the root, bit by bit, in a manner similar to the way we would take a decimal square root by hand using pencil and paper (does anyone remember how to do that in this age of electronic calculators?). The process is even easier for a binary root because the guesses are, of course, only 1 or 0. The procedure is almost identical to that for division: we guess a partial root bit (as we would guess the next quotient bit) and, in effect, compare the square of the partial quotient to the argument to see if we keep the 1 guessed or change it to a 0 and restore the argument. For more details about this procedure, I recommend section 17.3, "Binary Square Roots—Restoring Method," of the book The Logic of Computer Arithmetic by Ivan Flores (Englewood Cliffs, NJ: Prentice-Hall).

The last integer arithmetic routine is a fast procedure for generating pseudo-random bytes. An old and popular method for generating pseudo-random numbers is the so called congruential method, in which you derive each successive random number from the previous random number by multiplying by a suitably chosen constant and taking the product modulo P, where P is a large prime (see, for example, "A Better Random Number Generator," by H. Cem

Kaner and John R. Vokey in the June 1984 issue of *MICRO*). Again, though, because we are interested only in generating random bytes, we can use a much faster routine.

The method we use is to store two previous random bytes, exclusive OR them bit by bit, and rotate the result to the right by one bit to generate a new random byte. Because it needs no multiply or divide operations, provides a fairly uniform distribution of random bytes, and has a long period, this process is fast. Note that even though we are only generating random bytes (0-255), the pattern or sequence of successive bytes doesn't repeat until after 35,805 calls to the random-number-generator routine (for the initial parameters used in the program). The long period is necessary to ensure that we won't produce any unwanted secondary patterns in what should be "randomly" shaded images—the human eve is adept at picking out such correlations.

Now that some low-level number crunching is out of the way, we can concentrate on the graphics aspects of the main program.

Graphics Utilities

Listing Two (page 62) provides the source code for the elementary graphics functions of displaying and clearing the bit-map, filling the color map (determining dot/background colors), and plotting (and unplotting) individual points both directly and with the plot or unplot decision weighted by a shade value and a particular shade style function. The PLOT (and UNPLOT), CLEAR, COL-OR, GRFON (display graphics screen), and GROFF (return to text screen) routines are specific to the Commodore 64. To adapt this program for other 6502-based computers, you need to rewrite these routines, but the rest of the graphics package (save the final user interface to BASIC) is machine independent.

The SHADE routine is simple and straightforward. We set up an 8 x 8 threshold matrix as a linear array of values 0 to 63. The six bits we need to address an element of this threshold array are determined by masking off the lower three bits of the absolute X

and Y screen coordinates (we take X modulo 8 and Y modulo 8) with the Y bits shifted to become the upper three bits. This effectively repeats the threshold pattern over the entire bitmap area. For each point within an object to be drawn, we calculate a shade value normalized to the range 0-63 and then compare it to the threshold value at that screen point to decide whether to plot or unplot.

If we have, for example, a large area where the shade value is constant at 31 (a 50 percent gray), then within each 8 × 8-pixel character cell block, half of the threshold values will be greater than or equal to the shade value, turning on half the pixels. The order in which the values 0 to 63 are arranged in the threshold table determines how these on pixels are distributed. A common pattern for these so called ordered-dither matrices is a recursive arrangement such as that shown in Table 1 (page 53). If that matrix is subdivided into quarters, sixteenths, and so on, each submatrix has the same general pattern, with offsets between submatrices.

The matrix in Table 1 (classic Bayer ordered dither) keeps the on and off pixels spread as far apart as possible for any shade. This keeps the spatial frequencies in the shade "texture" as high as possible for a particular shade. A disadvantage, though, is that texture changes then accompany shade changes, making shade quantization more apparent. Another disadvantage is that many color monitors have a hard time coping with the very high frequency onoff-on sequence of pixels, distorting shade values when isolated pixels get lost.

As an aside, a convenient algorithm to generate the ordered-dither matrix in Table 1 (or such a matrix of any size) is found in Figure 7 (page 59).

The threshold matrix we actually use in the program is shown in Table 2 (page 53). Here we follow a recursive scheme as we start turning on pixels until eight nuclei have been established. As shades increase, new pixels are added around the edges of these nuclei, simulating the dotgrowth behavior seen in normal printing halftones. Except in the ex-

treme highlight and shadow regions, the shade texture remains fairly constant. Also, the clustering of on or off pixels is much less demanding on the display bandwidth. But take your pick—try filling the threshold matrix with your own patterns.

The RSHADE routine shades by comparing the shade value to a pseudo-random byte shifted right twice to match the 0-63 range. This scheme also tends to average out the tone errors generated as each pixel is turned only on or off (though we want an intermediate shade of gray) by dithering the threshold value randomly at each pixel. Both shading schemes produce sharp edges because each pixel is plotted independently. Abrupt shade changes are then followed faithfully.

The SCALE routine provides an opportunity to mention a general rule you should follow when you try to write fast programs: If at all possible, avoid division; and if you must divide, try to make it by a power of 2 (so that you can use right shifts). The SCALE routine helps correct some of the geometric distortion that otherwise results from plotting objects on the Commodore 64's rectangular bit map. While the monitor display has an aspect ratio of approximately 4:3, the bit-map aspect ratio is 320:200 or 8:5. A simple way to keep sphere outlines circular (instead of the usual egglike appearance) is to work in pseudoscreen coordinates of 320 X 240, giving the bitmap the same 4:3 aspect ratio as the screen display. The SCALE routine then converts 0-239 YPLOT values to a 0-199 range.

An obvious way to do this is to first multiply by 5 and then divide by 6. Or you might save a multiplication by first dividing a copy of YPLOT by 6 and subtracting that from the original YPLOT. A much faster way is to multiply the single-precision YPLOT by 213 and then take just the upper byte of the double-precision product (effectively dividing by 256) as the scaled YPLOT value. This gives us the proper range of absolute screen Y values and "rounds" the scaling as well.

This method of scaling means that you effectively replot every sixth Y line of pixels, but the routine's sim-

plicity more than makes up for plotting 20 percent more points. Scaling is left as an option because some printers (such as the Commodore 1525) have 1:1 dot densities. By not scaling, the screen display is distorted but a hard-copy printout will have the proper geometry. On the other hand, you can set up printers, such as the Epson RX-80, with the appropriate horizontal and vertical dot densities to produce a 4:3 aspect ratio screen dump. Scaling here corrects both the screen and the hard-copy output.

The routine PLTSHD is a higher level routine that simply checks a couple of flags to see what kind of shading we want and whether to scale or not. It then calls the appropriate shading routine. To plot a shade-weighted pixel from BASIC, POKE a shade value (0-63) into VALUE; POKE the absolute X and (optionally scaled) Y values into XPLOT, XPLOT+1, and YPLOT; set up the flags HTORRN (HALFTONE or RANDOM) and NOSCAL; then

SYS to PLTSHD. This seems like a lot of work to plot a single point, but we really won't be plotting shade-weighted points by hand—the later shapedrawing routines take care of this.

Line and Facet Drawing

Listing Three (page 64) completes the elementary graphics functions by adding line-drawing and shaded-facet-drawing routines. We draw lines using a modified form of Bresenham's algorithm, a DDA (Digital Differential Analyzer) technique that keeps the actual plotted points within one half-screen unit of the true line. Regardless of the order in which we specify the line's endpoints, the program sorts them so that lines are always drawn from left to right (the X position is incremented or unchanged at each step). We then need only determine which is greater, the change in X or the change in Y, and whether the Y coordinate difference is positive or negative. The program checks

0	32	8	40	2	34	10	42
48	16	56	24	50	18	58	26
12	44	4	36	14	46	6	38
60	28	52	20	62	30	54	22
3	35	11	43		33	9	41
51	19	59	27	49	17	57	25
15	47	7	39	13	45	5	37
63	31	55	23	61	29	53	21

Table 1
Recursive Arrangement of Ordered-Dither Matrix

0	8	53	61	2	10	55	63
16	24	37	45	18	26	39	47
49	57	4	12	51	59	6	14
33	41	20	28	35	43	22	30
3	11	54	62	1	9	52	60
19	27	38	46	17	25	36	44
50	58	7	15	48	56	5	13
34	42	23	31	32	40	21	29
			Tab	le 2			
							,
			Thresho	ld Matrix			

the scale flag to see if the endpoints should be adjusted (in their Y coordinates) before plotting commences. This keeps a common coordinate system for drawing shaded shapes (spheres and so on) and lines in the same image. A flag MODE determines whether the line is drawn by setting or clearing pixels ("black" or "white" lines).

The program's last elementary function is to draw shaded triangular facets. A powerful and flexible technique for rendering objects is by means of a polygon mesh. Natural polyhedral objects (for example, cubes and pyramids) are obvious candidates for this method, but you can also approximate curved surfaces by a connected mesh of planar polygonal sections. As in piecewise-linear approximations of curves, the finer the segmentation, the better the approximation—although at the price of greater computational overhead. The coordinates of the polygon vertices then constitute a data base that you can manipulate easily for rotational transformations, perspective transformations, and so on. Triangular sections are the simplest to handle and the most general because you can break any higher-order polygon down into triangular sections.

As in the line-drawing routine, the program sorts the endpoints of the facets into a left-to-right order and checks the scale flag to maintain a consistent coordinate system. At each X position across the facet, a top and bottom Y coordinate pair is determined by a simple proportionality between the X difference and Y difference for the particular triangular sides. Although this involves both multiplication and division operations, it is not the innermost loop here (drawing the shaded line segment between Y pairs is). It also does not give more than one Y value per X position, as would a DDA method (for lines with slope greater than 1 in magnitude).

The maximum X difference is restricted to less than 256 (single precision), though the absolute X coordinates for the triangle vertices can be anywhere on the screen. This is not too severe a constraint because most polygon meshes are made up of small

sections, and, if a larger triangle is essential, you can break it into smaller triangles that meet this condition. The occasional inconvenience this might cause is worth the increase in speed and shortened program.

We leave the shade value used in drawing facets as a parameter that the calling program specifies. Although adding "surface normal" calculation and shade value computation based on some illumination model (and requiring the Z coordinates then to be specified for the vertices) is not difficult, the same shade value is used for all points of the facet, making shade determination by a BASIC program practical. Also, leaving shade calculations up to BASIC adds the flexibility that we can use any shading model. The curved surface-drawing routines I describe later do include shade determination, but here each point can potentially have a different shade value, putting it in the innermost loop. For this reason, the curved surfaces must have a fixed shading model.

An interesting use for the BASICspecified shade values is drawing "white" facets by setting the shade to 64. This doesn't seem particularly useful, but when you draw "wireframe" polyhedra, it makes a simple, hidden-line removal scheme possible. If the facets of an object are sorted so that they are drawn from those furthest to those nearest the observer. with "black" lines added around the edges of each facet, then foreground facets that partially obscure background facets erase the lines in their interior. We can set a flag EDGES so that, after the program has drawn a shaded facet, it can add lines automatically to outline and emphasize the edges. The MODE flag again determines how the lines are drawn (set or cleared).

Now that we have a toolbox of arithmetic and primitive-graphics routines, we can concentrate on the main subprogram for drawing shaded, curved surfaces rapidly.

Determining Shade Values for Curved Surfaces

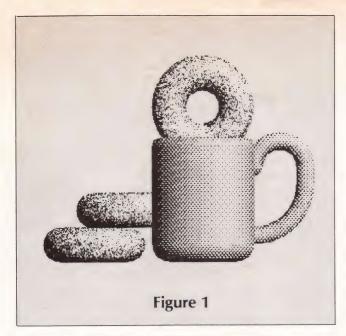
Many recent advances in computer image synthesis have dealt specifical-

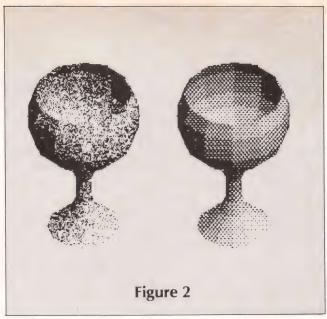
ly with how different surfaces reflect, refract, scatter, or absorb light. Increasingly complex models for the visual appearance of various surface textures have led to ever more realistic images, but increasing computational overhead accompanies these models. We'll employ the simplest shading scheme to start with and then suggest how you might modify it to add "realism," while still keeping the computations manageable.

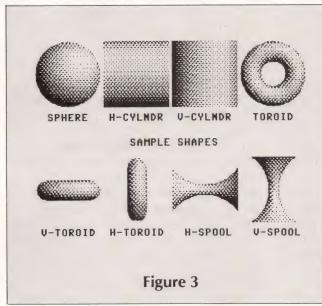
If we restrict ourselves to simple, symmetrical objects, we can greatly simplify the problem of calculating surface brightness. This is not a severe restriction in itself, as you can break most "complex" objects down into combinations of simple elemental shapes. We will have to limit ourselves to "normal" (that is, head-on) views of the objects, because once we allow objects to be rotated, they lose most of their symmetry. Also, calculation time (particularly that for hidden surface removal) increases enormously.

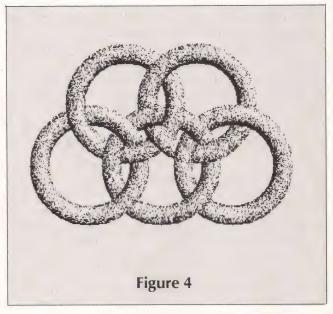
We use the Lambertian (diffuse reflector) model to determine the surface brightness. We assume that light comes from a single, specific direction and then find the cosine of the angle between this reference vector and a surface normal vector. This gives us shade values from 1, where the surface faces the light source, to 0 at the terminator, where light just grazes the surface; and negative values where the surface is turned away from the light. Here is our first option: We can clip the brightness values at 0 so that areas facing away from the light have zero brightness. as an object lit by a single source (in deep space) would appear. Or, we can use the absolute value of the cosine so that the object appears to be lit by two identical sources on opposite sides. Either way is simple, so we allow for both cases by using a flag to determine what kind of illumination scheme we want to use for a whole scene or any individual shape making up the scene.

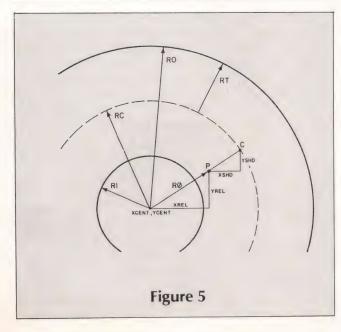
Although this simple model is valid for diffusely reflecting surfaces, it does not account for any contributions from specular surface reflections, secondary light sources (other

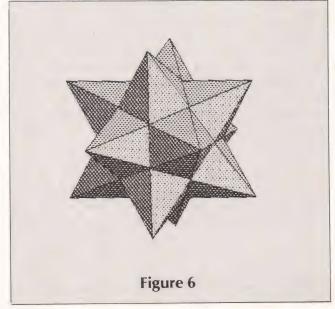












than the "opposite face" light) or ambient (nondirectional) light. These additional subtleties are not difficult to implement, but for the level of detail and dynamic range (number of gray levels) in our halftone display, they really aren't worth the effort.

Object Geometry

Calculating the surface normal vectors for points on an arbitrary surface can be difficult, but this is why we have restricted ourselves to objects that can be made up of combinations of the eight simple shapes shown in Figure 3 (page 55). All the basic shapes have at least fourfold symmetry; that is, once we have found the surface normal for a point (X, Y) in one quadrant of the object, we can easily find the surface normals for the other three quadrants by complementing appropriate components. Also, by restricting ourselves to shapes with relatively simple surfaces, we can determine the required surface normal vectors without resorting to trigonometric functions, using instead some simple geometric consider ations.

Another time-saver is to choose an illumination reference vector to best take advantage of symmetries in the objects. Unfortunately, the "best" choice for computational purposes—a light source directly behind the viewer—gives a rather flat-looking image. Much more pleasing shading results from illumination coming from "over the shoulder." The slight assymmetry that results gives a stronger sense of depth to the objects.

The coordinate system describing objects on the screen has its X axis increasing horizontally to the right, the Y axis increasing vertically upward, and the Z axis increasing out of the screen toward the viewer (a conventional right-handed coordinate system). The illumination reference vector used here has X, Y, and Z components of (1,1,2), representing light coming from over the right shoulder. The symmetry in X and Y makes for easier shade calculations and gives a similar looking scene if you rotate a hard-copy output 90 degrees to fit objects that are taller than they are wide into the display. Lighting in the "portrait" (as opposed to "landscape"—unrotated) mode then comes from over the left shoulder.

Drawing a Shaded Sphere

Spheres are particularly easy objects to deal with because a radial vector from the center to a point on the surface is in the same direction as a surface normal from that point. The only real problem we must deal with is one of normalization. We will always deal with coordinates relative to the "local center of curvature" of objects. For spheres, this is just their geometric center.

Consider a point on the surface that extends out of the screen from relative coordinates (X,Y). We find the corresponding Z value using the equation for a sphere:

$$Z = SQR(R*R - X*X - Y*Y)$$

This calculated Z then forms the third member of the surface normal vector. To calculate the cosine of the angle between this vector and the illumination reference vector, we must first normalize both to have unit length. The normal we have just found has, of course, a length of R, so we need only divide each component by the radius of the sphere. The illumination vector (1,1,2) has a length of SQR(6), so we normalize this vector to unit length by dividing each component by SQR(6).

Now that we have two vectors of unit length but different directions, we find the cosine of the angle between them by taking their inner product, which is nothing more than summing the products of the X, Y, and Z coordinate pairs—like so:

COSINE =
$$(1*X + 1*Y + 2*Z)$$

/ $(R*SQR(6))$

To use integer arithmetic throughout, we don't really want brightness expressed as a fraction between 0 and 1, but rather scaled to a range of integers from 0 to 63. We choose a maximum shade value of 63 to match the 64 gray levels that can be approximated by an 8×8 threshold matrix (described earlier in the "Graphics Utilities" section). We scale the

pseudo-random bytes (used in the RSHADE routine) to the same range simply by shifting them right twice. So, for a surface point (X, Y, Z) on a sphere of radius R, the appropriate SHADE integer (0-63) is:

$$SHADE = 26*(X + Y + 2*Z)/R$$

The factor 26 properly accounts for the SQR (6) in the denominator of the previous equation.

As stated earlier, all the primitive shapes considered here have at least fourfold symmetry. We need compute the Z component only once for each \pm X and \pm Y quartet of points (relative to the geometric center of the object). The sphere and top-view toroid actually have n-fold symmetry, which the Cartesian grid of screen pixels reduces to eightfold symmetry. That is, in addition to changing signs of X and Y, we can exchange the X and Y coordinates to find another surface point with the same Z value.

Shape-Drawing Routines

Listing Four (page 68) is a set of routines for drawing the eight simple shapes shown in Figure 5. The routines resemble some form of compiled BA-SIC in that they use no special bit manipulations or unusual addressing methods, but just have appropriately ordered calls to the lower-level routines set up earlier. Comment statements precede each shape-drawing routine, giving an equivalent BASIC routine. This seems to be the easiest way to explain each routine because most readers are familiar with BASIC program methods. I thus give special comment to only a few of the routines here.

GETVAL is a shade-normalization routine that also checks for normal or backlit illumination via the flag BAK-LIT. The byte pair at TONE contains the inner (dot) product of the illumination vector with the local surface normal vector. GETVAL then effectively does the division by SQR(6)*R and multiplication by 63 to put VALUE into the proper range, clipping at zero or taking the absolute value, as necessary.

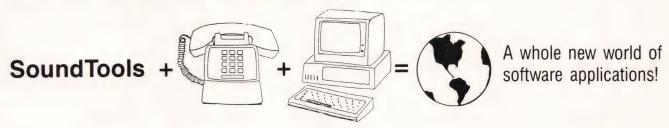
PTPLOT does all the dirty work needed to plot shaded points for four-

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SoundTools, Communicard and SoundWare are registered trademarks of Digital Pathways, Inc. IBM is a registered trademark of International Business Machines Corp. MS is a registered trademark of Microsoft Corporation. Touch Tone is a trademark of AT&T. fold symmetrical objects. Provision is also made here for clipping the object at independent levels up, down, left, and right of the object's center. The clipping feature is needed to blend various primitive objects into more complex ones with smooth transitions at the seams. Clipping also allows us to create some intricate "weaving" effects by redrawing portions of overlapping shapes as in Figure 4, "Linked Toroids," (page 55).

The flag NOROT determines whether the X and Y coordinates will be exchanged. This is useful both for drawing the eightfold symmetrical objects (top-view toroid and sphere) and for rotating the fourfold symmetrical objects 90 degrees so that a single drawing routine can produce two orientations of an object. The calculations are basically those for the sphere, but we'll see later that we can put other objects, such as toroids, into forms that look like spheres (at least locally).

GETZ calculates the effective Z coordinate for a spherelike surface, given the X and Y coordinates relative to the local center of curvature. Note that this routine needs the SQRT function, and because it is in general called for each quartet of points plotted, our fast SQRT makes a big difference in execution speed.

SPHERE follows the previously outlined algorithm and takes full advantage of the available symmetry. The radius is POKEd into RADIUS, and clipping distances relative to the sphere center are POKEd into CLIPL (left), CLIPR (right), CLIPU (up), and CLIPD (down).

CYLNDR draws a cylinder with sizes POKEd into RADIUS and HLEN (half-length). The flag NOROT determines whether the cylinder will be drawn with a horizontal or a vertical axis. The routine is simple, using PTPLOT for the main shade-calculation tasks. Actually, we could write a much faster routine to take advantage of the fact that we can use the same shade value for all points along lines parallel to the cylinder axis. This would require considerably more space, however.

The peculiar manipulations of relative coordinates for plotting toroids in various orientations are easier to explain with the help of the diagram in Figure 5 (appropriate for a topview toroid) (page 55).

Consider a point P at coordinates (XREL, YREL) relative to the center of the object. We can take the local center of curvature of the toroid to be at the point C, which lies on the intersection of the line passing through the center of the toroid and a point beneath P (where Z=0) with a circle of radius RC (the "average" radius of the toroid is RC=(RO+RI) / 2). We determine the surface normal by the relative X, Y, and Z displacements from the point C.

The byte R0 is used for temporary storage of the radial distance from the toroid center to the point beneath P (where Z=0):

R0 = SQR(XREL*XREL + YREL*YREL)

We can determine the relative X and Y displacements, XSHD and YSHD, respectively, of P from C easily by using similar triangles:

XSHD = XREL*(1 - RC/R0)YSHD = YREL*(1 - RC/R0)

We find the Z coordinate via the Pythagorean theorem, using the radius of the "ring" portion, RT, as the hypotenuse. RT is then also the length of the normal vector and is the value POKEd into RADIUS for normalization in the shade calculations.

The routine TOROID follows this algorithm, taking advantage of the eightfold symmetry in the object. EDGTOR and SPOOL use similar center of curvature coordinates to draw other orientations of a toroid. I have given the BASIC equivalents but leave it to readers to draw the appropriate geometric constructions if they desire more details.

Using the Shape-Drawing Routines—Interface to BASIC

The last module, completing our graphics package, is a convenient interface to BASIC. Listing Five (page 76) is a collection of routines to pass parameters easily to the graphics routines from the BASIC programs that control image generation. These rou-

tines are specific to the Commodore 64 in that they use some of the routines in the BASIC ROM to interpret statements in a BASIC program or entered in the direct command mode.

Graphics commands are given in the form:

SYS(FNCTN),PARAM1,PARAM2, [OPTIONAL PARAMETERS]

where FNCTN is the address of the graphic function you desire (or a variable that has been assigned the address value) and PARAM1 and PARAM2 are parameters (such as the center coordinates of an object) that the function requires, followed by any optional parameters.

The parameters can be either literal numerics or expressions that are evaluated for the desired values. We do not have to put the function address in parentheses, but it helps to make the program more readable. Separating parameters by commas is required. If you desire an optional parameter (such as specifying a new radius for sphere drawing), you add it by following the last required parameter by a comma and then the optional value or expression.

The use of optional parameters is more easily explained with the sequence of BASIC statements found in Figure 8 (page 59):

For all the shape-drawing functions, you must specify the X and Y center coordinates, but shape sizes are optional parameters. The last values specified become the new default sizes, allowing you to draw copies of similar objects (such as a bunch of grapes) just by setting the new center coordinates. All shapes but the sphere take two size parameters, and you must specify both when you desire a change (even when only one parameter takes on a new value). All size parameters (radii and cylinder half-lengths) must be less than 256, not a severe limitation considering the screen is only 200 pixels high ("240" with scaling).

The commands to clear the bit map and initialize the color map each take a single optional parameter, but the default values remain unchanged. Unless you specify alternate bytes, the bit map is filled with 0s (cleared) and the color map is filled with 1s (for black dots on a white background). The most useful alternate byte with which to fill the bit map is 255, setting the entire background. Because the shading process clears, as well as sets, appropriate points, objects still appear normal but against a black background (particularly effective when you use the "back light" style).

The byte used to fill the color map is made up of background and foreground nibbles. To initialize the color map for a different color combination than black on white, use:

SYS(52001),16*DC+BC

where DC is the dot-color number and BC is the background-color number (as given in any reference to programming the Commodore 64).

The addresses for all the graphic functions and the parameters they take, along with locations to be POKEd with clipping values and flags for various drawing styles, are summarized in Table 3 (page 60). The best way to see how these shapedrawing routines are used is to examine the demonstration programs in Listing Six (page 78), "SHAPES DEMO," and Listing Seven (page 80), "STELLATION." All the different style options are exercized there. It is useful to save an abbreviated version of SHAPES DEMO, consisting of just the lines up to 340 and the subroutines following line 1620. This skeleton program can then form a base, providing all the POKE and SYS locations you need and to which you can add your own image-generation control program.

Auxiliary Programs

SHAPES DEMO and STELLATION make use of two auxiliary programs that, while not strictly needed to produce graphic images, provide utilities to both enhance the image itself and speed the drawing process. Listing Eight (page 80) is a routine for sorting an integer array indirectly through a key array (to determine drawing order for facets in a polygon mesh quickly). Listing Nine (page 82) allows you to add text to graphic

- 10 REM ORDERED DITHER ARRAY—RECURSIVE FILL
- 20 I=0:J=0:K=1:N=0:P=3
- 30 REM 'P' IS ORDER OF ARRAY, SIZE IS (2 ^ P) x (2 ^ P)
- 40 P=2 P:DIM A(P-1,P-1)
- 50 GOSUB 100
- 60 END
- 100 IF K=P THEN A(I,J)=N:N=N+1:K=K/2:RETURN
- 110 K=2*K:GOSUB 100
- 120 I=I+K:J=J+K:K=2*K:GOSUB 100
- 130 I=I-K:K=2×K:GOSUB 100
- 140 I=I+K:J=J-K:K=2*K:GOSUB 100
- 150 I=I-K:K=K/2:RETURN

Figure 7

(SET UP GRAPHICS, STYLES, ETC)

- 200 SP=52119:REM ADDRESS OF SPHERE FUNCTION
- 210 SYS(SP), 80, 75, 30; REM DRAW A SPHERE OF RADIUS 30 AT X = 80, Y = 75
- 220 SYS(SP), 300, 50 : REM DRAW ANOTHER SPHERE AT X = 300, y = 50
- 230 REM SINCE NO RADIUS IS SPECIFIED, LAST VALUE IS USED AS DEFAULT
- 240 SYS(SP), 200, 150, 40: REM NEW SPHERE RADIUS (40) BECOMES DEFAULT

Figure 8

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The code for these routines is short enough to be tucked in behind Commodore's DOS Wedge program so that, again, you don't lose any BASIC program space but maintain full compatibility with the wedge. If you are willing to give up the convenience of the wedge, however, you can easily relocate these routines to the end of the "interface" subprogram to keep

the graphics package in one piece.

The KEYSORT routine works with two 1-dimensional integer arrays of the same size. One is filled with some "priority" parameter (such as the "average Z" coordinates for the facets in a polygon mesh), and the other becomes a "key" array whose elements index those in the "priority" array in increasing order.

To use the routine, POKE the max-

imum element index of the arrays into location 140, the address of the first (0th) element of the key array into locations 251 (low byte) and 252 (high byte), and the base address of the priority array into 253 and 254. You can find these array base addresses easily because the Commodore 64 places the address of the "current BASIC variable" into locations 71 and 72. Setting the 0th ele-

BIT-MAP SCREEN : 40960-48959 (\$A000-\$BF3F) **COLOR MAP** : 33792-34791 (\$8400-\$87E7) CLIPPING BOUNDS (relative to object center): 893 (\$037D) : LEFT BOUND 894 (\$037E) : RIGHT BOUND 895 (\$037F) : UPPER BOUND 896 (\$0380): LOWER BOUND 53280 (\$D020) : BORDER COLOR [0-15], POKE with 1 to match white screen STYLE FLAGS: 838 (\$0346) : SHADE STYLE [0=random, 1=halftone] 839 (\$0347) : SCALE FLAG [0 = normal (1:1), 1 = scaled (3:4)] 868 (\$0364) : EDGES FLAG [0=normal, 1=add lines to facet edges] : EDGE/LINE MODE [0=draw, 1=erase] 871 (\$0367)898 : LIGHTING STYLE [0=normal single source, 1=backlit] (\$0382)FUNCTION LOCATIONS—CALL WITH "SYS(FNCTN), PARAMETERS," OPTIONAL PARAMETERS IN SQUARE BRACK-ETS, DEFAULT VALUES ARE USED IF NOT SPECIFIED 49378 (\$COE2) SWITCH TO GRAPHICS MODE 49411 (\$C103) : RETURN TO TEXT SCREEN 51979 (\$CBOB) : CLEAR[,CLEAR BYTE] FILL BIT MAP WITH CLEAR BYTE [DEFAULT = 0] : COLOR[,COLOR BYTE] FILL COLOR MAP WITH COLOR BYTE 52001 (\$CB21) 52023 (\$CB37) : PLOT, X, Y SET POINT AT (X, Y) 52026 (\$CB3A) : UNPLOT, X, Y CLEAR POINT AT (X, Y) 52049 (\$CB51) : LINE, X1, Y1, X2, Y2 DRAW A LINE BETWEEN (X1, Y1) AND (X2, Y2) 52052 (\$CB54) : FACET,X1,Y1,X2,Y2,X3,Y3,VA DRAW A SHADED TRIANGULAR FACET BETWEEN COORDINATE PAIRS (X1, Y1), (X2, Y2) AND (X3, Y3) USING "VA" SHADE VALUE (0:BLACK TO 64:WHITE) **CURVED SURFACES:** SPHERE, X, Y[,R] DRAW A SPHERE CENTERED AT (X,Y) WITH RADIUS R 52119 (\$CB97) : TOROID, X, Y[,RI,RO] DRAW A TOP-VIEW TOROID WITH INNER RADIUS RI AND OUTER RADIUS RO 52141 (\$CBAD) 52150 (\$CBB6) : VCYL,X,Y[,R,HL] DRAW A CYLINDER WITH AXIS VERTICAL, RADIUS R AND HALF-LENGTH HL 52153 (\$CBB9) : HCYL,X,Y[,R,HL] DRAW A CYLINDER WITH AXIS HORIZONTAL 52186 : VTOR,X,Y[,RI,RO] DRAW AN EDGE-VIEW TOROID WITH AXIS VERTICAL (\$CBDA) 52189 (\$CBDD) : HTOR,X,Y[,RI,RO] DRAW AN EDGE-VEIW TOROID WITH AXIS HORIZONTAL 52203 : VSPOOL,X,Y[,RI,RO] DRAW AN INSIDE-VIEW TOROID ("SPOOL") WITH AXIS VERTICAL (\$CBEB) 52206 : HSPOOL,X,Y[,RI,RO] DRAW AN INSIDE-VIEW TOROID ("SPOOL") WITH AXIS HORIZONTAL (\$CBEE)

ment of an array equal to itself merely establishes it as the current variable. Then you can transfer the contents of 71 and 72 to the proper pointer at 251/252 or 253/254. Be careful to use literal numerics for "251" and so on, because you don't want to change the current variable. After setting up the pointers, SYS to the start of the KEYSORT routine. Another caution to observe if you use KEYSORT for other applications is not to create any new variables between finding the array base addresses and invoking KEYSORT, because arrays will be pushed up in memory to keep them at the end of BASIC's variable storage.

KEYSORT is set up to handle arrays with up to 256 elements each. If needed, you can sort larger arrays by breaking them into smaller arrays, sorting with KEYSORT, and then collating the results. Merging a few already sorted arrays is a fairly simple operation that you can do in a single pass, extending the usefulness of this routine.

STELLATION uses KEYSORT (lines 840–880) to determine the drawing order for the facets of a small stellated dodecahedron (Figure 6, page 55). This is a polyhedron that is not strictly convex (some internal dihedral angles are greater than 180 degrees), causing some foreground facets to partially obscure those in the background. The Painter's algorithm is used to handle hidden surface removal. Each newly drawn fac-

et completely overwrites the background (clearing, as well as setting, points) so that drawing facets from back to front gives a proper rendering of the object.

The stellated dodecahedron of Figure 6 usually has only 30 visible or partially visible facets. Sorting with BASIC would be satisfactory here, but more complex polygon mesh objects can easily contain hundreds of visible and partially visible facets. You can really appreciate KEYSORT in those cases. Although it uses the simple and inefficient bubble-sort algorithm, being implemented in machine code lets KEYSORT run circles around any BASIC sorting alternative.

The final routine, in Listing Nine, WRITE, allows you to add text to the graphic display. The primary reason for implementing this as a machine code routine is that the bit map for the graphic image is located in the shadow RAM beneath the BASIC ROM (again saving useful BASIC program memory space). We can transfer bit images from the character ROM to the bit map just by PO-KEing values to the bit map, but we gain more flexibility if we first read the bit-map bytes to exclusive OR them, AND them, and so on with character image. Reading the shadow RAM requires that the BASIC ROM be switched out (through the bank switching used in the Commodore 64), something a BASIC program cannot do (and hope to continue execution).

The subroutine following line 1780 in SHAPES DEMO illustrates how to print text in a variety of styles on the bit-map image. Although the program actually uses only one style, the subroutine allows for five possibilities. Normal ("black" on "white") characters and reverse characters can overwrite the background, black characters can be ORed with the background, white characters ANDed with the background, or black characters exclusive ORed with the background. The last possibility gives characters that "change phase" across black/white edges in the image.

Conclusion

The graphics package described here we hope presents some new approaches to computer-generated images on small systems. At the same time, I'm sure I've missed some obvious tricks that would speed up the programs or reduce their size. I will be interested to see responses to this article and shortcuts or enhancements that might be added. Although the price/performance ratio of small graphics system continues to improve (especially with the development of custom LSI graphics chips), there are vet many unexplored software approaches to the problem.

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Drawing on the C-64 (Text begins on page 50) **Listing One**

```
ดองด
                                      : INTEGER ARITHMETIC ROUTINES
00001
00002
00003
00004
00005
00006
00007
000007
00009
00009
                                       RICHARD L. RYLANDER 8/12/04
                                       REVISED 10/29/84 TO ADD FULL DOUBLE PRECISION ARGUMENTS IN DIVIDE ROUTINE
                                      ; USE PAGE ZERO LOCATIONS WHERE POSSIBLE FOR
; ITERATIVE PROCEDURE WORK SPACE
          anaa
          pppu
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00019
          9999
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                                                                        : MULTIPLICAND
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                                      MLFI.ER =#AD
PROD =#AE
                                                                         : MULTIPLIER
: PRODUCT
                                      DADND
                                                                         : DIVIDEND/QUOTIENT
                                      DVSOR
RMNDR
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00020
                                     RADEND =#AC
ROOT =#033C
00021
          ผผผล
00022
00023
          9000
          9000
9000
                                      TEMP
00024
00025
                                      ; SET UP SEED VALUES FOR ESCUDO RANDOM NUMBERS
00026
          0000
                                                *=#C000
BYTE #FF,#55
00027
           0000
00028
          C000
C001
C002
C003
C004
C004
                                      RTEMP . BYTE 100,100
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(Continued on next page)

Drawing on the C-64 (Listing Continued, text begins on page 50) Listing One

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                                               DIVIDE DOUBLE PRECISION DIVIDEND
BY DOUBLE PRECISION DIVISOR GIVING
DOUBLE PRECISION QUOTIENT
                                                                                                                                                                               DIVIDEND IS REPLACED BY QUOTIENT IN THE PROCESS
                                                                                                                                                                               QUOTIENT IS ROUNDED TO NEAREST INTEGER
                                                                                                                                                                     STA RMNDR
STA RMNDR
STA RMNDR+1
LDX #$10
                                                                                                        90
84
85
10
FD
FE
84
85
                                                                                        DLOOP
                                                                                                                                                                                                                ROL DVDND
ROL DVDND+1
                                                                                                                                                                                                                ROL RMNDR+1
                                                                                                                                                                                                                LDA RMNDR
                                                                                                                                                                                                                                       DVSOR
                                                                                                                                                                                                                LDA RMNDR+1
                                                                                                                                                                                                              SBC DVSOR+
BCC DECCNT
STY RMNDR
                                                                                                                                                                                                                                        DVSOR+
                                                                                                                                                                                                                STA RMNDR+1
                                                                                                                                                                  DECENT DEX
                                                                                                                                                                                                                BNE DLOOP
                                                                                       26 FD
26 FE
06 B4
26 B5
B0 0B
                                                                                                                                                                                                                                                                                                                 ; CHECK IF REMAINDER
; IS >= 1/2 OF DIVIDEND
; FOR ROUNDING
                                                                                                                                                                                                                ROL
                                                                                                                                                                                                                                       DVDND
                                                                                                                                                                                                                ROL
                                                                                                                                                                                                                                       DVDND+1
                                                                                                                                                                                                                                        RMNDR
                                                                                                                                                                                                                                       RMNDR+1
                                                                                                                                                                                                                BCS ROUND
                                                                                       38
A5
E5
E5
E6
E6
D0
E6
                                                                                                                                                                                                              LDA DVSOR
SBC RMNDR
      00099
00100
00101
00102
00103
00104
00105
00105
00106
00107
00108
00109
00111
                                                                                                                                                                                                              LDA DVSOR+1
SBC RMNDR+1
                                                                                                                                                                ROUND INC DVDND+1
NOCHNG RTS
                                                                                                                                                                            TAKE INTEGER SQUARE ROOT OF A DOUBLE PRECISION RADICAND GIVING SINGLE PRECISION ROOT ( <= REAL ROOT )
      00112
                                                                                                                                                       DRT LDX ##98
LDA ##98
STA ROUT |
STA ROUT |
STA ROUT |
STA ROUT |
STA TEMP |
ORT | ASL ROUT |
INC ROUT |
ROL TEMP |
ROL ROUT |
ROUT |
ROUT |
ROUT |
RESTOR SC |
LDA ROUT |
ROUT |
RESTOR SC |
LDA ROUT |
RESTOR SC |
ROUT |
RESTOR SC |
LDA ROUT |
RO
                                                                                 00114
      00116
                                            00119
      00120
      00121
                                                                                                                                                                                                                                                                                                              ; ASSUME CURRENT LSB OF ; ROOT WILL BE 1
    00121
00122
00123
00124
00125
00126
00127
                                                                                                                                                                                                                                                                                                              ; SHIFT RADICAND LEFT
; TWICE INTO TEMP
   00127
00128
00129
00130
00131
00132
00133
                                                                                                                                                                                                                                                                                                              ; SUBTRACT ROOT ESTIMATE
; FROM TEMP
    00134
   00136
   00138
                                                                                                                                                                                                                                                                                                              ; SUBTRACTION OF
   00140
                                             COAZ
    00141
   00142
                                             CØA5
                                            COAR
COAR
COAR
    00143
   00144
                                                                                 DØ C5
4C C1 CØ
38
   00146
00147
                                             CØAD
CØBØ
                                                                                 38
AD 3C 03
E9 01
8D 3C 03
B0 03
CE 3D 03
CA
D0 B1
   00148
00149
00150
00151
                                            CØB1
CØB4
CØB6
CØB6
CØB6
CØB6
CØC1
CØC4
CØC7
CØC8
CØC8
CØC8
CØC8
CØC8
CØC8
   00152
00153
20153 C0BE
20154 C0BF
20155 C0C4
20155 C0C7
20156 C0C4
20157 C0C7
20158 C0CB
20161 C0CB
20161 C0CB
20161 C0CB
20164 C0CB
20164 C0CB
20164 C0CB
20165 C0CB
20166 C0CE
20167 C0CB
20171 C0DB
20172 C0DB
20173 C0E1
20174 C0E2
20174 C0E2
20177 C0CB
201774 C0E2
201774 C0E2
201774 C0E2
                                                                                                                                                                                                         ENE SORT1
ROR ROOT+1
                                                                                 6E 30 03
6E 30 03
                                                                                                                                                             FINI
                                                                                                                                                                                                                                                                                                             ; FINAL /2 TO NORMALIZE
                                                                                                                                                                                                         ROR ROOT
                                                                                                                                                                ; GENERATE PSEUDO-RANDOM BYTES
; EXIT WITH P-R BYTE IN ACCUM.
                                                                                                                                                            ; RANDOM LDA RNDM
STA RTEHP
EOR RNDM+1
ROL RTEMP+1
ROR A RTEMP+1
STA RNDM+1
LDA RTEMP
STA RNDM+1
RTS
                                                                                 AD 00 C0
8D 02 C0
4D 01 C0
2E 03 C0
6A
6E 03 C0
8D 00 C0
AD 02 C0
8D 01 C0
                                                                                                                                                                                                                                                                                                             ; RTEMP+1 PRESERVES
; CARRY BIT FOR CYCLING
; RANDOM NUMBERS
                                                                                                                                                             . END
   ERRORS - 00000
   ERRORS = 00000
   SYMBOL TABLE
    SYMBOL VALUE
                                                                CØ45
ØØFB
ØØAD
CØBE
                                                                                                                  DIVIDE
FINI
MULT
NOADD
                                                                                                                                                                           CØ25
CØ01
CØ11
CØ1C
          DECCNT
                                                                                                                                                                                                                                                                                      C02D
C015
C080
C063
C0C8
033C
       DVSOR
MLPLER
NEXT3
PROD
                                                                   OOAE
                                                                                                                    RADOND
                                                                   00B4
0002
                                                                                                                    RNDM
                                                                                                                                                                           CØ64
   END OF ASSEMBLY
                                                                                                                                                                                                                                                                                                                                                                         End Listing One
```

Listing Two

```
00001
                                                                                                                                                           : GRAPHICS UTILITIES
        00002
00003
                                               DOMO
                                               DOGO
                                                                                                                                                                 RICHARD L. RYLANDER 11/4/84
        00004
                                               9000
        00005
                                               0000
                                                                                                                                                                   LOAD ARITHMETIC UTILITIES FIRST
        99996
                                               8888
        00007
                                               DIDIDID
       MANAN
                                               agaa
                                               2000
2000
2000
2000
2000
       00000
                                                                                                                                                        ;
MLPCND=#AC
MLPLER=#AD
PROD=#AE
MULT=#C011
                                                                                                                                                                                                                                                                                               ; MULTIPLICAND (S)
; MULTIPLIER (S)
; PRODUCT (D)
; CALL FOR MULTIPLY
       00010
       00011
00012
00013
00014
                                              2000
2000
2000
2000
                                                                                                                                                       RNDH=#C000 ; RANDOM NUMBER
RANDOM=#C0C8 ; CALL FOR RANDOM
; NOTE - A CALL TO 'RANDOM' LEAVES A RANDOM BYTE
; IN THE ACCUMULATOR
        00015
       00016
                                               BNAG
        00018
                                              9990
                                                                                                                                                                                                  *=RAM
       00020
00021
                                              0000
                                                                                                                                                       *=RAM
PLTFLG *=*+1
XPLT *=*+1
VIC1 *=*+1
VIC2 *=*+1
VALUE *=*+2
HTDRRN *=*+1
                                                                                                                                                                                                                                                                                               ; PLOT/UNPLOT FLAG
; ABSOLUTE PLOT COORD
; ABSOLUTE PLOT COORD
; REGISTER STORAGE
; REGISTER STORAGE
       00022
00023
00024
00025
                                            0341
0342
0343
0344
0346
0347
       00026
00027
00028
00029
                                                                                                                                                                                                                                                                                               ; FINAL NORMALIZED SHADE VALUE
; FINAL NORMALIZED SHADE VALUE
; SHADE FLAG, 1=HALFTONE
; SCALE FLAG, 1=NO SCALE
; TEMPORARY STORAGE
                                                                                                                                                          NOSCAL ***+1
TEMP ***+2
                                               0348
       00030
                                            034A
034A
                                                                                                                                                                                                 #=ORIGIN
        00031
       00032
                                              CØE2
        00033
                                              CØE 2
                                                                                                                                                          **********************
       00034
                                              CØE2
                                                                                                                                                                 TURN ON BIT MAP GRAPHICS MODE,
SAVING REGISTER VALUES FOR
RETURN TO TEXT MODE LATER.
        00035
       00036
                                              CØE2
       00037
00038
                                                                               AD 11 D0 297 20 8D 11 D0 AD 880 D0 AD 880 D0 AD 880 DD AD 18 D0 AD 19 BD 18 D0 AD 18
                                                                                                                                                      $ GRFON LDA $DØ11 ORA #$20 STA $DØ10 LDA $DD00 STA VIC1 AND #$FC ORA #$01 STA $DD00 LDA $DD00 LD
       00039
00040
                                              CØE2
       00041
                                            C0E7
C0EA
C0ED
C0F0
C0F2
C0F4
C0FA
C0FA
C0FF
C102
C103
C103
C103
C103
     00043
    00045
00046
00047
00048
00050
00051
00051
00053
00053
                                                                                                                                                                                               LDA #D018
STA VIC2
LDA ##19
STA #D018
                                                                                                                                                        ; RETURN TO TEXT SCREEN
       00054
00057
                                              C103
                                                                             AD 11 D0
29 DF
BD 11 D0
AD 42 03
6D 00 DD
AD 43 03
8D 18 D0
60
                                                                                                                                                       # GRFOFF LDA *DØ11
AND **DF
STA *DØ11
LDA VIC1
STA *DØ08
LDA VIC2
STA *DØ18
RTS
       00059
00059
                                              C106
C108
                                            C108
C108
C10E
C111
C114
C117
C118
     00061
00061
     00063
00064
00065
 200266
000267
000068
000269
000070
000071
000071
000073
000074
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000077
                                           FILL COLOR MAP FOR BLACK DOTS ON WHITE
                                                                               A9 01
A2 00
9D 00 84
9D 00 85
9D 00 86
9D 00 87
CA
DØ F1
                                                                                                                                                      COLOR LDA #$01
LDX #0
COL1 STA #8400,X
STA #8500,X
STA #8700,X
DEF
                                                                                                                                                                                                                                                                                          : POKE NEW COLORS HERE
                                                                                                                                                                                               BNE COL1
                                                                                                                                                          ;
; *********************************
                                                                                                                                                           CLEAR HI-RES GRAPHICS SCREEN
                                                                                                                                                      ;
CLEAR LDA ##AØ
STA #FC
LDY #Ø
STY #FB
LDA #Ø
LDX ##EØ
CLRLP STA (#FB),Y
                                                                                                                                                                                                                                                                                             : CLEAR BYTE
                                                                                                                                                                                                 INY
                                                                                                                                                                                              DEX
BNE CLRLP
RTS
                                                                                                                                                                PLOT AND UNPLOT POINTS ON HI-RES GRAPHICS SCREEN. ABSOLUTE X AND Y SCREEN COORDINATES ARE POKED INTO XPLT, XPLT+1, AND YPLT
00100
00101
00102
00103
00104
00105
00106
00107
00109
00109
                                           C143
C143
C143
C145
C145
C146
C148
C148
C148
                                                                            A9 00
2C
A9 80
8D 3E
A5 01
29 FE
B5 01
                                                                                                                                                    PLOT LDA #8
.BYTE #2C
UNPLOT LDA #880
STA PLTFLG
LDA #81
AND ##FE
STA #81
                                                                                                                                                                                                                                                                                             BASIC ROM OUT
 00111
00112
00113
00114
00115
00116
                                           C151
C152
C154
C157
C158
C159
C15A
                                                                                                                                                                                              LDA ##C7
SBC YPLT
TAX
                                                                                                                                                                                                                                                                                            ; INVERT Y COORDINATE TO
; PUT ORIGIN IN LOWER LEFT
; CORNER OF SCREEN
; (199.-YPLT)
                                                                                                                                                                                           TAX
LSR A
LSR A
TAY
LDA TABLE1,Y
SIA #FB
LDA TABLE2,Y
STA #FC
                                        C158
C15C
C15F
  00118
                                                                             89 AB C1
85 FB
  00120
                                         C161
C164
```

(Continued on page 64)

DIGITAL RESEARCH COMPUTERS [214] 225-2309

DISK DRIVE SPECIAL!

51/4" Double Sided-Double Density

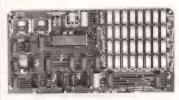
Brand New, Unused. Mfg. in Japan by 2/3 Height - Direct Drive! Industry Standard Pin Out. 6MS Access. 40 Tracks.

2 FOR \$85

Sold on a First Come Basis Add \$2 Each U.P.S.

256K S-100 SOLID STATE DISK SIMULATOR! WE CALL THIS BOARD THE "LIGHT-SPEED-100" BECAUSE IT OFFERS AN ASTOUNDING INCREASE IN YOUR COMPUTER'S PERFORMANCE WHEN COMPARED TO A MECHANICAL FLOPPY DISK DRIVE.

PRICE CUT!



BLANK PCB (WITH CP/M° 2.2 PATCHES AND INSTALL PROGRAM ON DISKETTE) \$6995

(8203-1 INTEL \$29.95)

FEATURES:

- 256K on board, using + 5V 64K DRAMS.
- Uses new Intel 8203-1 LSI Memory
- Requires only 4 Dip Switch Selectable I/O Ports
- Runs on 8080 or Z80 S100 machines. Up to 8 LS-100 boards can be run
- Up to 8 LS-100 boards can be run together for 2 Meg, of On Line Solid State Disk Storage. Provisions for Battery back-up. Software to mate the LS-100 to your CP/M* 2.2 DOS is supplied.
- The LS-100 provides an increase in speed of up to 7 to 10 times on Disk
- Intensive Software.
- Compare our price! You could pay up to 3 times as much for similar boards.

#LS-100

(FULL 256K KIT)

THE NEW ZRT-80

CRT TERMINAL BOARD!

A LOW COST Z-80 BASED SINGLE BOARD THAT ONLY NEEDS AN A LOW COST 2-80 BASED SINGLE BOARD THAT ONLY NEEDS A ASCII KEYBOARD, POWER SUPPLY, AND VIDEO MONITOR TO MAKE A COMPLETE CRT TERMINAL. USE AS A COMPUTER CONSOLE, OR WITH A MODEM FOR USE WITH ANY OF THE PHONE-LINE COMPUTER

FEATURES

- Uses a Z80A and 6845 CRT Controller for powerful video capabilities
- RS232 at 16 BAUD Rates from 75
- 24 x 80 standard format (60 Hz).
- Optional formats from 24 x 80 (50 Hz) to 64 lines x 96 characters 60 Hz)
- Higher density formats require up to 3 additional 2K x 8 6116 RAMS.
 Uses N.S. INS 8250 BAUD Rate Gen. and USART combo IC.

 3 Terminal Emulation Modes which
- are Dip Switch selectable. These include the LSI-ADM3A, the Heath H-19, and the Beehive.
- Composite or Split Video.
 Any polarity of video or sync.
 Inverse Video Capability.
 Small Size: 6.5 x 9 inches.
- Upper & lower case with descenders
- 7 x 9 Character Matrix. Requires Par. ASCII keyboard.

SOURCE DISK! (CP/M COMPATIBLE)



(COMPLETE KIT.

BLANK PCB WITH 2716 CHAR. ROM, 2732 MON. ROM

\$4995

SOURCE DISKETTE - ADD \$10

SET OF 2 CRYSTALS - ADD \$7.50

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64K S100 STATIC RAM

\$139 PM

NEW!

LOW POWER! 150 NS ADD \$10

BLANK PC BOARD WITH DOCUMENTATION \$49.95

SUPPORT ICs + CAPS \$17.50

FULL SOCKET SET \$14.50

FULLY SUPPORTS THE NEW IEEE 696 S100 STANDARD (AS PROPOSED)

FOR 56K KIT \$125

ASSEMBLED AND TESTED ADD \$50

PRICE CUT! FEATURES:

- Uses new 2K x 8 (TMM 2016 or HM 6116) RAMs. Fully supports IEEE 696 24 BIT Extended

- Fully supports IEEE 696 24 BIT Extended Addressing.
 64K draws only approximately 500 MA.
 200 NS RAMs are standard. (TOSHIBA makes TMM 2016s as fast as 100 NS. FOR YOUR HIGH SPEED APPLICATIONS.)
 SUPPORTS PHANTOM (BOTH LOWER 32K AND ENTIRE BOARD).
 2716 EPROMs may be installed in any of top 46K. Any of the top 8K (E000 H AND ABOVE) may be disabled to provide windows to eliminate any possible conflicts with your system monitor, disk controller, etc.
- disk controller, etc. Perfect for small systems since BOTH RAM and EPROM may co-exist on the same board.
- * BOARD may be partially populated as 56K

64K SS-50 STATIC RAM

NEW!

LOW POWER!

RAM OR EPROM!

BLANK PC BOARD WITH DOCUMENTATION

\$52 SUPPORT ICs + CAPS \$18.00

FULL SOCKET SET

\$15.00 56K Kit \$129

64K Kit \$139

ASSEMBLED AND TESTED ADD \$50

FEATURES:

- EATURES:
 Uses new 2K x 8 (TMM 2016 or HM 6116) RAMs.
 Fully supports Extended Addressing.
 64K draws only approximately 500 MA.
 200 NS RAMs are standard. (TOSHIBA makes
 TMM 2016s as fast as 100 NS. FOR YOUR HIGH
 SPEED APPLICATIONS.)
 Board is configured as 3-16K blocks and 8-2K
 blocks (within any 64K block) for maximum
 flashbility.
- flexibility
- 2716 EPROMs may be installed anywhere on
- Board.
 Top 16K may be disabled in 2K blocks to avoid any I/O conflicts.
 One Board supports both RAM and EPROM.
 RAM supports 2MHZ operation at no extra

NEW!

BLANK PC BOARD

WITH DATA

\$39.95

SUPPORT

IC'S

PLUS CAPS \$16

FULL

SOCKET SET

- charge!
 Board may be partially populated in 16K increments.

32K S100 EPROM/STATIC RAM

NEW!

EPROM II

FULL EPROM KIT

\$69.95

A&T EPROM ADD \$35.00

FOUR FUNCTION BOARD!



We took our very popular 32K S100 EPROM Card and added additional logic to create a more versatile EPROM/RAM Board.

* This one board can be used in any one of four ways:

A. As a 32K 2716 EPROM Board

B. As a 32K 2732 EPROM Board (Using Every Other Socket)

C. As a mixed 32K 2716 EPROM/2K x 8 RAM Board

C. As a mixed 32K 2716 EPROM/2K x 8 HAM Board D. As a 32K Static RAM Board Uses New 2K x 8 (TMM2016 or HM6116) RAM's Fully Supports IEEE 696 Buss Standard (As Proposed) Supports 24 Bit Extended Adressing 200 NS (FAST!) RAM'S are standard on the RAM Kit Supports both Cromemco and North Star Bank Select Supports Phantom

On Board wait State Generator Every 2K Block may be disabled

Addressed as two separate 16K Blocks on any 64K Boundary Perfect for MP/M* Systems

* RAM Kit is very low power (300 MA typical)

32K STATIC RAM KIT — \$109.95

TERMS: Add \$3.00 postage. We pay balance. Orders under \$15 add 75¢ handling. No. C.O.D. We accept Visa and MasterCharge. Tex. Res. add 5-1/8% Tax. Foreign orders (except Canada) add 20% P & H. Orders over \$50, add 85¢ for for insurance.

Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Two

00123 C166 8A TXA 00124 C167 29 07 AND ##07	00214 C212 F0 10 BEQ NORM 00215 C214 :
00125 C169 18 CLC 00126 C16A 65 FB ADC \$FB	00216 C214 ; SCALE Y FROM 0-239 PSEUDO-COORDINATES 00217 C214 ; TO 0-199 TRUE SCREEN COORDINATES BY
00127 C16C 85 FB STA \$FB 00128 C16E AD 3F 03 LDA XPLT	00218 C214 Y = (Y+1) *213/256
00129 C171 29 F8 AND ₩\$F8 00130 C173 65 FB ADC \$FB	00220 C214 AC 41 03 SCALE LDY YPLT
00131 C175 85 FB STA ≸FB	00222 C218 84 AD STY MLPLER
00133 C17A 65 FC ADC 4FC	00223 C21A A9 D5 LDA ##D5 ; 213.
00134 C17C 85 FC STA \$FC 00135 C17E A9 A0 LDA ##A0	00225 C21E 20 11 C0 JSR MULT ; RETURN WITH HIGH BYTE 00226 C221 8D 41 03 STA YPLT ; IN ACCUMULATOR
00136 C180 65 FC ADC \$FC 00137 C182 85 FC STA \$FC	00227 C224 AD 46 03 NORM LDA HTDRRN 00228 C227 F0 03 BED RPLT
00138 C184 AD 3F 03 LDA XPLT 00139 C187 29 07 AND #107	00229 C229 4C DD C1 JMP SHADE
00140 C189 49 07 EOR #\$07 00141 C188 AA TAX	00230 C22C 4C FF C1 RPLT JMP RSHADE 00231 C22F ;
00142 C18C A9 01 LDA #101	00232 C22F ; 00233 C22F ;
00143 C18E CA PLOTEP DEX 00144 C18F 30 03 BMI PLOT2	00234 C22F 00 THRESH .BYTE \$00,\$08,\$35,\$3D
00145 C191 0A ASL A 00146 C192 D0 FA BNE PLOTLP	90234 C231 35 90234 C232 30
00147 C194 AØ 00 PLOT2 LDY #0 00148 C196 2C 3E 03 BIT PLTFLG	00235 C233 02 .BYTE #02,#0A,#37,#3F
00149 C199 10 05 BPL NOPLOT 00150 C19B 49 FF EOR ##FF	00235 C235 37
00151 C19D 31 FB AND (#FB),Y	00235 C236 3F 00236 C237 10 .BYTE \$10,\$18,\$25,\$2D
00153 C1A0 11 FB NOPLOT ORA (#FB),Y	00236 C238 18 00236 C239 25
00154 C1A2 91 FB STA (#FB),Y 00155 C1A4 A5 01 LDA #01 ; BASIC ROM RESTORED	00236 C23A 2D 00237 C23B 12 .BYTE \$12,\$1A,\$27,\$2F
00156 C1A6 09 01 ORA #\$01 00157 C1AB 85 01 STA \$01	00237 C23C 1A 00237 C23D 27
00158 C1AA 60 RIS 00159 C1AB :	00237 C23E 2F
00160 CIAB 00 TABLE1 BYTE \$00,\$40,\$80,\$C0	00238 C240 39
00160 CIAD 80 00160 CIAE CO	00238 C241 04 00239 C242 0C
00161 C1AF 00 .BYTE #00, #40, #80, #C0	00239 C243 33 .BYTE \$33,\$3B,\$06,\$0E
00161 CIBO 40 00161 CIBI 80	00239 C245 05 00239 C246 0E
00161 C1B2 C0 00162 C1B3 00 ,BYTE #00,#40,#80,#C0	00240 C247 21 .BYTE #21,#29,#14,#1C
00162 C184 40 00162 C185 80	00240 C249 14
00162 C1B6 C0	00240 C24A 1C 00241 C24B 23 .BYTE \$23,\$2B,\$16,\$1E
00163 C1B8 40	00241 C24C 28 00241 C24D 16
00163 C1B9 80 00163 C1BA C0	00241 C24E 1E 00242 C24F 03 .BYTE #03,#0B,#36,#3E
00164 C1BB 00 .BYTE \$00,\$40,\$80,\$C0	00242 C250 0B 00242 C251 36
00164 CIBD 80 00164 CIBE C0	00242 C252 3E 00243 C253 01 .BYTE \$01,\$09,\$34,\$3C
00165 C1BF 00 .BYTE \$00,\$40,\$80,\$00 00165 C1C0 40	00243 C254 09 00243 C255 34
00165 C1C1 80 00165 C1C2 C0	00243 0256 30
00165 C1C3 00 00166 C1C4	99244 C25B 1B
00167 C1C4 00 TABLE2 .BYTE #00.#01.#02.#03	00244 C259 26 00244 C25A 2E
00167 C1C5 01 00167 C1C6 02	00245 C25B 11 .BYTE \$11,\$19,\$24,\$2C 00245 C25C 19
00167 C1C7 03 00168 C1C8 05 .BYTE \$05,\$06,\$07,\$08	00245 C25D 24 00245 C25E 2C
00168 CIC9 06 00168 CICA 07	00246 C25F 32 .BYTE \$32,\$3A,\$0F
00168 C1CB 0B	00246 C261 07
00169 C1CD 0B	00246 C262 0F 00247 C263 30 .BYTE \$30,\$38,\$05,\$0D
00169 C1CF 0D	00247 C264 38 00247 C265 05
06170 C1D0 0F .BYTE #0F, #10, 411, #12	00247 C266 0D 00248 C267 22 .BYTE #22,#2A,#17,#1F
00170 C1D2 11 00170 C1D3 12	00248 C268 2A 00248 C269 17
00171 C1D4 14 .BYTE \$14,\$15,\$16,\$17	00248 C26A 1F
00171 C1D6 16 00171 C1D7 17	88249 C26C 28
00172 C1D8 19 .BYTE \$19,\$1A,\$1B,\$1C,\$1E	00249 C26E 1D
00172 C1DA 1B 00172 C1DB 1C	00250 C26F .END
00172 C1DC 1E	ERRORS = 00000
00173 C1DD ; 00174 C1DD ; ***********************************	
00175 CIDD ; 00176 CIDD ; SHADING BY HYBRID DITHER/DOT-GROWTH	SYMBOL TABLE
00177 C1DD ; 00178 C1DD AD 3F 03 SHADE LDA XPLT : USE BITS***	SYMBOL VALUE CLEAR C12C CLRLP C13B COL1 C11C CDLOR C11B
00179 C1E0 29 07 AND ##07 ; OF 'X' SCREEN COORD 00180 C1E2 8D 48 03 STA TEMP	GREATR CIFC GREOFF C103 GREON COE2 HTDRRN 0346
00181 C1E5 AD 41 03 LDA YPLT ; AND BITS** 00182 C1E8 29 07 AND #≱07 ; DF 'Y' SCREEN COORD	MLPCND 00AC MLPLER 00AD MORE C20C MULT C011 NOPLOT C1A0 NORM C224 NOSCAL 0347 ORIGIN C0E2
00193 CIEA 0A ASL A ; SHIFTED INTO***	PLOT C143 PLOT2 C194 PLOTLP C18E PLTFLG 033E PLTSHD C20F PROD 00AE RAM 033E RANDOM C0C8
00184 CIEB 0A ASL A ; POSITION TO DETERMINE 00185 CIEC 0A ASL A ; 6-BIT OFFSET IN	RNDM C000 RPLT C22C RSHADE C1FF SCALE C214 SHADE C1DD TABLE1 C1AB TADLE2 C1C4 TEMP 0348
00186 C1ED 0D 48 03 ORA TEMP ; THRESHOLD TABLE	THRESH C22F UNPLOT C146 VALUE 0344 VIC1 0342 VIC2 0343 XPLT 033F YPLT 0341
00188 C1F1 BD 2F C2 LDA THRESH,X ; SCREEN-POSITION-WEIGHTE 00189 C1F4 CD 44 03 CMP VALUE ; THRESHOLD VALUE	p
00190 C1F7 10 03 BPL GREATR 00191 C1F9 4C 46 C1 JMP UNFLOT	END OF ASSEMBLY End Listing Two
00192 CIFC 4C 43 CI GREATR JMP PLOT 00193 CIFF	
00194 CIFF ;***********************************	Listing Three
00196 CIFF : SHADING BY RANDOM HALFTONE	
00198 C1FF 20 CB C0 RSHADE JSR RANDOM	00001 0000 ; FACET - DRAW SHADED TRIANGULAR FACETS 00002 0000 ; AND STRAIGHT LINES.
00200 C203 4A LSR A ; TO 6 BITS FOR SHADE	88883 8888
00201 C204 CD 44 03 CMP VALUE ; VALUE COMPARISON BPL MORE	89995 9999 ;
00203 C209 4C 46 C1 JMP UNPLOT 00204 C20C 4C 43 C1 MORE JMP PLOT	00007 0000 BEFORE USING
00205 C20F ; ***********************************	00008 0000 3 00009 0000 DRIGIN = \$C26F
00207 C20F 00208 C20F ; PLOT A POINT WEIGHTED BY SHADING SCHEME	00010 0000 RAM = 1034A 00011 0000
00209 C20F ; AND SHADE VALUE 00210 C20F ; CHECK 'NDSCAL FLAG FOR SCALING OF Y COORD	00012 0000 XPLT = \$033F
00211 C20F ; CHECK NUSCAL FLAG FOR SCALING OF Y COORD 00212 C20F ; CHECK 'HTORRN' FLAG FOR TYPE OF SHADING	00013 0000 YPLT = \$0341 00014 0000 NDRH = \$1224 00015 0000 NDSCAL = \$0347
00213 C20F AD 47 03 PLTSHD LDA NDSCAL	00016 0000 PLOT = \$C143
	UNPLOT = \$C146 (Continued on page 66)

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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Three

00019	8000 8000		; MLF'CND * \$AC		C2EB		OUTLN	JSR FINDXY	; ENTRY POINT TO
88828 88821	8888		MLPLER = \$AD PROD = \$AE	00148				LDA XMIN STA XPLT	; OUTLINE FACETS
88822 88823	0000 0000		MULT = #CØ11	00150	C2F4			LDA XMIN+1 STA XPLT+1	
00024	0000		DVDND = \$FD	00152	C2FA	AD 4C 03		LDA YMIN	
00025 00026	2002 2002		DVSDR = \$FB QUOT = \$FD	00153 00154	C2FD			STA YPLT LDA DLTAX1+1	: CHECK FOR DX>DY
00027 00028	8888 8888		DIVIDE = #C025	00155	C303			BNE STEPX SEC	,
80029	8666		**RAM	00157 00158	C309	AD 56 03		LDA DLTAX1	
00030 00031	034A		XMIN *=*+2	00159	C30C	BØ 74		BCS STEPX	
00032 00033	034C		YMIN #=#+1 XMID #=#+2	00160			STEPY	LDA DLTAY1 STA ERROR	
00034 00035	Ø34F		YMID *=*+1	00162 00163	C314	8D 68 03		STA COUNT	
00036	0350 0352		XMAX *=+2 YMAX *=+1	00164	C31A	38		LSR ERROR SEC	
00037 00038			YTOP ***+1 YBOT *=*+1	00165 00166				SBC ERROR	
00039 00040	0.355		YBASE *=*+1	00167	C321	8D 65 03		STA ERROR	
00041	8328		DLTAX1 #=#+2 DLTAX2 #=#+1	00169	C327	E9 00		LDA DLTAX1+1	
00042 00043	0359 035A		DLTAX3 *=*+1 DELTAX *=*+1	00170 00171	C329			STA ERROR+1	
00044 00045			DLTAY1 *=*+1	00172	C32F	AD 67 03	LNLP1	LDA MODE	; Ø = DRAW, 1 = ERASE
00046	Ø350		DLTAY2 #=#+1 DLTAY3 #=#+1	90173 90174				BNE ERASE1 JSR PLOT	
00047 00048	035E		DELTAY *=x+1 XDIFF *=x+1	00175	C337	4C 3D C3		JMP SK1	
00049 00050	0290		FLAG1 *=*+1	00176 00177	C33D	AD 60 03	SK1	LDA FLAG1	; 0 = POSITIVE SLOPE
00051	Ø361		FLAG2 *=*+1 FLAG3 *=*+1	00178				BNE NSLOPE INC YPLT	
99952 99953	Ø363 Ø364		FLAG *=+1 EDGES *=+1	89189	C345	DØ Ø3		BNE SK2	; ALWAYS BRANCH
99954 99955	0365		ERROR *=*+2	00181	C34A	20 66 03	SK2	BIT ERROR+1	
00056	Ø367 Ø368		MODE *=*+1 COUNT *=*+2	00183 00184				BMI SK3 INC XPLT	
00057 00058	036A		!	99185 99186		DØ Ø3		BNE NOINC1	
00059	036A		#=DRIGIN	00187	C357	38	NOINCE	INC XPLT+1	
88848 88848	C26F		3. 按注查查查查查查查查查证证证证证证证证证证证证证证证证证证证证证证证证证证证证	00189	C358			LDA ERROR SBC DLTAY1	
88863 88863	C26F		1	00190	C22E	8D 65 03		STA ERROR	
00064	C26F		; SCALE ALL Y COORDINATES FROM 0239 ; PSUEDO-COORDINATE RANGE TO 0199	00191 00192	C364			LDA ERROR+1 SBC #0	
00065 00066	C26F		TRUE SCREEN COORDINATE RANGE	00193	C366	8D 66 03	OL/T	STA ERROR+1	
90067 90068	C26F	AØ Ø6	SCALE LDY #6	00194	C369		SK3	CLC LDA ERROR	
00069	C271	A9 D5 85 AC	LDA ##D5 STA MLPCND	00196	C36D C37Ø			ADC DLTAX1	
00070	C275	89 4C Ø3 85 AD	SCLP LDA YMIN,Y	00198	C373	AD 66 03		STA ERROR LDA ERROR+1	
00072	C27A	20 11 00	STA MLPLER JSR MULT	00199	C376			ADC DLTAX1+1 STA ERROR+1	
00073 00074	C27D	99 4C Ø3	STA YMIN,Y	00201	C37C	CE 68 03		DEC COUNT	
00075 00076	C281	88	DEY	80202 80203	C37F C381	DØ AE 60		BNE LNLP1	
00077	C283	88 10 F0	DEY BPL SCLP	80204 80205	C382		ETERY	LDA DLTAX1	
00078 00079	C285	60	RTS	00206	C385	80 65 03	SIEPX	STA ERROR	
000000	C286		;	00207 00208	C288	AD 57 03		STA COUNT LDA DLTAX1+1	
00081 00082	C286		; *************************************	00209	C38E	BD 66 03		STA ERROR+1	
00083 00084	C286 C286		; EXCHANGE 'MIN' AND 'MID' COORDINATES	00211	C394	4E 66 03		STA COUNT+1 LSR ERROR+1	
00005	C286	AØ Ø2	SWAP12 LDY #2	00212	C397			ROR ERROR SEC	
00086 00087	C288	89 4A 03	LOOP1 LDA XMIN,Y	00214	C39B	AD 58 03		LDA DLTAY1	
00000	C28C C28F	89 4D Ø3	LDA XMID,Y	00215 00216	C39E			SBC ERROR STA ERROR	
80099	C292	99 4A Ø3	STA XMIN,Y PLA	00217	C3A4	A9 00 ED 66 03		SBC ERROR+1	
00091	C293	99 4D Ø3	STA XMID,Y	00219	C3A9	8D 66 03		STA ERROR+1	
88893 88894	C297	10 EF	BPL LOOP1	00220 00221	C3AC C3AF	AD 67 03 D0 06	LNLP2	LDA MODE BNE ERASE2	
00095	C29A	60	RTS	00222 00223	C3B1 C3B4	20 43 C1		JSR PLOT	
88896 88897	C29A C29A		*************	00224	C3B7	4C BA C3 20 46 C1	ERASE2	JMP SKP1 JSR UNPLOT	
0009B	C29A		EXCHANGE 'MID' AND 'MAX' COORDINATES	00225 00226	C3BD	EE 3F 03	SKP1	INC XPLT BNE NOINC2	
00100	C29A C29A	AØ 02	SWAP23 LDY #2	00227 00228	C3BF C3C2	EE 40 03		INC XPLT+1	
00101	C29C	89 4D 63	LOOP2 LDA XMID,Y	00229	C3C5	2C 66 03 30 20	NU1NU2	BIT ERROR+1 BMI Skp3	
00103	C2A0	89 50 03	LDA XMAX, Y.	00230	C3C7	AD 60 03 D0 05		BNE NGSLP	
00104	C2A3	99 4D Ø3	STA XMID,Y	00232	C3CC	EE 41 03		INC YPLT	
00106	C2A7 C2AA	99 50 03 88	STA XMAX,Y	00233 00234	C3CF C3D1	DØ Ø3 CE 41 Ø3	NGSLP	BNE SKP2 DEC YPLT	; ALWAYS BRANCH
80108	C2AB	10 EF	BPL LOOP2	00235 00236	C3D4 C3D5	38 AD 65 Ø3	SKP2	SEC LDA ERROR	
00109	C2AD C2AE	60	RTS	00237	C3D8	ED 56 03		SBC DLTAX1	
00111	CZAE CZAE		*************	00238 00239	C3DE	BD 65 03 AD 66 03		STA ERROR LDA ERROR+1	
00113	CZAE		; SORT COORDINATES ACCORDING TO X COMPONENTS	00240 00241	C3E1	ED 57 03 BD 66 03		SBC DLTAX1+1 STA ERROR+1	
00114	C2AE	A2 Ø2	SORTX LDX #2	00242	C3E7	18	SKP3	CLC	
90116 90117	C280 C281	38 AD 4D Ø3	SORTLP SEC	00243 00244	C3EB	AD 65 Ø3		ADC DLTAY1	
00118	C2B4	ED 4A Ø3	LDA XMID SBC XMIN	00245 00246	C3EE	8D 65 03 AD 66 03		STA ERROR	
00119	C287 C28A	AD 4E 03 ED 4B 03	LDA XMID+1 SBC XMIN+1	00247	C3F4	69 00		ADC #0	
00121	C2BD	BØ Ø3	BCS NOSWF1	ØØ248 ØØ249	C3F6 C3F9	38 66 03		STA ERROR+1 SEC	
00123	C2BF C2C2	20 86 C2 CA	JSR SWAP12 NOSWP1 DEX	00250 00251	C3FA	AD 68 03		LDA COUNT	
00124	C2C2	FØ 15 38	BEQ SORTED SEC	00252	C3FD C3FF	BD 68 03		SEC #1 STA COUNT	
00126 00127	C2C6 C2C9	AD 50 03	LDA XMAX	00253 00254	C402 C404	DØ 03		BCS TEST DEC COUNT+1	
00128	CZCC	ED 4D 03 AD 51 03	SBC XMID LDA XMAX+1	00255 00256	C407	2C 69 Ø3	TEST	BIT COUNT+1	
00129	C2CF C2D2	ED 4E 03 B0 DC	SBC XMID+1 BCS SORTLP	00257	C40C	10 A0		BPL LNLP2 RTS	
00131	C2D4	20 9A C2	JSR SWAP23	00259	C40D		;	***********	*****
00133	C2D7 C2DA	4C BØ C2	JMP SORTLP SORTED RTS	89268	C40D		; DRAW	A SHADED VERTI	CAL LINE AT
00134 00135	C2DB		} }***********************************	00261 00262	C4ØD		; XPLT	FROM YTOP TO Y	BOT
00136 00137	C2DB		I and the second	00263	C4ØD C4ØE	38 AD 53 03	VLINE	SEC LDA YTOP	; MAKE SURE YTOP>YBOT
00138	C2DB		; DRAW A LINE BETWEEN XMIN, YMIN AND XMID, YMID ; USING FAST DDA (DIGITAL DIFFERENTIAL ANALYZER)	00265 00266	C411	ED 54 03		SBC YBOT	
00139	C2DB C2DB		; TECHNIQUE	00267	C416	AD 53 03		DCS DRAW LDA YTOP	
00141	C2DB	A9 02	LINE LDA #2 ; ENSURE XMAX IS	00268 00269	C419 C41A	48 AD 54 Ø3		PHA LDA YEOT	
00143	C2EØ	8D 51 03 20 AE C2	STA XMAX+1 ; LARGEST BEFORE JSR SORIX ; ORDERING 'MIN' AND 'MID'	00270	C41D	8D 53 Ø3		STA YTOP	
00144	CZE6	AD 47 Ø3 FØ Ø3	LDA NOSCAL BEQ OUTLN	00271 00272	C421	68 8D 54 @3		PLA STA YBOT	
20146		20 6F C2	JSR SCALE						10
									(Continued on pag

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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Three

00273	C424	AD 53 03	DRAW	LDA YTOP	
00274	C427	BD 41 Ø3		STA YPLT	
00275				JSR NORM	; PLOT A SHADE-WEIGHTED
00276				LDA YTOP	; PIXEL CHECKING ONLY
00278	C430			DEC DONE	FOR SHADE STYLE
00279	C435	CE 53 Ø3		DEC YTOP	
00280				JMP DRAW	
00281	C43B	60	DONE	RTS	
00283			; ****	***********	*****
00284			;		
00285 00286			; FIN	ENDPOINTS FOR	VERTICAL LINES
00287	C43C		; BEII	WEEN FACET EDGES	
00288		AD 5F 03	ENDET!	LDA XDIFF	
00289		85 AC		STA MLPCND	
00290				LDA DELTAY	
00271		20 11 00		STA MLPLER JSR MULT	
00293				STA DVDND+1	
00294	C44B			LDA PROD	
00295 00296				STA DVDND	
00297	C451			STA DVSOR+1	
00298	C453	AD 5A 03		LDA DELTAX	
00299				STA DVSOR	
00300		20 25 C0 AD 63 03		JSR DIVIDE LDA FLAG	
00302	C45E			BNE NEGSLP	
86363	C460	18		CLC	
00304 00305	C461 C464			LDA YBASE	
00306	C466			ADC QUOT BCC SKIP2	
00307	C468		NEGSLE	SEC	
00308	C469			LDA YBASE	
00310	C46E	E5 FD .	SKIP2	SBC QUOT RTS	
00311	C46F		;		
00312	C46F		; ****	*******	****
00313	C46F		; FIND	COORDINATE DIF	FERENCES
00315	C46F				
00316	C46F		; ALL	"DELTA X" VALUE	POSITIVE,
00318	C46F		SINE	LE PRECISION (J	JST LOWER BYTE)
00319	C46F	38	FINDXY	SEC	
00320	C470	AD 4D 03		LDA XMID	
00321	C473	ED 4A 03 BD 56 03		SBC XMIN STA DLTAX1	
00323	C479	AD 4E 03		LDA XMID+1	
00324		ED 4B Ø3		SBC XMIN+1	
00325	C47F	8D 57 Ø3 38		STA DLTAX1+1 SEC	
00327	C483	AD 50 03		LDA XMAX	
00328 00329	C486	ED 4D 03 8D 58 03		SBC XMID	
00330	C489	38 80 28 63		STA DLTAX2	
00331	C48D	AD 50 03		LDA XMAX	
00333	C498	ED 4A 03 8D 59 03		SBC XMIN	
00334	C496	00 37 03	i	STA DLTAX3	
00335	C496		, USE	ABS (DELTA Y) VA	UES,
00337	C496		; FLAG	S INDICATE SLOP	OF LIMIT LINES
00338	C496		,	LDA ##00	
88339 88348	C498	BD 60 03		STA FLAGI	
00341	C49E	8D 62 Ø3		STA FLAG2 STA FLAG3	
00342 00343	C4A1	38		SEC	
00344	C4A2	AD 4F 03 ED 4C 03		LDA YMID SBC YMIN	
00345	C4A8			BCS STORE1	
00346	C4AA	EE 60 03		INC FLAG1	
00348	C4AD C4BØ	AD 4C 03 ED 4F 03		LDA YMIN SBC YMID	
00349	C4B3	8D 5B 03	STORE 1	STA DLTAY1	
99359 99351	C486	38		SEC	
00352	C4B7	AD 52 03 ED 4F 03		LDA YMAX SBC YMID	
00353	C4BD	BØ Ø9		BCS STORE2	
00354	C4BF C4C2	AD 4F 03		INC FLAG2	
00356	C4C5	ED 52 03		LDA YMID SBC YMAX	
00357	C4C8	8D 5C 03	STORE2	STA DLTAY2	
00358	C4CB	38 AD 52 Ø3		SEC	
00360	C4CF	ED 4C 03		LDA YMAX SBC YMIN	
00361	C4D2 C4D4	BØ 09		BCS STORES	
00393 00393	C4D4	EE 62 03 AD 4C 03		INC FLAGS	
00364	C4DA	ED 52 03		SBC YMAX	
00365 00366	C4DD C4EØ	8D 5D 03	STORE3	STA DLTAY3	
00367	C4E1	00		RTS	
00368	C4E1		; ****	**********	****
00369	C4E1		\$		
00371	C4E1) DAMW	A SHADED TRIANG	ULAR FACET
00372	C4E1	20 AE C2	FACET	JSR SURTX	
00374	C4E4 C4E7	AD 47 03 F0 03		LDA NOSCAL BEQ YSOK	
00375	C4E9	20 6F C2		JSR SCALE	
00376	C4EC	20 6F C4	YSOK	JSR FINDXY	
00377	C4EF C4F2	AD 4A 03 BD 3F 03		LDA XMIN	
00379	C4F5	AD 4B 03		LDA XMIN+1	
00380	C4FB	BD 40 03	F0F-	STA XPLT+1	
90382	C4FB C4FC	38 AD 3F 03	FCETLP	SEC LDA XPLT	
00383	C4FF	ED 4A Ø3		SBC XMIN	
00384 00385	C502 C505	8D 5F 03		STA XDIFF	
00386	C509	FØ 53		LDA DLTAX1 BEQ CONT	
00387 00388	C50A	BD 5A Ø3		STA DELTAX	
00389	C50D C510	AD 58 03 8D 5E 03		LDA DLTAY1 STA DELTAY	
00390	C513	AD 60 03		LDA FLAG1	
00391	C516 C519	8D 63 03 AD 4C 03		STA FLAG	
00393	C51C	AD 4C Ø3 BD 55 Ø3		LDA YMIN STA YBASE	
00394	C51F	20 3C C4		JSR ENDPTS	
00395 00396	C522 C525	AD 59 Ø3		STA YTOP LDA DLTAX3	
00397	C528	FØ 33		BEQ CONT	
00398 00399	C52A C52D	BD 5A Ø3 AD 5D Ø3		STA DELTAX	
		8D 2E 02		LDA DLTAY3 STA DELTAY	
				Date (M)	

```
LDA FLAG3
STA FLAG
JSR ENDPTS
STA YBOT
JSR VLINE
LDA XPLT+1
END ENEXTX1
LDA XPLT
END STA YBOT
NEXTX1
INC XPLT+1
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SEC CHIP
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20 3C C4
8D 53 CC 44
8D 54 03
20 0D C4
AD 4E 03
AD 3F 03
C6 0B
AD 3F 03
AD 5F 03
AD 
                   SYMBOL TABLE
```

SYMBOL VA	ALUE						
CONT	C55D	COUNT	0298	DELTAX	Ø35A	DELTAY	Ø35E
DIVIDE	CØ25	DLTAX1	0356	DLTAX2	0358	DLTAX3	0359
DLTAY1	0359	DLTAY2	Ø35C	DLTAY3	035D	DONE	C43B
DRAW	C424	DVDND	ØØFD.	DVSOR	ØØFB	EDGES	0364
ENDETS	C43C	ERASE1	C33A	ERASE2	C3B7	ERROR	0365
FACET	C4E1	FCETLP	C4FB	FINDXY	C46F	FINI	CSCF
FINISH	C5E9	FLAG	0363	FLAG1	0360	FLAG2	0361
FLAG3	0362	LINE	C2DB	LNLP1	C32F	LNLP2	C3AC
LOOP1	C288	LDDP2	C29C	MLPCND	ØØAC	MLPLER	ØØAD
MODE	0367	MULT	CØ11	NEGSLP	C468	NEXTX1	C552
NEXTX2	C5C4	NGSLP	C3D1	NOINC1	C357	NOINCZ	C3C2
NORM	C2Z4	NOSCAL	0347	NOSWP1	C2C2	NSLDPE	C347
ORIGIN	C26F	DUTLN	C2EB	PLOT	C143	PROD	ØØAE
QUOT	MOFD	RAM	Ø34A	SCALE	C26F	SCLP	C275
SK1	C33D	SK2	C34A	SK3	C369	SKIP2	C46E
SKIP3	C55A	SKIP4	C5CC	SKP1	C3BA	SKP2	C3D4
SKP3	C3E7	SORTED	C2DA	SORTLP	C28Ø	SORTX	C2AE
STEPX	C382	STEPY	C30E	STORE 1	C4B3	STORE2	C4CB
STORE3	C4DD	SWAP12	C286	SWAP23	C29A	TEST	C407
SYMBOL 1	ABLE						

SYMBOL VA	LUE						
UNPLOT XMID YBOT YPLT	C146 034D 0354 0341	VLINE XMIN YMAX YSOK	C40D 034A 0352 C4EC	XDIFF XPLT YMID YTOP	035F 033F 034F 0353	XMAX YBASE YMIN	0350 0355 0340

END OF ASSEMBLY

End Listing Three

Listing Four

00001	8888	; PRIMITIVE SOLID SHAPE DRAWING
00002	8888	i
88883	8888	RICHARD L. RYLANDER 11/7/84
00004	ଉପରର	
00005	ଉଉପର	; LOAD ARITHMETIC AND GRAPHIC UTILITIES FIRST
000006	ଉଉଉଡ	I STATE OF THE CONTENT OF THE
20007	0000	*******
88888	2022	RAM=#036A
00009	2000	ORIGIN=#C5EA
00010	0000	1
00011	0000	MLPCND=\$AC : MULTIPLICAND (S)
00012	0000	MLPLER=#AD ; MULTIPLIER (S)
80013	8000	PROD=4AE PRODUCT (D)
00014	9999	MULT=#C011 : CALL FOR MULTIPLY
00015	ଉପରର	to the total the
00016	0000	DVDND≃\$FD : DIVIDEND (D)
		,

(Continued on page 70)

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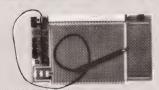


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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Four

## 1982 1982	00017 00016 00019	0000		DVSOR=#FB QUOT=#FD DIVIDE=#C@25	; DIVISOR (D) ; QUOTIENT (D) ; CALL FOR DIVIDE	00146 00147 00148	C628		CHO	CLUP'		ND -XREL:REM LEFT HE FU THEN GOTO 'DH	
SANCE SANCE SANCE SANCE SERVICE SANCE	00021 00022	0000 0000		SQR=#AE	; SQUARE OF ARG (D)	00150 00151	C628 C628		3		TONE=ZWX+YS GOSUB 'GETV YPLT=YCENT	SHD /AL':REM NORMALIZ YREL	E SHADE VAL
AND PROPERTY PRO	00025 00026 00027	9000 9000		ROOT=#033C	; RADICAND (D) ; SQUARE RODT (S)	00153 00154 00155	C628 C628 C628		3 DHE	EMI'	REM POINT IF YREL>CLI TONE=ZWX-YS	'S WEIGHTED BY SH PD THEN GOTO 'RII SHD	IADE VALUE
The College	00029 00030	0000		; RNDM=#C000 RANDOM=#C0C8	; RANDOM NUMBER : CALL FOR RANDOM	00157 00158	C628		; ; ; 'RHE	MI,	YPLT≃YCENT- GOSUB 'PLTS	YREL SHD	
Wilson W	00033 00033	9999		; IN THE ACCUMULATOR	NDOM' LEAVES A RANDOM BYTE	00161	C628				HEMI=0 IF XREL>CL1	PR THEN RETURN	
WARD STATE Fine STATE	00035 00036	0000		YPLT=\$0341 NORM=\$C224		00164	C628		;		XPLT=XCENT+ GOSUB 'CHCL	XREL	
Section Color Co	00039 00039	9000 9000		VALUE=#0344 HTDRRN=#0346	; SHADE FLAG, 1=HALFTONE	00167	C628		PTPLOT		HVFLAG		
Sect Might	00041	9999 9999		*=RAM		00170 00171	C630	48		PHA			
\$200 \$1.00	00044	036D		XREL *=*+1 XSHD *=*+2	; RELATIVE (TO CENTER) ; USED IN SHADE CALC	00173 00174	C635	9D 9C 63		STA			
2	00047 00048	0370 0371		YREL *=*+1 YSHD *=*+2	; RELATIVE (TO CENTER) ; USED IN SHADE CALC	00176 00177	C63C	AD 6D 03 48		LDA			
1000 171	00051	0375 0377		ZWX *=x+2	3 Z WITH X (+ DR -)	00179 00180	C641 C644	AD 71 03 8D 6D 03		LDA			
\$27.2 CLIP	00054	0378		TNTMP *=*+2	; USED IN SHADE CALC	00182 00183	C648	8D 71 03 AD 6E 03		STA			
March Carl	00057 00058	037E		CLIPR *=*+1	; RIGHT CLIPPING HOUND	00185 00186	C64F C650	48 AD 72 03		PHA LDA			
MARCH 1987 1 1 1 1 1 1 1 1 1	00061 00061	Ø381		;	; DOWN CLIFFING BOUND	00188 00189	C656 C657	68 8D 72 Ø3	NOPAT	FLA	YSHD+1		
Section	00063 00064	Ø382 Ø383		HVFLAG ***+1	: HORIZONTAL/VERTICAL FLAG	00191 00192	C65D C65F	A9 01 80 81 03		STA	##Ø1		
Section Sect	00065 00067	Ø386 Ø387		ENTX *=*+1 ENTY *=*+1	LOOP COUNTER LOOP COUNTER	00194 00195	C666	AD 7D 03 CD 6C 03		LDA	XREL	; CHECK LEFT HE	MISPHERE
1	00069 00070	Ø389 Ø389		; HLEN *=x+1	; HALF LENGTH OF CYLINDERS	00197 00198	C66B	AD 3C 03		SEC	ROOT		
Section Sect	00072 00073	038D		RT *=*+1 fsC *=*+1	; TORDID (RING) RADIUS ; CENTER RADIUS DE TORDID	00200 00201	C672 C675	AD 3D 03		STA	ZWX ROOT+1		
Section Sect	00075 00076	038F 0390		RI *=*+1 XSOR *=*+2		00204	C678	8D 76 03		SEC	ZWX+1		
0.000 0.00	00078 00079	Ø393 Ø393		; RØ=HLEN		00207	C682 C685	8D 3F 03		SEC	XREL XPLT		
DIVIDE WITH STRINGE FRECUSION DIVIDOR	00081 00082	0393 C5EA		*=ORIGIN	******	00210	CABD			SBC	##00		
Section Sect	00084 00085	C5EA C5EA				00213 00214	C691	AD 7F 83	CHCLUP	LDA		; CHECK FOR UP I	CLIPPING
Second Corp.	00088	C5EA C5EC	85 FC	STA DVSOR+1		00216 00217	C699 C69A	10 AD 75 Ø3		CLC	ZWX		
	00091	CSF 1		; ; *****************	************	00219 00220	C6A3	8D 79 03 AD 76 03		STA	TONE ZWX+1		
0.00 0.00	00094 00095	C5F1		; MULTIFLYING 'TONE' BY	26 THEN	00222 00223	C6AC	8D 7A Ø3 2Ø F1 C5		STA JSR	TONE+1		
Defending CSF Defending	00097 00098	C5F1	10 12	BFL CHTNU	; IF 'TONE' < 0, THEN	00225 00226	C683	AD 6F 03 6D 70 03		LDA ADC	YREL		
March Marc	00100	C5F9 C5FB	DØ 04 BD 44 03	DNE NEGATE STA VALUE		00228 00229	C6BC	20 0F C2	;	JSR			
00110 C608 AD 79 03 STA TONE 00110 C608 AD 79 03 STA TONE 00110 C608 BD 64 C 00110 C608 BD 79 03 STA TONE 001110 C608 BD 79 03 STA TONE 001110 C608 BD 79 03 STA TONE 001111 C611 C608 BD 79 03 STA TONE 001111 C611 C608 BD 79 03 STA TONE 00111 C611 C609 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD 79 BD 79 03 STA TONE 00111 C611 BD 79 BD	00103	C5FF C600	38 A9 00	NEGATE SEC LDA ##00		00231 00232	C460	AD 80 03 CD 70 03	DHENT	LDA	YREL	; CHECK FOR DOWN	CLIPPING
98190 C680 A9 1A	00106 00107	C605	8D 79 03 AD 79 03	CNTNU LDA TONE		00234 00235	C6C5	38 AD 75 Ø3		SEC	ZWX		
801-12 Col-14 85 FE	00109	C60D C60F	A9 1A 85 AD	LDA ##1A STA MLFLER		00238	CACF	BD 79 03 AD 76 03		LDA	TONE. ZWX+1		
00115 C610 85 FB STA DVSOR 00116 C610 85 FB STA DVSOR 00117 C61F 20 EA C5 JSR SDIV 00119 C62F 8D 40 40 3 SEC YPEL 00119 C62F 8D 41 05 JSR SDIV 00119 C62F 8D 51A DVSOR 00110 C62F 8D 51A DVSOR 00111 C62F 8D 51A DVSOR 00111 C62F 8D 51A DVSOR 00112 C62B 1 POINT PLOTTING BY QUADRANTS USING 00112 C62B 1 POINT	00112	C614 C616	85 FE A5 AE	STA DVDND+1 LDA PROD		00241	C6D8	8D 7A 03 20 F1 C5		STA JSR	TONE+1		
00119 C624 80 44 03 STA VALUE 00120 C627 60 04 03 STA VALUE 001210 C628 0 1 STA VALUE 00121 C628 0 1 STA VALUE 00122 C628 0 1 STA VALUE 00122 C628 0 1 STA VALUE 00123 C628 0 1 STA VALUE 00124 C628 0 1 STA VALUE 00124 C628 0 1 STA VALUE 00125 C628 0 1 STA VALUE 00126 C628 0 1 STA VALUE 00127 C628 0 1 STA VALUE 00128 C628 0 1 STA VALUE 00129 C628	00115	C61A C61D	AD 77 03 95 FB	LDA RADIUS STA DVSOR		00244 00245	C6DF C6E2	ED 70 03 8D 41 03		SEC	YREL YPLT		
90121 C428	00119	C622 C624	A5 FD BD 44 Ø3	STA VALUE		00247 00248	C9E8	AD 81 03	RHEMI	LDA I	HEMI		
Point Plotting By Quadrants Using Point Plotting By Quadrants Point Plot	00122	C628		1		00250 00251	C6ED C6FØ	28 CE 81 62		DEC	IEMI		
	00125 00126	C628 C628		; THE FOUR-FOLD SYMMETR	Y OF SIMPLE OBJECTS	00253 00254	C6F4 C6F7	CD 6C 03 90 28		BCC F	KREL.	; CHECK FOR RIGH	IT CLIPPING
00131 C620 1 OF AN OBJECT 1 08 VERTION VERSIONS 00259 C703 AD 30 03 LDA ROOT+1 00132 C620 1 THE FOLDWING IS A 'BASIC SUBROUTINE' 00260 C706 6D 6E 03 ADC XSHD>1 00134 C620 1 THE FOLDWING IS A 'BASIC SUBROUTINE' 00262 C706 IB CLC 00134 C620 1 C102 C102 C102 C102 C102 C102 C102	00128 00129	C628		; X AND Y COORDINATES T ; SINGLE SHAPE ROUTINE	O ROTATE OBJECTS 90 DEG	00256 00257	C6FA C6FD	9D 9D 03		ADC)	SHD		
00134 C628 EDUIVALENT TO EXPLAIN ITS UPERATION 00262 C70C 18 CLC 00135 C628 NOTE THAT LABELS ARE USED IN PLACE OF 00264 C710 6D 6C 83 ADC XREL 00137 C628 LINE NUMBERS 00265 C713 8D 3F 83 STA XPLT 00139 C629 FTPLOT IF HVFLAG(0 THEN GOID 'NOROT' 00266 C713 8D 3F 83 STA XPLT 00140 C628 STACK) XSED XSECLIVEL (STACK) 00266 C714 AD 68 83 LDA XCENT+1 00144 C628 STACK) XSED XSECLIVEL (STACK) 00266 C716 AD 68 83 LDA XCENT+1 00145 C628 NOROT' GOUD STHEN YSED XSED XSECLIVEL (STACK) 00266 C718 BD 40 83 STA XPLT+1 00145 C628 NOROT' GOUD STHEN YSED XSECLIVEL (STACK) 00269 C712 4C 90 C6 MPC CHECLUP 00146 C628 RECALCULATE 2*Z FROM X,Y AND RADIUS 00147 C628 RECALCULATE 2*Z FROM X,Y AND RADIUS 00148 C628 RECALCULATE 2*Z FROM X,Y AND RADIUS 00149 C628 RECALCULATE 2*Z FROM X,Y AND RADIUS 00149 C629 RECALCULATE 2*Z FROM X,Y AND RADIUS 00149 RECALCULATE 2*Z FROM X,Y AND RADIUS 001	00131 00132 00133	C958		; OF AN OBJECT ; ; THE FOLOWING IS A 'BA	SIC SUBROUTINE	ØØ260 ØØ261	C703 C706 C709	AD 3D 03 6D 6E 03 BD 76 03		ADC)	ROOT+1 (SHD+1		
00139 C628	00135	C628 C628 C628		; EDUIVALENT TO EXPLAIN	ITS OPERATION	00263 00264	C70D C710	AD 6A 03		LDA A	(CENT		
00141 C628 (STACK)=XREL:XREL:YREL:YREL:(STACK) 00269 C71E 4C 90 C6	0013B	C628 C628		; LINE NUMBERS ; 'PTPLOT' IF HVFLAG(0)	THEN GOID 'NOROT'	00266 00267	C716 C719	69 00 03 69 00		ADC #	CENT+1		
00144 C628 ; HEMI = 1 00145 C628 ; IF XREL>CLIPL THEN GOTO 'RHEMI'	00141	C628 C628		(STACK) = XREL (STACK) = XSHD (STACK) = XSHD (STACK) = XREL	: XREL=YREL: YREL=(STACK) : XSHD=YSHD: YSHD=(STACK)	00269 00270	C71E C721	4C 90 C6 2C 83 03		JMP C	HELUP		
	00144	C628		; HEMI = 1						art. P	NOIR	(Continued	on nac- 73

(Continued on page 72)

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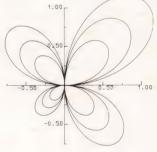
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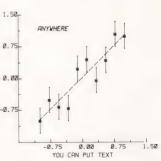
*Specify Compiler (Mfg. and Version): IBM/MS/IBM Prof. FORTRAN †Specify Plotter: HP/HI/IBM

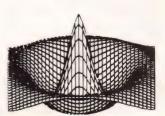
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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Four

00272	C726	AD 6E 03	LDA XSHD+1 ; RESTORE COORDS	00401 C80D 8D 6C 03	STA XREL
00273 00274	C729 C72C	8D 72 Ø3 6B	STA YSHD+1 PLA	00402 CB10 BD 6D 03 00403 CB13 20 04 C0	STA XSHD JSR SQUARE
00275 00276	C730	AD 6D 03	STA XSHD+1 LDA XSHD	00404 C816 38 00405 C817 AD 84 03	SEC LDA TEMP
00277 00278	C736	8D 71 Ø3 6B	STA YSHD PLA	00406 C81A E5 AE 00407 CB1C 85 AC	SBC SQR STA RADCND
00279 00280	C73A	AD 9C 03	STA XSHD LDA XREL	00409 C81E AD 85 03 00409 C821 E5 AF	LDA TEMP+1 SBC SQR+1
00281 00282	C740	8D 70 03 6B	STA YREL PLA	00410 C823 85 AD 00411 C825 20 64 C0	STA RADCND+1 JSR SQRT
00283 00284	C744	PD PC 02	STA XREL NORSTR RTS	00412 C828 AD 3C 03 00413 C828 8D 88 03	LDA ROOT STA MAX
00285 00286	C745		; ; ***********************************	00414 C82E AD 87 03 00415 C831 BD 70 03	LOOPY LDA CNTY STA YREL
00287 00288	C745		; CALCULATE Z FROM LOCAL X,Y BY	00416 CB34 BD 71 03 00417 CB37 A9 00	STA YSHD
00289 00290	C745		; PYTHAGOREAN SUM	00418 C839 8D 83 03 00419 C83C 20 28 C6	STA HVFLAG JSR PTPLOT
00291 00292	C748	AD 77 03 85 AC	GETZ LDA RADIUS STA ARG	00420 CB3F A9 B0 00421 CB41 BD B3 03	LDA ##8Ø STA HVFLAG
00293 00294	C74D	20 04 C0 8D 7C 03	JSR SQUARE STA TNTMP+1	00422 C844 20 28 C6 00423 C847 AD 87 03	JSR PTPLOT LDA CNTY
00295 00296	C752	A5 AE BD 7B Ø3	LDA SOR STA TNTMP	00424 C84A CD 88 03 00425 C84D F0 06	CMP MAX BEQ DONEY
00297 00298	C758	AD 6D 03	LDA XSHD STA ARG	00426 C84F EE 87 03 00427 C852 4C 2E C8	INC CNTY JMP LODPY
00299 00300	C75D	20 04 C0 38	JSR SQUARE SEC	00428 C855 AD 86 03 00429 C858 CD 92 03	DONEY LDA CNTX CMP XMAX
00301 00302	C761	AD 78 03 E5 AE	LDA TNTMP SBC SQR	00430 C85B F0 06 00431 C85D EE 86 03	BEQ DONE 1NC CNTX
00303 00304	C766	8D 7B Ø3 AD 7C Ø3	STA TNTMP LDA TNTMP+1	00432 C860 4C 05 C8 00433 C863 60	JMP LOOPX DONE RTS
00305 00306	C76B	E5 AF BD 7C Ø3	SBC SQR+1 STA TNTMP+1	00434 C864 00435 C864	; **************
00307 00308	C771	AD 71 03 85 AC	LDA YSHD STA ARG	00436 CB64 00437 CB64	; DRAW SHADED CYLINDERS
00310 00310	C776	20 04 C0 38	JSR SQUARE SEC	0043B C864 00439 C864	; 'BASIC SUBROUTINE' EQUIVALENT
00311	C77A	AD 7B 03 E5 AE	LDA TNTMP SBC SQR	00440 C864 00441 C864	3 'CYLNDR' XSHD≖Ø
00313 00314 00315	C77E	85 AC AD 7C Ø3	STA RADOND LDA TNTMP+1	00442 C864 00443 C864	; FOR YREL=RADIUS TO 0 ; YSHD=YREL
90316 90317	C783	E5 AF 85 AD 30 ØA	SHC SOR+1 STA RADCND+1 BMI ZERODT	00444 C864 00445 C864	FOR XREL=HLEN TO 0 GOSUB 'PTPLOT'
00318	C787	20 64 CØ ØE 3C Ø3	JSR SØRT ASL ROOT	00446 C864 00447 C864	NEXT XREL NEXT YREL
00320 00321	C78D	2E 3D 03	ROL ROOT+1	00448 C864 00449 C864	RETURN
00322 00323	C791	A9 00 BD 3C 03	ZERDOT LDA ##ØØ STA ROOT	00450 C864 A9 00 00451 C866 BD 6D 03 00452 C869 BD 6E 03	CYLNDR LDA #Ø STA XSHD STA XSHD+1
00324 00325	C796	8D 3D Ø3	STA ROOT+1	00453 C86C 8D 72 03 00454 C86F AD 77 03	STA YSHD+1 LDA RADIUS
00326 00327	C79A		; ; ***********************************	00455 C872 BD 70 03 00456 C875 AD 89 03	STA YREL CYLOOP LDA HLEN
00328 00329	C79A		SET UP PARAMETERS FOR TOROIDS	00457 C878 8D 6C 03 00458 C878 AD 70 03	STA XREL LDA YREL
00330 00331	C79A C79A		; RT=(RO-RI)/2 RS=RT*RT RC=RT+RI	00459 C87E 8D 71 03 00460 C881 20 28 C6	STA YSHD CXLOOP JSK PTPLOT
00332 00333	C79A	AD 8E 03	TPARM LDA RO	00461 C884 CE 6C 03 00462 C887 10 F8	DEC XREL BPL CXLOOP
00334 00335	C79D	38 ED 8F 03	SEC SBC RI	00463 C889 CE 70 03 00464 C88C 10 E7	DEC YREL BPL CYLOOP
00336 00337	C7A1	4A 8D 8C Ø3	LSR A STA RT	00465 CBBE 60 00466 CBBF	RTS
00336 00339		8D 77 03	STA RADIUS CLC	00467 C88F 00468 C98F	************************
00340 00341	C7AC	8D 8D 03	ADC RI STA RC	00469 C88F 00470 C88F	DRAW EDGE-VIEW TOROIDS
00342 00343	C782	AD 8C 03	LDA RT STA ARG	00471 CBBF 00472 CBBF	'BASIC SUBROUTINE' EQUIVALENT
00344	C7B7	20 04 C0 A5 AE	JSR SQUARE LDA SQR	00473 CBBF 00474 CBBF	; 'EDGTOR' GOSUB 'TPARM': REM SET UP RADII ; FOR CNTX=0 TO RT
00346 00347	C7FC	8D 8A Ø3	STA RS LDA SQR+1	00475 CBBF 00476 CBBF	<pre>XREL=CNTX:XSHD=CNTX R0=SQR(RT*RT-CNTX*CNTX)</pre>
00349 00349 00350	C7C1	8D 8B 03 A9 00	STA RS+1 LDA #0	00477 CBBF 00478 CBBF	FOR CNTY=0 TO R0+RC YREL=CNTY
00351 00352		8D 86 Ø3	STA CNTX RTS	00479 CBBF 00480 CBBF	YSHD=(RØ*CNTY)/(RØ*RC) GOSUB 'PTPLOT'
00353 00354	C7C7		\$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$ \$	00481 C88F	NEXT CNTY NEXT CNTX
00355 00356	C7C7 C7C7		DRAW A SHADED SPHERE	00483 C88F 00484 C88F	RETURN
90357 90358	C7C7		BASIC SUBROUTINE' EQUIVALENT	00485 C88F 20 9A C7 00486 C892 A9 00	EDGTOR JSR TPARM LDA ##00
99359 99369	C7C7		; 'SPHERE' FOR CNTX=0 TO RADIUS/SQR(2)	00487 C894 BD 6E 03	STA XSHD+1
				00488 CB97 8D 72 03	STA YSHD+1
00361	C7C7		; XREL=CNTX:XSHD=CNTX FOR CNTY=CNTX TD SQR(RAD*RAD-CNTX*CNTX)	00488 C897 80 72 03 00489 C89A AD 86 03 00490 C89D 8D 6C 03	LOOPX4 LDA CNTX STA XREL
00361 00362 00363 00364	C7C7 C7C7 C7C7		; XREL=CNTX:XSHD=CNTX ; FOR CNTY=CNTX TO SOR(RAD*RAD-CNTX*CNTX) ; YREL=CNTY:YSHD=CNTY ; HFFLAG=0	00488 C897 8D 72 03 00489 C89A AD 86 03 00490 C89D 8D 6C 03 00491 C8A0 8D 6D 03 00492 C8A3 85 AC	LOOPX4 LDA CNTX STA XREL STA XSHD STA ARG
00361 00362 00363 00364 00365	C7C7 C7C7 C7C7 C7C7 C7C7		<pre># XEEL=CNTX:XSHD=CNTX FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) YEEL=CNTY:YSHD=CNTY HYFLAG=0 GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM</pre>	00488 C897 8D 72 03 00499 C89A AD 86 03 00490 C89D 8D 6C 03 00491 C8A0 8D 6D 03 00492 C8A3 85 AC 00493 C8A3 20 04 C0 00494 C8A8 38	LOOPX4 LDA CNTX STA XREL STA XSHD STA ARG JSR SOUARE SEC
00361 00362 00363 00364 00365 00366 00367 00368	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C		; XREL=CNTX:XSHD=CNTX ; FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) ; YREL=CNTY:YSHD=CNTY ; HVFLAG=8 ; GDSUB 'PTPLOT' ; REH EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 ; GOSUB 'PTPLOT'	00488 C897 80 72 03 00489 C890 80 80 80 80 80 80 80 80 80 80 80 80 80	LOOPX4 LDA CNTX STA XREL STA XRHD STA ARG JSR SOUARE SEC LDA RS SBC SOR
00361 00362 00363 00364 00365 00365 00367 00368 00369 00370	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C		; XREL=CNTX:XSHD=CNTX ; FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) ; YREL=CNTY:YSHD=CNTY ; HYFLGS=9; ; GDSUB 'PTPLOT' ; REH EXCHANGE X & Y TO USE B-FOLD SYM HYFLGS=128 ; GOSUB 'PTPLOT' ; NEXT CNTY ; NEXT CNTY ; NEXT CNTX	00488 C897 80 72 03 00499 C89A 80 80 60 33 00490 C89D 80 6C 03 00491 C8A8 80 6C 03 00492 C8A5 20 04 60 00493 C8A9 A0 80 00494 C8A9 A0 80 00494 C8A9 AD 8A 03 00495 C8AP AD 8A 03	LOOPX4 LDA CNTX STA XREL STA XRHD STA ARG JSR SOUARE SEC LDA RS SBC SOR STA RADCND LDA RS 11
00361 00362 00363 00365 00365 00366 00367 00368 00369 00370 00371 00372	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C		XEEL=CNTX:XSHD=CNTX FOR CNTY=CNTX TO SOR(RAD*RAD-CNTX*CNTX) YEEL=CNTY:YSHD=CNTY HyFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HyFLAG=-128 GOSUB 'PTFLOT' NEXT CNTY	00488 C897 80 72 03 00489 C897 80 60 60 60 60 60 60 60 60 60 60 60 60 60	LOOPX4 LDA CNTX STA XREL STA XSHD STA ARG JSR SOUARE SEC LDA RS SSC SOR STA RADCND
00361 00362 00363 00364 00365 00366 00367 00368 00370 00372 00373 00373	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	AD 77 0 3	; XREL=CNTX:XSHD=CNTX ; FOR CNTY=CNTX: TO SOR (RAD*RAD-CNTX*CNTX) ; YREL=CNTY:YSHD=CNTY ; HVFLAG=0 ; GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 ; GOSUB 'PTFLOT' I REXT CNTY I REXT CNTY I REXT CNTX ; RETURN ; ; SPHERE LDA RADIUS	00488 C897 80 72 03 00489 C897 80 60 60 60 60 60 60 60 60 60 60 60 60 60	LOOPX4 LDA CNTX STA XREL STA XRHD STA ARG JSR SOUARE SEC LDA RS SBC SOR STA RADEND LDA RS+1 SBC SOR+1 STA RADEND+1
00361 00362 00363 00364 00365 00367 00369 00371 00372 00372 00373 00374 00375	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC 20 04 C0	; XREL=CNTX:XSHD=CNTX ; FOR CNTY=CNTX: TO SOR(RAD*RAD-CNTX*CNTX) ; YREL=CNTY:YSHD=CNTY ; HVFLAG=0 ; GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 ; GOSUB 'PTFLOT' NEXT CNTY NEXT CNTY NEXT CNTY RETURN ; FFLURN ; FFLURN ; STA ARG JSR SOUARE	00489 C897 80 72 03 00490 C890 80 60 03 00490 C890 80 60 03 00492 C863 85 AC 00492 C864 85 AC 00497 C864 85 AC 00497 C864 85 AC 00497 C864 85 AC 00496 C865 85 AC 00496 C865 85 AC 00500 C885 85 AD 00500 AD 0	LOOPX4 LDA CNTX STA XREL STA XRHD STA ARG JSR SOUARE SEC LDA RS SBC SOR STA RADCND LDA RS+1 SBC SOR+1 STA RADCND+1 JSR SORT LDA ROOT
00361 00362 00363 00364 00365 00366 00367 00370 00371 00372 00373 00374 00376 00376	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC 20 04 C0 06 AE 26 AF	; XREL=CNTX:XSHD=CNTX ; FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) ; YREL=CNTY:YSHD=CNTY ; HVFLAG=0 ; GGSUB 'PTPLOT' REM EXCHANGE X & Y TO USE B-FOLD SYM HVFLAG=-12B ; GOSUB 'PTPLOT' NEXT CNTY NEXT CNTX ; RETURN ; ; PHERE LDA RADIUS STA ARG JSR SOUARE ASL SOR ROL SOR*1	00489 C897 80 72 03 00499 C890 80 60 03 00490 C890 80 60 03 00492 C863 85 AC 00492 C864 80 80 80 80 80 80 80 80 80 80 80 80 80	LOOPX4 LDA CNTX STA XREL STA XREL STA XRHD STA ARG JSR SOUARE SEC LDA RS SSC SC S
00361 00362 00363 00363 00364 00365 00366 00367 00371 00372 00373 00373 00373 00373 00373 00373 00376 00376 00376	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC 20 04 C0 06 AE 26 AF A5 AE 85 AC	I XEEL=CNTX:XSHD=CNTX FOR CNTY=CNTX:TO SOR (RAD*RAD-CNTX*CNTX) VREL=CNTY:YSHD=CNTY HVFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 GOSUB 'PTFLOT' NEXT CNTY NEXT CNTY NEXT CNTY RETURN SPHERE LDA RADIUS STA ARG JSR SOUARE ASL SOR ROL SOR*1 LDA SOR STA RADCOD	00489 C897 80 72 03 00490 C890 80 60 03 00490 C890 80 60 03 00490 C890 80 60 03 00492 C883 85 AC 00494 C884 85 AC 00497 C884 85 AC 00497 C884 85 AC 00497 C884 85 AC 00497 C884 85 AC 00498 C885 85 AC 00498 C885 85 AC 00498 C885 85 AD 00503 C885 86 AD 00503 C885 AD 00503 AD 00503 C885 AD 00503 AD 00	LOOPX4 LDA CNTX STA XREL STA XREL STA XRHD STA ARG JSR SOUGHE SEC LDA AS SSE SOUCH STA RADEND LSB SOR+1 STA RADEND+1 JSR SORT LDA ROOT STA RADEND LCC ADC RC STA HAX LDA #JØØ STA CNTY LOOPY4 LDA CNTY
90361 90362 90363 90364 90366 90366 90369 90370 90371 90372 90371 90374 90376 90377 90377	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC 20 04 C0 06 AE 26 AF A5 AC B5 AC B5 AD	I XREL=CNTX:XSHD=CNTX FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) VREL=CNTY:YSHD=CNTY HVFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 GOSUB 'PTFLOT' NEXT CNTY NEXT CNTY NEXT CNTY RETURN I SPHERE LDA RADIUS STA ARG STA ARG STA ARG STA SOR ROL SOR ROL SOR STA RADCND LDA SOR*1 STA RADCND+1	00489 C897 80 72 03 00490 C890 80 60 03 00490 C890 80 60 03 00490 C890 80 60 03 00492 C863 85 AC 00494 C864 85 AC 00497 C864 85 AC 00497 C864 85 AC 00499 C865 85 AC 00500 C865 86 AD 00500 C865 80 AD 00500 AD 00500 C865 80 AD 00500	LOOPY4 LDA CNTY STA XREL STA XREL STA XRHD STA ARG JSR SOUGRE SEC LDA RS SEC SUBSTA SOUGH STA RADCND LES SOR SOUGH LES SOR SOUGH STA RADCND-1 JSR SORT LDA ROOT STA RA CLC ADC RC STA MAX LDA #JØØ STA CNTY LOOPY4 LDA CNTY STA YREL STA YREL STA YREL STA YREL STA MLFLER
00361 00362 00363 00364 00366 00366 00369 00370 00370 00373 00373 00373 00373 00373 00373 00373 00378 00379 00389 00389	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC 20 04 C0 06 AE 26 AF A5 AC A5 AC	I XREL=CNTX:XSHD=CNTX FOR CNTY=CNTX:D SOR(RAD*RAD-CNTX*CNTX) VREL=CNTY:YSHD=CNTY HYFLAG=0 GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HYFLAG=-128 I GOSUB 'PTFLOT' I NEXT CNTY NEXT CNTY NEXT CNTY SETURN I SPHERE LDA RADIUS STA ARG JR SOARE AGL SOR+1 LDA SOR+1 LDA SOR+1 STA RADCND+1 JST SORT+1 LST RADCND+1 JST SORT+1 LST ROOT+1 LST R	00489 C897 80 72 03 00499 C890 80 60 03 00490 C890 80 60 03 00492 C890 80 60 03 00492 C803 C80492 C804 80 60 60 60 60 60 60 60 60 60 60 60 60 60	LOOPY4 LDA CNTY STA XREL STA XREL STA XRHD STA ARG JSR SOUGARE SEC LDA RS SSEC SOR STA RADCND LDA RS! STA RADCND LDA RS! STA RADCND LDA RS! STA RADCND LDA RS! STA RADCND+1 JSE SORT LDA ROOT STA RB CLC ADC RC STA RAX LDA #JØØ STA CNTY LOOPY4 LDA CNTY STA YREL STA WEPLER LDA RØ STA MLPLER
00361 00362 00363 00364 00365 00366 00369 00372 00373 00373 00373 00374 00376 00376 00376 00376 00376 00376 00376 00376 00376 00376 00386 00381 00388 00388	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC 20 04 C0 06 AE 26 AF AF AF AF B5 AC 20 64 C0 45 3C 03 6E 3C 03 BD 92 03	I XEEL=CNTX:XSHD=CNTX FOR CNTY=CNTX:D SOR(RAD*RAD-CNTX*CNTX) VREL=CNTY:YSHD=CNTY HYFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HYFLAG=-128 GOSUB 'PTFLOT' NEXT CNTY NEXT CNTY RETURN STA ARDIUS STA ARG JAR SOARE AGE JAR SOARE AGE STA ARGOLARE STA ARGOLARE STA ARGOLARE LDA SOR: STA RADCND LDA SOR*1 STA RADCND LDA SOR*1 STA RADCND+1 JSR SORT LSR ROOT+1 ROR ROOT LDA ROOT	00489 C897 80 72 03 00499 C890 80 60 03 00490 C890 80 60 03 00492 C863 85 AC 00492 C864 85 AC 00492 C864 85 AC 00492 C865 85 AC 00492 C865 85 AC 00500 AC 0	LOOPY4 LDA CNTY STA XREL STA XREL STA XRHD STA ARG JSR SOUGRE SEC LDA RS SSC SUR STA RADCND LDA RS'1 SSC SUR STA RADCND LDA RS'1 SSC SUR STA RADCND LDA RS'1 STA RADCND+1 JSR SGRT LDA ROOT STA RB CLL ARC
00361 00363 00364 00365 00366 00367 00368 00369 00371 00372 00373 00374 00377 00376 00376 00376 00376 00376 00376 00376 00376 00376 00376 00376 00376 00376 00376 00376	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC 04 C0 04 C04	XEEL=CNTX:XSHD=CNTX FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) YEEL=CNTY:YSHD=CNTY HVFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 GOSUB 'PTFLOT' NEXT CNTX NEXT CNTX RETURN STA ARD STA ARG STA ARG STA ARG SOR ROL SOR+1 LDA SOR STA RADCND STA RADCND LST RADCND STA RADCND	00489 C897 80 72 03 00490 C890 80 60 03 00490 C890 80 60 03 00490 C890 80 60 03 00492 C803 C803 C803 C803 C803 C803 C803 C803	LOOPX4 LDA CNTX STA XREL STA XREL STA XRHD STA ARG JSR SOUGRE SEC LDA RS SEC SUR STA RADCND LDA RS'1 SEC SUR STA RADCND+1 JSR SORT LDA ROOT STA RADCND+1 JSR SORT LDA ROOT STA RB CLL AUC RC AUC RC AUC RC AUC RC STA CRC STA
00361 00363 00364 00365 00366 00367 00368 00370 00371 00373 00373 00373 00373 00378 00379 00379 00379 00379	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC C0 22 04 C0 22 05 AE C0 26 AE C0 26 AE C0 26 AE C0 26 AE C0 27 AE C0	XEEL=CNTX:XSHD=CNTX FOR CNTY=CNTX TO SOR(RAD*RAD-CNTX*CNTX) YEEL=CNTY:YSHD=CNTY HYFLAG="" GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HYFLAG=-128 GOSUB 'PTFLOT' NEXT CNTX RETURN STA ARD STA ARG STA ARG STA ARG SOR ROL SOR*1 LDA SOR*1 LDA SOR*1 LDA SOR*1 STA RADCND LDA SOR*1 STA SHD*1 STA YSHD*1 STA YSHD*1	00488 C897 80 72 03 00499 C890 80 60 03 00490 C890 80 60 03 00491 C840 80 60 03 00491 C840 85 60 00493 C843 85 AC 00493 C845 85 AC 00494 C848 85 AC 00497 C846 85 AC 00497 C846 85 AC 00497 C846 85 AC 00499 C865 85 AC 00499 C865 85 AC 00500 C865 85 AC 00511 C860 B9 03 00511 C860 B9 03 00511 C860 B9 70 00513 C860 B9 70 00513 C860 B9 70 00513 C860 B9 70 00514 C860 B9 70 00515 C860 B5 F6 00516 C865 B5 F6 00517 C866 B5 F6 00517 C866 B5 F6 00518 C865 B5 F6	LOOPY4 LDA CNTY STA XREL STA XREL STA XREL STA XRHD STA ARG JSR SOURRE SEC LDA RS SEC SOR STA RADCND LDA RS+1 SEC SOR+1 STA RADCND+1 JSR SORT LDA ROOT STA RADCND+1 JSR SORT LDA ROOT STA RO CLC ADC RC STA MAX LDA #JWW STA CNTY LOOPY4 LDA CNTY STA CNTY STA YREL STA MLECND JSR MULT STA DVOND+1 LDA PROD
00361 00363 00364 00365 00366 00367 00368 00370 00371 00372 00373 00373 00373 00376 00376 00376 00376 00376 00376 00376 00376	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	B5 AC C0 04 C0 04 C0 04 AC AC C0 04 AC AC C0 04 AC AC C0 04 AC	XREL=CNTX:XSHD=CNTX FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) YREL=CNTY:YSHD=CNTY HVFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 0-FOLD SYM HVFLAG=-128 GOSUB 'PTFLOT' NEXT CNTX RETURN STA ARD STA ARD JSR SQUARE AS SOR ROL SOR*1 LDA SOR*1 LDA SOR*1 STA RADCND JSR ROUT*1 STA RADCND STA SHIDD STA SHIDD STA SHIDD STA SHIDD STA SHIDD STA SHIDD STA RADCUS STA RADCUS	00488 C897 80 72 03 00499 C890 80 60 03 00490 C890 80 60 03 00491 C840 80 60 03 00491 C840 85 60 00493 C843 85 AC 00492 C842 85 AC 00494 C848 85 AC 00497 C848 85 AC 00497 C846 85 AC 00497 C846 85 AC 00499 C865 85 AC 00499 C865 85 AC 00500 C865 85 AC 00511 C860 85 AC 00512 C860 85 AC 00515 C860 85 AC 00516 C865 85 AC 00517 C860 85 AC 00517 C860 85 AC 00518 C865 85 AC 00519 C865 85 AC 00519 C865 85 AC 00511 C866 85 AC 00512 C866 85 AC 00512 C866 85 AC 00511 C866 85 AC 00512 C866	LOOPY4 LDA CNTY STA XREL STA XREL STA XREL STA XRED STA ARG JSR SOURRE SEC LDA RS SEC SOR STA RADCND LDA RS+1 SEC SOR+1 STA RADCND+1 JSR SORT LDA ROOT STA RADCND+1 JSR SORT LDA ROOT STA RO CLC ADC RC STA MAX LDA #JØØ STA CNTY LOA #JØØ STA CNTY STA YREL STA MIPLER LDA RØ STA DVOND+1 LDA PROD STA DVOND LDA MAX STA DVOND LDA MAX STA DVOND JSR SDIV LDA GUOT STA YSHD
00361 00363 00364 00365 00366 00367 00368 00370 00371 00372 00373 00371 00372 00379 00379 00379 00379 00379 00379 00379 00379 00379 00379 00379 00379 00379	C7G7 C7G7 C7G7 C7G7 C7G7 C7G7 C7G7 C7G7	85 AC 03 46 03 80 04 C0 03 04 AC 03 04 C0 05 C0 03 04 C0 05	XREL=CNTX:XSHD=CNTX FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) YREL=CNTY:YSHD=CNTY HVFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 GOSUB 'PTFLOT' NEXT CNTX RETURN STA ARD STA ARG JSR SQUARE ASL SOR ROL SOR*1 LDA SOR*1 LDA SOR*1 STA RADCND LDA SOR*1 STA RADCND LDA RADTUS STA RADCND LDA SOR*1 STA RADCND LDA SOR*1 STA RADCND*1 JSR SQUARE STA RADCND*1 JSR SOUT*1 STA RADCND*1 JSR SOUT*1 STA RADCND*1 JSR SOR*1 LDA RADTUS STA TATAX LDA #100 STA YSHD*1 STA XSHD*1 STA XSHD*1 STA XSHD*1 STA XSHD*1 STA XSHD*1 STA RADCUS STA RADCUS STA RADCUS STA XSHD*1 STA XSHD*1 STA XSHD*1 STA RAGUUS STA STEMP*1	00488 C897 80 72 03 00489 C890 80 60 03 00490 C890 80 60 03 00491 C803 85 60 60 03 00492 C803 85 60 60 03 00492 C803 85 60 60 03 00494 C803 85 60 60 03 00494 C803 85 60 60 03 00494 C803 85 60 60 03 00497 C862 85 60 60 00499 C863 85 60 60 00499 C863 85 60 00499 C863 85 60 60 60 60 60 60 60 60 60 60 60 60 60	LOOPY4 LDA CNTY STA XREL STA XREL STA XREL STA XRED STA ARG JSR SOURRE SEC LDA RS SEC SOR STA RADCND LDA RS+1 SEC SOR+1 STA RADCND+1 JSR SORT LDA ROOT STA RADCND+1 JSR SORT LDA ROOT STA RO CLC ADC RC STA MAX LDA #JØØ STA CNTY LOOPY4 LDA CNTY STA YREL STA MIPLER LDA RØ STA DVOND+1 LDA PROD STA DVOND LDA MAX STA DVOND LDA MAX STA DVOND JSR SDIV LDA QUOT STA YSHD JSR FTFLOT LDA CNTY
00361 00363 00364 00365 00366 00367 00368 00376 00370 00371 00373 00373 00373 00373 00379 00379 00380 00380 00380 00380 00380 00380 00380 00380 00380 00380 00380 00380	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC C0 06 AE C0 07 AE C0 08 AE C0	XEEL=CNTX:XSHD=CNTX FOR CNTY=CNTX: TO SOR (RAD*RAD-CNTX*CNTX) YEEL=CNTY:YSHD=CNTY HVFLAG= GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HVFLAG=-128 GOSUB 'PTFLOT' NEXT CNTX RETURN RETURN STA ARG JSR SQUARE ASL SOR ROL SOR+1 LDA SOR STA RADCND LDA SOR+1 STA RADCND LDA SOR+1 STA RADCND LDA SOR+1 STA RADCND LDA SOR+1 STA RADCND-1 JSR SQUARE ASL SOR STA RADCND-1 JSR SCAT LSR ROOT-1 ROR ROOT LDA ROOT STA XHAX LDA ##00 STA XHAX LDA ##00 STA XHOP-1 STA XSHD-1 STA XSHD-1 STA RADCUS STA XSHD-1 STA XSHD-1 STA XSHD-1 STA RADCUS STA RAG STA TEMP-1 LDA SOR	00488 0897 80 72 03 00489 C890 80 60 03 00491 C840 80 60 03 00491 C840 85 60 00493 C843 85 60 00493 C843 85 60 00494 C848 38 00495 C847 85 60 00494 C848 38 00495 C847 85 60 00497 C848 85 60 00499 C868 85 60 00499 C868 85 76 00500 C868 85 76 00500 C868 85 76 00500 C868 85 76 00500 C868 85 76 00501 C867 20 64 00502 C868 80 30 00503 C860 80 89 03 00504 C860 80 89 03 00505 C861 80 89 03 00506 C867 80 89 03 00507 C867 80 89 03 00508 C867 80 80 00508 C867 80 80 00509 C867 80 80 00509 C867 80 80 00501 C867 80 80 00502 C867 80 80 00502 C867 80 80 80 00502 C86	LOOPY4 LDA CNTX STA XREL STA XREL STA XRHD STA ARG JSR SOUARE SEC LDA RS SEC SER SEC SER SEC SER-1 STA RADEND-1 JSR SERT LDA RODT STA RADEND-1 STA RADEND-1 STA RADEND-1 STA WELL STA DVOND+1 LDA PROD STA WELL STA DVOND+1 LDA PROD STA STA DVOND+1 LDA PROD STA
00361 00363 00364 00365 00366 00367 00368 00376 00377 00377 00377 00377 00377 00379 00379 00379 00379 00379 00389 00399 00399	C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C7 C7C	85 AC C0 20 04 C0	I XREL=CNTX X SHD=CNTX FOR CNTY=CNTX TO SOR (RAD*RAD-CNTX*CNTX) YEL=CNTY:YSHD=CNTY HYFLAG=0 GOSUB 'PTFLOT' REM EXCHANGE X & Y TO USE 8-FOLD SYM HYFLAG=-128 GOSUB 'PTFLOT' NEXT CNTY NEXT CNTX RETURN I SPHERE LDA RADIUS STA ARG JSR SOUARE ASL SOR ROL SOR*1 LDA SOR*1 LDA SOR*1 LTA SOR*1 LTA RADCUB+1 LTA ROOT*1 LTA RADCUB+1 LTA RADCUB-1 LTA RADCUB-1 LTA RADCUB-1 LTA RADCUB-1 LTA RADCUB-1 LTA RADCUS-1 STA ARG JSR SOUARE STA TEHP+1 LDA SOR	00489 C897 80 72 03 00499 C890 80 60 03 00490 C890 80 60 03 00490 C890 80 60 03 00492 C890 80 60 60 03 00492 C890 80 60 60 03 00492 C890 80 60 60 03 00494 C800 80 60 60 60 60 60 60 60 60 60 60 60 60 60	LOOPX4 LDA CNTX STA XREL STA XREL STA XRHD STA ARG JSR SOUGRE SEC LDA RS SEC SOR STA RADCND LDA RS': SEC SOR': STA RADCND+1 JSR SORT LDA ROOT STA RADCND+1 JSR SORT LDA ROOT STA RO CLC ADC RC STA RADC ADC RC STA RADC STA STA DVONDD LDA RADC STA DVONDD LDA RADC STA STA DVSOR JSR SDIV LDA GUOT STA YSHD JSR FTFLOT LDA CNTY CRP MAX

(Continued on page 74)

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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Four

00529 C900 00530 C903 00531 C906 00532 C908 00533 C908	4C CC CB AD 86 03 CD 8C 03 F0 06 EE 86 03 4C 9A CB	DONE4 LD CM BE IN	IP LOOPY4 JA CNTX IP RT 10 DOMEHT 10 CONEHT 10 CONTX 1P LOOPX4 5	00657 00658 00659 00659 00660 00661 00662	C9FB C9FB C9FD CA00 CA01 CA04 CA06	AD 87 0 85 FB 20 EA C 38 AD 87 0 E5 FD 8D 71 0	3		LDA RØ STA DVSOR JSR SDIV SEC LDA CNTY SBC QUOT STA YSHD				
00535 C90F 00536 C90F		\$	*************	99664 99665	CAØ9 CAØB	A9 00 8D 83 0		. 1	LDA ##00 STA HVFLAG				
00537 C90F 00538 C90F		DRAW A	SHADED, TOP-VIEW TOROID	00666 00667	CA0E CA10	E5 FE 8D 72 0		5	SBC QUOT+1 STA YSHD+1				
00539 C90F 00540 C90F 00541 C90F		BASIC	SUBROUTINE' EQUIVALENT	90668 90669 90679	CA15 CA16	20 28 C A9 80 8D 83 0		1	JSR PTPLOT				
00542 C90F 00543 C90F		TOROID	FOR CNTX=0 TO RO/SQR(2)	00671 00671	CAIB	20 28 C AD 87 Ø	6		STA HVFLAG				
00544 C90F 00545 C90F		;	REM 8-FOLD SYMMETRY USED XREL=CNTX	00673 00674		CD 88 Ø			LDA CNTY CMP MAX BEQ DDNY1				
00546 C90F 00547 C90F		;	MAX=SQR(RO*RO-CNTX*CNTX) IF CNTX>RI THEN GOTO 'GRTR'	00675 00676		EE 87 0 4C 9A C			INC CNTY				
00548 C90F 00549 C90F			CNTY=SQR(RI*RI-CNTY*CNTY) GOTO 'LLPY1'	00677 00678	CAZC	AD 86 0 CD 92 0	13 DI	DNY1 I	LDA CNTX				
00550 C90F 00551 C90F		GRTR'	CNTY=CNTX	00679 00680	CA32	FØ 06 EE 86 0		1	BEQ DUNTOR				
00552 C90F 00553 C90F			RØ=SQR(CNTY*CNTY+CNTX*CNTX) XSHD=CNTX-(CNTX*RC)/RØ	00681 00682	CA3A	4C 35 C	9		JMP LLPX1				
00554 C90F 00555 C90F		1	YSHD=CNTY-(CNTY*RC)/RØ HVFLAG=0:GOSUB 'PTPLOT'	00683 00684	CA3B		3 9	*****	******	*****	******	******	
00556 C90F 00557 C90F		3 3	HVFLAG=-128:GOSUB 'PTPLOT' IF CNTY=MAX THEN GOTO 'DDNY1'	00685 00686	CA3B		3	DRAW	"INSIDE VIE	W" TOR	DIDS		
00558 C90F 00559 C90F		; ;	CNTY=CNTY+1 GOTO 'LLPY1'	00687 00688 00689	CA3B CA3B		3	BASI	C SUBROUTIN	E, EDN:	IVALENT		
00560 C90F 00561 C90F 00562 C90F		DDNY1.	NEXT CNTX RETURN	00690 00691			;	'SPOOL	L' GOSUB		DT		
00563 C90F	20 9A C7 AD 8E 03	TOROID JS	R TPARM	00692 00693	CA3B		;		XREL=Ch	ITX: XSHI			
	85 AC 20 04 C0	ST	A ARG SR SQUARE	99694 99695	CA3B		;		FOR CNI	Y=Ø TO	MAX		
00567 C91A 00568 C91C	06 AE 26 AF	AS RO	SL SQR DL SQR+1	00696 00697	CA3B		;		GOSUB (PTPLOT	MAX)-CNTY		
00570 C920	A5 AE 85 AC	ST	A SOR	00698 00699	CA3B		;		NEXT CH	ITY ITX			
00572 C924	AS AF BS AD	ST	A SOR+1 A RADCND+1	00700 00701	CA3B		- 1		RETURN				
00574 C929	20 64 C0 4E 3D 03 6E 3C 03	LS	GR SORT	00702 00703 00704	CA3B CA3E CA41	20 9A C AD 86 0 BD 6C 0	3 LL	.PX2 L	JSR TPARM LDA CNTX STA XREL				
00576 C92F	AD 3C 03 BD 92 03	LD	R ROOT OA ROOT OA XMAX	99795 99796	CA44 CA46	85 AC		5	STA ARG				
00578 C935 00579 C938	9D 9C 03	LLPX1 LD		00707	CA47	A9 00 ED 86 0	3	į.	DA ##ØØ				
	85 AC 20 04 C0	ST	A ARG	00709	CA4C	8D 6D 0		9	DA ##00				
	BD 91 Ø3 A5 AE	ST	A XSQR+1 A SQR	00711 00712	CAS1 CAS3	8D 6E 0		9	SEC ##00 STA XSHD+1				
	8D 90 03 AD 8E 03	LD	A XSQR	00713 00714	CA56	20 04 C		9	ISR SQUARE				
	85 AC 20 04 C0 38		A ARG R SQUARE	00715 00716		AD BA Ø	2		DA RS SBC SQR				
00589 C951	A5 AE ED 90 03	LD	A SOR	00717 00718 00719	CA61	85 AC AD 85 Ø	13	1	STA RADCND LDA RS+1				
00591 C956 00592 C958	85 AC A5 AF	ST	A RADCND DA SQR+1	00720 00721	CA64 CA66	E5 AF 85 AD 20 64 C	· OA		SBC SQR+1 STA RADOND JSR SQRT	+1			
00593 C95A	ED 91 03 85 AD	SB	C XSCR+1 A RADCND+1	00722 00723	CA6B CA6C	38 AD 8D 0		5	SEC LDA RC				
	20 64 C0 AD 3C 03	LD	GR SORT DA ROOT	00724 00725	CA6F CA72	BD 88 Ø	13.	9	SEC ROOT				
00597 C965 00598 C968	38 8D 88 03	SE		00726 00727	CA75	A9 00 80 87 0		5	LDA ##ØØ STA CNTY				
00599 C969 00600 C96C 00601 C96F	AD BF 03 90 23	SB	A RI C CNTX	00728 00729	CA7A CA7D	AD 87 Ø		5	LDA CNTY STA YREL				
00602 C971 00603 C974	AD BF 03 85 AC	L.D	CC GRTR DA RI TA ARG	00730 00731 00732	CABO CABO CABO	85 AD 81 81 B5 AC	3	L	DA RC				
00604 C976 00605 C979	20 04 C0 38		IR SQUARE	00733 00734	CAB7	20 11 C	120		STA MLFCND JSR MULT STA DVDND+1				
00607 C97C	A5 AE ED 90 03	SB	OA SQR C XSQR	00735 00736	CABC	A5 AE 85 FD		L	DA PROD				
00609 C981	85 AC AS AF	LD.	A RADCND A SOR+1	00737 00738	CA90 CA93	AD 88 Ø	3	L	DA MAX STA DVSOR				
00611 C986	ED 91 03 85 AD 20 64 C0	ST	C XSQR+1 A RADEND+1 GR SQRT	ØØ739 ØØ74Ø	CA95	20 EA C: A5 FD	5	L	JSR SDIV DA QUOT				
00613 C988 00614 C98E	AD 3C 03 BD 87 03	L.D	A ROOT A CNTY	00741 00742 00743	CA9B	38 ED 87 Ø	3	9	SEC CNTY				
00616 C794	4C 9A C9 AD 86 Ø3	GRTR LD	P LLPY1 A CNTX	00744 00745	CAA1	8D 71 0: A5 FE E9 00	3	L	STA YSHD LDA QUOT+1 SBC ##00				
00618 C99A	BD 87 Ø3 AD 87 Ø3	LLPY1 LD	A CNTY	00746 00747	CAA5 CAAB	8D 72 00 20 28 C		9	STA YSHD+1				
00620 C9A0	8D 70 03 85 AC	ST	A YREL A ARG	00748 00749	CAAB	AD 87 0	3	L	DA ENTY				
00622 C9A5	20 04 C0 18 A5 AE	CL		00750 00751	CAB1	FØ Ø6 EE 87 Ø:		1	DEQ DDNY2				
00624 C9AB 00625 C9AB	6D 90 03 85 AC	AD	C XSOR A RADCND	00752 00753 00754	CAB6 CAB9 CABC	4C 7A CA AD 86 03 CD 8C 03	3 DD	NY2 L	DA CNTX				
80627 C9AF	6D 91 03	ADI	A SQR+1 C XSQR+1	99755 99756	CABF	FØ 06 EE 86 0		E	CMP RT BEQ DUNHSP INC CNTX				
80629 C9B4	85 AD 20 64 C0 AD 3C 03	JSI	A RADCND+1 R SQRT	00757 00758	CAC4 CAC7	4C 3E C	A		IMP LLPX2				
00631 C9BA	BD 89 Ø3 85 FB	ST	A ROOT A RO A DVSOR	00759	CACB			ND					
00633 C9BF 00634 C9C2	AD 86 03 85 AD	LD	A CNTX A MLPLER		= 000 TABLE								
00636 C9C7	AD 8D 03 85 AC	LD/ ST/	A RC A MLPCND	SYMBO	VALUE								
00638 C9CC	20 11 C0 85 FE A5 AE	ST	R MULT A DVDND+1 A PROD	ARG CLIP	. 03	7D CL	KLIT IPR	0302 037E	CLIPU	037F	CLIPD	0380 C608	
00640 C9D0	85 FD 20 EA C5	STA	A PROD A DVDND R SDIV		_ TABLE								
00642 C9D5 00643 C9D6	38 AD 86 03	SEC		CNTX		86 CN		0387	CXLOOP	C881	CYLNDR	C864	
00644 C9D9 00645 C9DB	E5 FD BD 6D 03	SBO	C QUOT A XSHD	DIVI	DE CE	25 DO		C843	DDNY2 DONE4	CAB9 C900	DHEM! DONEHT	C6BC C9ØE	
00647 C9E0 I	A9 00 E5 FE	LD/ SBC	A ##00 C QUOT+1	DVSOI	8 88		INHSP IGTOR	CAC7 CBBF 0381	DUNTOR GETVAL HIEN	CSF1	DVDND GETZ	00FD C745	
00649 C9E5	8D 6E 03 AD 87 03 85 AD	LDF	A XSHD+1 A CNTY A MLPLER	HVFL/	AG 03	7A LO	PX1 OPX	C935	HLEN LLPX2 LOOPX4	CA3E CB9A	HTORRN LLPY1 LODPY	0346 E99A CB2E	
00651 C9EA (AD 8D 03 85 AC	LDF	A MLPLER A RC A MLPCND	LOOP	/4 C8	ICC MA	X	0388 C5FF	MLPCND NORM	00AC C224	MLPLER NOROT	00AD C65A	
00653 C9EF :	20 11 C0 85 FE	JSF	R MULT									nd Listing Four	
	AS AE BS FD		A PROD A DVDND						(I_i)	stino	Five hea	ins on page 76)
									12.	8		, on page 70)	

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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Four

LTSHD								
100			NOSCAL	0347	DRIGIN	C5EA	PLDONE	C721
RAM 03-6 RANDOM C0CB RC 03-9B RHEMI C6EB RI 03-9B RI 03-9B RI 05-9B RI 05-9B RI 05-9B RI 05-9B RI 05-9B RI 05-9B RI 03-9B RI 03-9								
RI 036F RNDM C0000 RO 039E RODT 033C RS 036A RT 038C SDIV C5EA SPHERE C7C7 SPOOL CA3B SOR 00AE SORT C064 SQUARE C004 TEMP 0384 TNTHP 037B TDNE 0379 TOROID C00F TEARN C79A VALUE 0344 XCENT 035A XHAX 0392 XPLT 033F XREL 036C XSHD 035A XHAX 0392 XPLT 033F XREL 036C XSHD 035A XHAX 0392 XPLT 037F XREL 0370 XSDR 0370 XPLT 037F XPLT 0341 YREL 0370 YSHD 0371 XPLT 037F XPL								
RS 039A RT 039C SDIV CSEA SPHEE C7C7 FSFOOL CASB SOR 00AE SORT C064 SQUARE C004 FEMP 0394 TNTMP 037B TONE 0379 TOR0ID C90F FEMP 0394 TNTMP 037B TONE 0379 TOR0ID C90F FTARR C79A VALUE 0344 XCENT 036A XMAX 0392 KPLT 033F XREL 036C XSHD 036D XSOR 0392 VCENT 033F YPLT 0341 YREL 0370 YSHD 0371 VCENT 035F YPLT 0341 YREL 0378 YSHD 0371 ND 0F ASSEMBLY Listing Five 0000 ; INTERFACE - EASY PARAMETER SETTING FOR SHAPE 00001 0000 ; DRAWING ROUTINES FROM BASIC. 00002 0000 ; ICHARD L. RYLANDER 11/23/B4 00003 0000 ; ICHARD L. RYLANDER 11/23/B4 00004 00006 GRIGIN=#16593								
SPOOL								
TEMP 0394 TNTMP 037B TONE 0379 TOROID C90F TEARN C794 VALUE 0344 XCENT 0359 TOROID C90F XELT 035F XREL 036C XSHD 036D XMAX 0392 XELT 033F XREL 036C XSHD 036D XSOR 0390 0390 YCENT 035F YELT 0341 YREL 0370 YSHD 0371 ZEROUT C791 ZREL 0373 ZWX 0375 ND 0F ASSEMBLY Listing Five 0001 0000 ; INTERFACE - EASY PARAMETER SETTING FOR SHAPE 0002 0000 ; DRAWING ROUTINES FROM BASIC. 0000 ; INTERFACE 0000 1 ; RICHARD L. RYLANDER 11/23/B4 0000 0000 0000 00000 00000 00000 00000 0000								
TRARH C79A VALUE 0344 XCENT 036A XMAX 0392 XPLT 0315 XEL 036C XSHD 036D XSDR 0390 XSDR 0390 XCENT 036F VPLT 0341 YREL 0370 YSHD 0371 ZEROOT C791 ZREL 0373 ZWX 0375 VSHD 0371 ZEROOT C791 ZREL 0373 ZWX 0375 VSHD 0371 ZEROOT C791 ZREL 0373 ZWX 0375 VSHD 0371 ZEROOT C791 ZREL 0370 ZWX 0375 VSHD 0371 ZEROOT C791 ZREL 0373 ZWX 0375 VSHD 0371 ZEROOT C791 ZREL 0370 ZWX 0375 VSHD 0371 ZEROOT C791 ZREL 0370 ZWX 0375 VSHD 0371 ZEROOT C791 ZWX 0375 ZWX								
XPLT 033F XREL 036C XSHD 036D XSDR 0390 YCENT 034F YPLT 0341 YREL 0370 YSHD 0371 ZEROOT C791 ZREL 0373 ZWX 0375 YSHD 0371 ZWX 0375 ZWX 037								
YEAR YEAR YEAR 0341 YEAR 0370 YEAR 0371								
ZEROOT C791 ZREL 0373 ZWX 0375 ND OF ASSEMBLY Listing Five 2001 0000 ; INTERFACE - EASY PARAMETER SETTING FOR SHAPE 2002 0000 ; DRAWING ROUTINES FROM BASIC. 2000 ; RICHARD L. RYLANDER 11/23/84 2000 ; RICHARD L. RYLANDER 11/23/84 2000 ; RICHARD L. RYLANDER 11/23/84 2000 ; RICHARD R. RYLANDER 11/23/84 2000 0000 RAM = 10593								
IND OF ASSEMBLY Listing Five 0001 0000 ; INTERFACE - EASY PARAMETER SETTING FOR SHAPE 0002 0000 ; DRAWING ROUTINES FROM BASIC. 0003 0000 ; 0000 0000 ; RICHARD L. RYLANDER 11/23/84 0000 00000 0000 ; 00000 00000 00000 RAM = 10595							YSHD	0371
isting Five 2001 0000 ; INTERFACE - EASY PARAMETER SETTING FOR SHAPE 2002 0000 ; DRAWING ROUTINES FROM BASIC. 2003 0000 ; RICHARD L. RYLANDER 11/23/84 2004 0000 ; 2005 0000 ; 2006 0000 GRIGIN-ICACE 2006 0000 RAM = 10595	ZEROO	T C791	ZREL	0373	ZWX	0375		
0003 0000 ; RICHARD L. RYLANDER 11/23/84 ; RICHARD L. RYLANDER	אט טר	ASSEMBLY						
2004 9000 RAY RICHARD L. RYLANDER 11/23/84 2005 9000 9 2006 9000 9 2006 9000 0RIGIN=#CACB 2008 9000 RAY =10593	List	ting F	ive					NG FOR SHAPE
8805 0000 ; ******************************	List 0001 0002	ting F	ive					NG FOR SHAPE
2006 0000 ;	2001 2002 2003	ting F	ive	DRAWIN	G ROUTINES	FROM B	ASIC.	NG FOR SHAPE
0000 ORIGIN=#CAC8	2001 2002 2003 2004	ting F	ive	DRAWIN	G ROUTINES	FROM B	ASIC.	NG FOR SHAPE
2008 0000 RAM =10393	2001 2002 2003 2004 2005	ting F	ive	DRAWIN	G ROUTINES	FROM B	ASIC.	NG FOR SHAPE
2000	2001 2002 2003 2004 2005 2006	ting F		DRAWIN RICHAR	D L. RYLAN	FROM B	ASIC.	NG FOR SHAPE
	2001 2002 2003 2004 2005 2006 2007	ting F		RICHAR ******	D L. RYLAN	FROM B	ASIC.	NG FOR SHAPE

90009 90010 90011 90012 90013 90014 90015 90016 90018 ; PARAMETER LOCATIONS FOR VARIOUS SHAPES ; PARAMETER LE XCENT =#036F XPLOT =#035F XPLOT =#034F XMIN =#0340 XMIN =#0340 XMID =#0340 YMID =#0340 YMID =#0340 YMIA =#0350 YMAX =#0350 00010 00020 0000 00022 00023 00024 0000 00025 0000 ; HVFLAG =#0383 VALUE =#0344 PLTFLG =#033E 00027 0000 00029 00030 0000 DEFLAG *#FR 00031 9999 00033 *********************************** 00034 0000 FUNCTION LOCATIONS 00035 00036 0000 GRFON =\$C0E2 GRFOFF =\$C103 ; SWITCH TO GRAPHICS MODE ; RETURN TO TEXT DISPLAY 00037 00038 0000 00039 0000 CLEARR=#C12C ; CLEAR BITMAP ; CLEAR (FILL) BYTE ; LOAD COLOR MAP ; COLOR BYTE 00040 00041 8888 CLRBYT=\$C135 COLORR=\$C118 00042 0000 90043 0000 COLBYT=#C119 00044 0000 PLOTR =\$C14B LINER =\$C2DB FACETR =\$C4E1 ; POINT FLOT ROUTINE ; DRAW A LINE ; DRAW A SHADED FACET 00045 00046 00047 0000 ; ; ********************************** SHADED SHAPE DRAWING ROUTINES SPHERR=#C7C7 CYLNDR=#C864 TORUSR=#C90F EDGTOR=#C88F SPOOLR=#CA38 ; SPHERE ; CYLINDER ; TOP-VIEW TOROID ; EDGE-VIEW TOROID ; INSIDE-VIEW TOROID 00000 00061 ; BASIC ROM ROUTINES 00062 8888 CHECK FOR COMMA

CVALUATE EXPRESSION

CONVERT TO FIXED 00063 CHKCOM=#AEFD 00064 9999 99965 FLTFIX=\$BIAA 00066 0000 00067 LINFAC *=*+1 : LINE OR FACET FLAG 00069 *=ORIGIN 0394 CACB 00072 00073 00074 GET PARAMETERS FROM BASIC CALLING STATEMENT CAC8 CACB THE FORM: OF THE FORM: SYS(FNCTN), PARAM1, PARAM2, PARAM3(OPT) WHERE THE THIRD PARAMETER (FOR EXAMPLE) MAY BE OFTIONAL (A DEFAULT VALUE IS USED IF THE PARAMETER IS NOT SPECIFIED) 00076 00078 00079 00080 00081 00082 00083 00083 00084 00085 00086 00086 CACS

GETNUM JSR CHKCOM ; LOOK FOR COMMA JSR EVAEXP ; EVALUATE EXPRESSION JSR FLTFIX ; CHANGE TO INTEGER WITH ; HIGH BYTE IN "A" AND LOW BYTE IN "Y" ; CHECK FOR ADDITIONAL (OPTIONAL) PARAMETERS PCHECK LDA ##2C LDY #0 STY DEFLAG CMP (#7A),Y BNE NOMORE JDY ##80 STY DEFLAG RTS ; "," COMMA : NO COMMA - USE DEFAULT ; GET TWO ADDITIONAL PARAMETERS FOR TOROIDS GETTWO JSR PCHECK
BIT DEFLAG
BHI DFAULT
JSR EVAEXP
JSR FLTFIX
STY RI
JSR GETNUM
STY RO
DFAULT RTS

00111 00112 00113 CAFB CAFB SET CENTER COORDINATES CAFB CAFB CAFE CBØ1 00114 CENTER JSR GETNUM STY XCENT STA XCENT+1 JSR GETNUM STY YCENT 20 C8 CA 8C 6A 03 8D 6B 03 20 C8 CA 8C 6F 03 00116 00117 20118 00119 CBØ4 CBØ7 RTS 00120 60 00121 CBØB , 00122 CBØB 00123 ; CLEAR THE BITMAP, FILLING WITH (OPTIONAL ; FILL VALUE SPECIFIED OR WITH (DEFAULT) "! 00124 CBØB 00125 00126 00127 CBØB CBØB CBØE CB1Ø CLEAR2 JSR FCHECK 20 D2 CA 24 FB 30 07 20 9E AD 20 AA B1 BIT DEFLAG BMI DEFCLR JSR EVAEXP JSR FLTFIX 00120 00129 00130 CB12 00131 CB18 CB19 CB1B CB1E CB21 00132 2C AØ ØØ BC 35 C1 4C 2C C1 BYTE #20 .BYTE #2C DEFCLR LDY #0 STY CLRBYT JMP CLEARR 00133 00134 00135 00136 00137 **CB21** CB21 CB21 CB21 00138
00139
00140
00141
00142
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00150
00151 ; FILL COLOR MAP WITH (OPTIONAL) COLOR BYTE ; SPECIFIED OR WITH (DEFAULT) "\$01" ; (BLACK DOTS ON WHITE BACKGROUND) COLOR2 JSR PCHECK
BIT DEFLAG
BMI DEFCOL
JSR EVAEXP
JSR FLIFTIX
.EVIE 42C
DEFCOL LDV ##01
STY COLEVT
JMP COLORR 20 D2 CA 24 FB 30 07 20 9E AD 20 AA B1 2C A0 01 BC 19 C1 4C 18 C1 PLOT OR UNPLOT POINTS PLOT2 LDA #0

BYTE #2C

UNPLT2 LDA ##0

STA PLIFLE

JSR GETNUM

STY XPLOT

JSR GETNUM

STY YPLOT

JMP PLOTR A9 00 2C A9 B0 BD 3E 03 20 CB CA BC 41 03 4C 4B C1 00165 00166 00167 CB4B CB4E CB51 CB51 CB51 00168 , 00169 ; DRAW LINES BETWEEN (X1,Y1) AND (X2,Y2) ; OR SHADED FACETS BETWEEN THREE POINTS ; (X1,Y1), (X2,Y2) AND (X3,Y3) CB51 00171 CB51 CB51 00173 LINE2 LDA #0 00174 CB51 A9 00 LINE2 LDA #0
BYTE #2C
FACET2 LDA ##BØ
STA LINFAC
JSR GETNUM
STY XMIN
STA XMIN+1 00175 00176 00177 2C A9 80 8D 93 03 CB54 CBS6 CBS9 8D 9% 03 20 C8 CA 08 C4 08 CA 08 C4 03 20 C8 CA 08 C4 03 20 C8 C4 03 20 C8 C4 03 20 C8 C4 03 20 C8 CA 08 C4 03 20 C8 CA 08 C4 00178 00179 CB5C CB5F STA XMIN+1 JSR GETNUM STY YMIN JSR GETNUM STY XMID STA XMID+1 JSR GETNUM CB62 CB65 CB68 00181 00182 00183 00184 CB6B 00185 00186 CB71 00187 CB74 CB77 STY YMID BIT LINFAC 00188 00189 CB7A CB7C CB7F BPL LDRAW JSR GETNUM 20 CB CA 00191 8C 50 03 8D 51 03 STY XMAX STA XMAX+1 CB82 00193 20 CB CA 8C 52 03 JSR GETNUM 00173 00194 00195 00196 00197 00198 00199 00200 00201 00202 8C 52 Ø3 2Ø C8 CA STY YMAX JSR GETNUM CBBB 8C 44 03 4C E1 C4 4C DB C2 JMP FACETR LDRAW JMP LINER ; DRAW A SPHERE CENTERED AT (XCENT, YCENT) ; DEFAULT RADIUS IS LAST VALUE USED CB97 CB97 00203 00204 CB97 20 FB CA 20 D2 CA 24 FB 30 09 20 9E AD 20 AA B1 8C 77 03 4C C7 C7 SPHER2 JSR CENTER 00205 JSR PCHECK BIT DEFLAG BMI SKIP1 CB9A CB9D CB9F CBA1 CBA4 CBA7 CBAA CBAD CBAD CBAD CBAD 00209 00210 00211 00212 00213 00214 00215 00216 JSR EVAEXP JSR FLIFIX SKIP1 JMP SPHERR DRAW A TOP-VIEW TORDID AT (XCENT, YCENT)
DEFAULT INNER AND QUTER RADII ARE LAST USED 00218 CBAD TORUS2 JSR CENTER JSR GETTWO JMP TORUSR CBB0 CBB0 20 FB CA 20 E4 CA 4C 0F C9 00221 00222 00223 00224 00225 CBB6 ; ; DRAW CYLINDERS WITH AXES HORIZONTAL OR ; VERTICAL. DEFAULT RADIUS AND "HALF-LENGTH" ; ARE LAST VALUES USED. 00226 CBB6 LDA ##80 BYTE #2C HCYL2 LDA #0 STA HVFLAG JSR CENTER JSR FCHECK BIT DEFLAG BMI SKIP2 JSR EVAEXP Ø0228 CBB6 00229 00230 00231 00232 00233 00234 00235 00236 00237 CBB6 CBB8 CBB9 CBBB CBBE CBC1 CBC4 CBC6 CBC8 A9 80 2C A9 80 8D 83 03 20 FB CA 20 D2 CA 24 FB 30 0F 20 9E AD

(Continued on page 78)

00089

99999

00091 00092 00093

A9 2C A0 00 84 FB

FB D1 7A A D0 03 4C 73 00 A0 80 84 FB 60

20 D2 CA 24 FB 30 0F 20 9E AD 20 AA B1 8C 8F 03

20 CB CA BC BE 03

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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Five

```
CBCB 20 AA B1
CBCE 8C 77 03
CBD1 20 CB CA
CBD4 8C 89 03
CBD7 4C 64 CB
                                                                 STY RADIUS
JSR GETNUM
STY HLEN
SKIP2 JMP CYLNDR
 00239
00240
00243
00244
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00247
00248
00250
00251
00255
00255
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                                                                   ; DRAW EDGE-VIEW TOROIDS WITH AXES HORIZONTAL
                 ; OR VERTICAL
; INNNER AND OUTER RADII ARE OPTIONAL
                                                                HTOR2 LDA ##8Ø
BYTE #2C
HTOR2 LDA #Ø
STA HVFLAG
JSR CENTER
JSR GETTWO
JMP EDGTOR
                                                                     DRAW INSIDE-VIEW TOROIDS, "SPOOLS",
WITH AXES HORIZONTAL OR VERTICAL
INNER AND DUTER RADII ARE OPTIONAL
                                A9 80
2C
A9 00
8D 83 03
20 FB CA
20 E4 CA
4C 3B CA
                                                                LUM ##80
BYTE #20
LDA #0
STA HVFLAG
JSR CENTER
JSR GETTWO
JMP SPOOLR
 00268
00269
 00271
ERRORS = 00000
SYMBOL TABLE
 SYMBOL VALUE
                                              CHRCOM
COLBYT
DEFCLR
EDGTOR
FLTFIX
GREON
   CENTER CAFB
CLRBYT C135
CYLNDR C864
                                                                                          CLEAR2
COLOR2
DEFCOL
EVAEXP
GETNUM
HCYL2
HVFLAG
LINFAC
PLOT2
RAM
SKIP2
TORUS2
VCYL2
XMAX
                                                                                                                                      CLEARR
COLORR
DEFLAG
FACET2
GETTWO
HLEN
LDRAW
NOMORE
PLOTR
RI
SPHER2
TORUSR
VSPL2
XMID
YMAX
                                                                                                                 CBØB
CB21
CB2F
AD9E
CAC8
CBB9
Ø393
CB37
Ø393
CB37
CBAD
CBBAD
CBBAD
   FACETR
   GREDEE
   HSPL2
                                               HTDRZ
   LINE2
                          CB51
                                                LINER
   ORIGIN
   PLTFLG
                                                RADIUS
                                                SKIPI
   SPHERR
                          C7C7
CB3A
                                                                      CA3B
Ø344
                                                VALUE
   UNFLT2
VTOR2
                                                XCENT
                                                                      Ø36A
                           Ø34A
                                                XPLDT
   YMID
                         034F
                                                YMIN
```

END OF ASSEMBLY End Listing Five

Listing Six

```
530 POKE 198,0:WAIT 198,1:POKE 198,0
540 REH WAIT FOR A KEY TO BE PRESSED
530:
550 REH DRAW THO "GOBLETS" ONE WITH HALFTONE, THE OTHER RANDOM SHADING.
570:1
                    560 REM DRAW TWO "GOBLETS" ONE WITH HALFTONE, THE OTHER RANDOM SHADING.
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                           720 PURE 194,6 WAIT 195,1 PURE 198,8

730 REM DRAW "WINE" SCENE
750 REM DRAW "WINE" SCENE
750 REM DRAW "WINE" SCENE
750 REM DRAW "REM BACKLIT ILLUMINATION
770 POKE SH,1 : REM HALFIONE SHADING FOR MOST (RANDOM "LABEL" ON BOITLE)
770 SYSTCO'D : SYSTCO'D, 255: REM FILL BITHAP WITH I'S ("SET" BACKGROUND)
770 PURE BU,8 : REM BLACK BORDER TO MATCH BACKGROUND
770 PURE BU,8 : REM BLACK BORDER TO MATCH BACKGROUND
770 REM STORM STOR
                 870 PORE UB, 2351515100, 110, 221, 110, 221, 110, 110
890 : B90 REM DRAW WINE GLASS
900 POKE UB, 20:595(SP), 80, 120, 60
910 POKE UB, 23:5760KE DB, 34:595(V5), 80, 34, 10, 110
920 : 930 REM DRAW SOME GRAPES
940 SYS(SP), 8, 8, 8: REM OTHER "GRAPES" WILL BE SAME RADIUS - JUST GIVE POSITIONS
950 SYS(SP), 20, 8: SYS(SP), 40, 8:5YS(SP), 12, 20:5YS(SP), 30, 20:5YS(SP), 25, 16
940 REM DRAW AN APPLE BY A PAIR OF EDDE-VIEW TOROIDS AND A SPHERE SECTION
980 POKE UB, 255:POKE DB, 255:POKE LB, 255:POKE RB, 59
979 SYS(VT), 260, 29, 0, 50:5YS(VT), 260, 79
1800 POKE UB, 43:FOKE DB, 43:SYS(SP), 260, 546, 50
1810 REM PUT STEM ON AFPLE
                    1000 POKE UB, 43:FOKE DB, 45:SYS(SP), 260,54,60
1010 REM PUT STEM DN APPLE
1020 POKE RB,0:FOKE DB,0:SYS(TR),272,104,10,15
1030 REM ADD A LEAF BY A SPHERE SECTION
1040 POKE DB,255:FOKE RB,0:SYS(SP),256,119,15
1050 REM ADD A RANDOM SHADED "LABEL" TO THE SOTTLE
1060 POKE UB,255:POKE KB,255:FOKE LB,6
1050 REM SSH,0:SYS(SP),255:POKE LB,6
1050 REM SSH,0:SYS(SP)
1618 END
1620: 1618 END
1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620: 1620:
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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Six

```
1760 POKE 34752+1X-40*1Y,CC
1770 NEXT:NEXT:RETURN
1780:
1790 REM SUBROUTINE TO ADD TEXT TO GRAPHIC SCREEN.
1800 REM "RW" AND "CH" ARE THE ROW (0-24) AND COLUNN (0-39) COORDINATES FOR THE
1810 REM FIRST LETTER OF THE TEXT STRING TO BE PRINTED.
1820 REM "THE TEXT STRING ITSELF; IS ASSIGNED TO "AS".
1830 REM "MD" IS THE MODE FOR THE PRINTING. FIVE MODES ARE ALLOWED:
1840 REM "I - NORMAL "BLACK" LETTERS ON "WHITE" BACKGROUND)
1850 REM 2 - REVERSED ("WHITE" LETTERS ON "WHITE" BACKGROUND)
1850 REM 3 - SET ("BLACK") LETTERS ON "BLACK" BACKGROUND)
1870 REM 4 - UNSET ("WHITE") LETTERS "AND "ED" WITH BACKGROUND
1870 REM 4 - UNSET ("WHITE") LETTERS "AND "ED" WITH BACKGROUND
1870 REM 5 - SET LETTERS "XOR "ED" WITH BACKGROUND
1870 IS BA40952:TB=54272:IF (MD AND 1) THEN TB=53248:REM SCREEN AND TEXT BASE ADDR'S
1910 OS=320*RW-8-CH:REM OFFSET FROM CHARACTER SCREEN BASE
1910 OS=320*RW-8-CH:REM OFFSET FROM CHARACTER SCREEN BASE
1912 POKE 54334,FEET (56334) AND 254:REM DISABLE IRD THER
1930 POKE 1,FEEK(1) AND 251:REM SWITCH CHARACTER ROM IN
1940 L=LEMIA):FOR N=1 TO L:NB#10+0505-98
1940 POKE 5431,17:GOTO 2020
1940 POKE 5231,17:GOTO 2020
1940 POKE 5321,17:GOTO 2020
1940 POKE 5321,
```

Listing Seven

```
18 REM "STELLATION" DRAW A SMALL STELLATED DDDECAHEDRON IN VARIOUS ORIENTATIONS
20 REM AND STYLES RICHARD L. RYLANDER 12/5/84
30 : REM 93789 : REM GRAPHICS MODE
40 REM-493789 : REM FACH COLOR
40 EN-3280 : REM TEXT MODE
40 B0-53280 : REM BORDER COLOR
70 :
80 REM INITIALIZE A FEW STYLE PARAMETERS
90 POKE 879.1 : REM SCALE (3:4) FOR UNDISTORTED SCREEN DISPLAY
100 POKE 871.0 : REM FACHET EDGE/LINE HODE (0-DRAW, 1-ERROSE)
110 SH-030 : REM FACHET EDGE/LINE HODE (0-DRAW, 1-ERROSE)
120 EG-9640 : REM EDGES FLAG (0-NORMAL, 1-ADD LINES TO FACHT EDGES)
130 :
140 REM FUNCTION LOCATIONS
150 CL-51979 : REM CLEAR BITMAP
  130 : 140 REM FUNCTION LOCATIONS
150 CL=51979 : REM CLEAR BITMAP
160 CO=52001 : REM FILL COLOR MAP
170 FC=52052 : REM DRAW A SHADED FACET
180 KS=53081 : REM DO KEYED SORT
190
          :
XC=160:YC=120 :REM (SCALED) SCREEN CENTER COORDINATES
 520 :

530 :

530 PA=60: REH TOTAL NUMBER OF FACETS

530 DIM FK(FA/2,2), SH(FA/2), 7%(FA/2), K%(FA/2)

550 PRINT" READING CONNECTION DATA

560 PF=1: REM OF = # VISIBLE FACETS

570 FOR N=1 TO FA
800 REM FIND AVERAGE Z FOR EACH FACET
 818 FOR N=8 TO VF
828 ZX(N)=(PX(FX(N,8),2)+PX(FX(N,1),2)+PX(FX(N,2),2))/J:NEXT
  840 PRINT" SORTING FACETS ACCORDING TO AVG 'Z' "
 950 POKE 140,VF
950 POKE 140,VF
960 KX(0)=KX(0):POKE 251,PEEK(71):POKE 252,PEEK(72)
970 ZX(0):POKE 253,PEEK(71):POKE 254,PEEK(72)
980 SYS(KS)
990 :
```

Listing Eight

```
00001
00002
00003
00003
00004
00005
00006
00007
00018
00012
00013
00014
00015
00016
00017
00018
                                                             KEYSORT - RELOCATABLE BUBBLE SORT USING KEY ARRAY POINTING TO INTEGER ARRAY
                RICHARD L. RYLANDER 1/12/85
                                                                                                          ; 53081. (FOLLOWING DOS 5.1)
                                                                        = $FB
= $FD
                                                                                                             ; 251. POINTER TO KEY ARRAY
; 253. POINTER TO DATA ARRAY
; 140. POKE WITH MAX ARRAY INDEX
                                                         ZB = $FD
MAX = $BC
TOP = $AC
TOPDIS = $AD
FLAG = $AE
NXTFLG = $61
CRRNT = $62
REPEAT = $64
                                                                   *=ORIGIN
                             AØ FF
CB
98
91 FB
C5 8C
DØ F8
                                                         ;
INIT LDY ##FF
INLOOP INY
TYA
STA (KB),Y
CMP MAX
                                                                                                             ; INITIALIZE KEY ARRAY
                CF59
CF5C
CF5C
CF5D
CF5F
CF61
CF63
CF63
 00021
  00022
  00023
                                                                         DNE INLOOP
 00025
  00026
                                                        SORT STA TOPDIS
LOOP1 LDA TOPDIS
STA TOP
LDX #0
STX NXTFLG
STX FLAG
LOOP2 STX REPEAT
 00027
                                                                                                             : 'A' HOLDS 'MAX'
 00028
00029
               00030
 00031
 00032
00033
 20034
                                                         ; GET BOTH BYTES OF INTEGER POINTED TO BY
; 'KEY' ELEMENT. RETURN WITH MSB ON STACK
; AND LSB IN THE ACCUMULATOR
 00035
00036
 00037
 90038
                                                       GETINT TXA
TAY
LDA (KB),Y
ASL A
BCC LOAD
DEC NXTFLG
INC ZB+1
LDAD TAY
LDA (ZB),Y
PHA (ZB),Y
 00040
 00041
 00042
 00043
 00044
 00045
 00045
00046
00047
00048
                                                                      PHA
INY
LDA (ZB),Y
BIT NXTFLG
BPL NODEC
INC NXTFLG
DEC ZB+1
CPX REPEAT
BNÉ ORDER
STA CRRNT+1
INX
 00049
 00050
 00051
 00052
 00053
00054
 00055
                                                        NODEC
 00056
 00057
00058
00059
00060
00061
00062
00063
00064
00065
                                                                         INX
BNE GETINT
                             DØ DA
                                                            COMPARE INTEGERS OBTAINED THROUGH KEY ARRAY IF 'CURRENT' >* 'NEXT' THEN SWAP KEY ELEMENTS, ELSE CONTINUE
 00066
                                                        00067
 00048
 88869
 00070
 00071
 00072
 00073
00074
 00075
              CFA4
CFA6
CFA9
CFAA
CFAC
CFAD
CFAF
CFAF
CFB0
00075
00076
00077
00079
00089
00081
00082
00083
00084
                             88
B1 FB
                                                                        DEY
LDA (KB),Y
                            C8
91
68
                                                                        STA (KB) .Y
                                                                        DEY
STA (KB),Y
```

(Continued on page 82)

Dr. Dobb's Journal

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Drawing on the C-64 (Listing Continued, text begins on page 50) Listing Eight

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	90086	CFB5		AC	NOSWAP	CPX	TOP					
6	00087	CFB7	DØ	86		BNE	L00P2					
	90088	CFB9		AE		LDA	FLAG					
8	00089	CFBB	DØ	AB		BNE	LOOP1					
6	00070	CFBD										
6	00091	CFBD				CK TI	HE BYTE	ELEM	ENTS	OF THE	'KEY' ARR	۵v
6	00092	CFBD			INTO	RAS	IC'S NOE	IAMS	2-BV	TE INTER	SER FORMAT	-
Q	10093	CFBD			1			11 17 162		16 114120	LIN FUNITHI	
6	00074	CFBD	86	BC BC	UNFACK	LDY	MAY					
8	00095	CFBF	E8			INX						
2	00096	CECØ	CA		PKLOOP							
0	0097	CFC1	BA		, ,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	TXA						
	10098	CFC2	AB			TAY						
	00079	CFC3		FB			(VD) V					
	0100	CFC5					(KB),Y					
	00101	CFC6	88			PHA						
	0102					TXA						
	00102	CFC7				ASL			3 M	OVE TO 2	* I + 1	
			09			DRA						
	0104	CFCA					STORE					
	0105	CFCC	E6				NXTFLG					
	0106	CFCE					KB+1					
	00107	CFD0			STORE	TAY						
	0106	CFD1				FLA						
	0109	CFD2					(KB),Y					
	0110	CFD4		88		LDA	#12					
	00111	CFD6				DEY						
	0112	CFD7				STA	(KB),Y					
	0113						NXTFLG					
		CFDB				BEQ	DK					
	0115	CFDD				DEC	NXTFLG					
		CFDF		FC		DEC	KB+1					
		CFE1			OK	TXA						
	0118	CFE2		DC		BNE	PKLOOP					
	0119	CFE4	60		DONE	RTS						
0	0120	CFE5			. END							
Ε	RRORS	- 900	900									
s	YMBOL	TABLE										
s	YMBOL	VALUE										
	CRRNT		162	DONE	CFE4		LAG	ØØA	-	GETINT	CETA	
	INIT		59	INLOOP			В	ØØFI		LOAD	CF71	
	LOOP1		65	L00F2	CF4F		1AX	0080			CF7C	
	NOSWAP		85	NXTFLG	0061		IM X IK			NODEC	CFBB	
	ORIGIN		59	PKLOOP	CFC0		EPEAT	CFE!		DRDER	CF97	
	STORE		DØ	SWAP	CFA2			0064		SORT	CF63	
	TOPDIS		AD	UNPACK	CFBD		EST	CFA		TOP	BOAC	
				WITHER.			B	ØØF1				

Listing Nine

00001				3 "1	RITE	" RICHAR	D L. RYLANDER
00002				3		12/30/	84
00003				; REVI	SED	1/19/85	- ORIGIN MOVED TO \$CFE5 (53221.
00004				3			
00005				; PUT	TEX	T CHARACTE	RS ON GRAPHIC SCREEN
90000				3 (LIN	DER I	BASIC ROM)	IN VARIOUS STYLES
00007				3			
00000				*=\$CFE	5		; PUT CODE AFTER DOS 5.1 ; SWITCH OUT BASIC ROM
00009				WRITE	LDA	#01	SWITCH OUT BASIC ROM
00010					AND	##FE	
00011					STA	\$01	
00012					LDY	#7	
00013	CFED	B1	FD	LOOP	LDA	(#FD).Y	; READ CHARACTER BYTE ; MODIFY W/SCREEN BYTE ; STORE IN SCREEN
00014	CFEF	31	FB		AND	(#FB).Y	MODIEY WASCREEN BYTE
00015	CFF1	91	FB		STA	(#FB) . Y	STORE IN SCREEN
20216	CFF3			2		14, 5, 1,	, STORE IN SCREEN
00017	CFF3			POKE	MELL	LOGICAL O	PERATOR TO REPLACE
00018							DIFFERENT STYLES
00019	CFF3						=36. AND=49. EOR=B1.
00020				3	17.	DIT (NUF)	-30. HND=47. EUR=81.
00021				,	DEY		
00022	CEEA	10	F7			LOOP	
00023	CEEA	7.60	m.				
00023	CEEO	MO	01		LDA	301	; RESTORE BASIC ROM
00025	CFFO	07	01			#1	
20026						\$01	
					RTS		
00027	CFFD			.END			
ERRORS	= 000	00					
SYMBOL	TABLE						
SYMBOL	VALUE						
LOOP	CF	ED	WRITE	CFE5			

End Listings

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A Compiler Written in Prolog

by G. A. Edgar

hy would anyone write a compiler in a language like Prolog? Actually, the compiler seems to have come out rather well. I wrote it primarily as a learning exercise for Micro-PROLOG.

Micro-PROLOG

I first heard of the Prolog programming language in connection with the Japanese effort to develop the "fifth generation" in computing. At the time, I read a few things about Prolog, such as the book by Clocksin and Mellish. My impression was that Prolog is an interesting language (I was right), but that it probably is not practical to implement PROLOG on a microcomputer (was I ever wrong!).

substantial effort for a programmer experienced only in Pascal, C, or PL/I. The tutorial² is published separately as a book; if you are curious, it should be available in most large university libraries.

After I began to get a feel for the language, I used it to solve a few logic puzzles. For example, Lewis Carroll's logic book³ includes some puzzles at the end as a teaser for a later book that was never published. They are much too complicated to solve simply by common sense reasoning, but I can now say definitively that "All of the Monitors are awake."

If Prolog were good only for logic problems, it would not be of wide interest. So I looked around for some other relatively easy task to use as a

The early returns on our March special issue on Prolog are encouraging—so much so that we're offering this excursion into programming in logic.

D. E. Cortesi, in "Dr. Dobb's Clinic" for May 1984, gave a half-page description of Micro-PROLOG. I wrote for information on it and by July was running it on my CP/M Z80 computer. Micro-PROLOG is also available for MSDOS, PCDOS, CP/M-86, and Unix.

Micro-PROLOG comes with an IBM PC-size loose-leaf manual with more than 200 pages. This is a complete reference to the language but not a good tool for learning. Micro-PROLOG, however, also comes with a 400-page soft-cover tutorial, which is a good place for a beginner to start. Learning the language will take a

programming exercise. A compiler for VALGOL fit the need nicely. It improved my understanding of Micro-PROLOG and my understanding of what a compiler does.

This compiler is described below. It translates VALGOL I, also described below, into 8080 assembly language. Of course, you would learn more by undertaking such a programming project yourself than you will by examining mine, but it serves as proof that you can use Micro-PROLOG for something unexpected.

Using Micro-PROLOG

Before I describe the compiler, let me give a brief description of how Micro-PROLOG differs from other versions of Prolog.

G. A. Edgar, 107 W. Dodridge St., Columbus, OH 43202

Upper- and lower-case letters are considered different, and the hyphen is treated like a letter. Variables are X, Y, Z, x, y, z, or one of these six letters followed by a positive integer, such as X132 or z22. Other strings of letters and numbers are identifiers for constants.

Micro-PROLOG consists primarily of a pattern matcher, plus a few other built-in routines, such as a routine for I/O. The way Micro-PROLOG looks depends on which of the front ends you are using. Each front-end program is written in Prolog. If you are using the SIMPLE front end, one-place predicates can be given either in prefix form

alphanumeric(X)

or in postfix form

X alphanumeric

Two-place predicates can be given either in prefix form

LESS(@X)

or in infix form

@ LESS X

Predicates with three or more arguments must be in prefix form. Thus, a Prolog program entered via SIMPLE tends to look like this:

labelused (0)

X alphanumeric if X alphabetic

X alphanumeric if X digit

X alphabetic if @ LESS X and X LESS [

X alphabetic if 'LESS X and X LESS {

One useful object in Micro-PRO-LOG is the list. The notation for a list is to enclose the items between parentheses: (A B C 1 2 3). Some of the items might be lists themselves: (A (BC) (1 2 3)).

The 16-bit versions of Micro-PRO-LOG include a front-end supervisor that imitates the DEC-10 Prolog:

labelused(0). alphanumeric(X):- alphabetic (X). alphanumeric(X):- digit(X).

The form in which an operator is used is declared using the types fx, xf, xfy, and so on (see Clocksin and Mellish, page 91). DEC-10 lists are built using square brackets: [a,b,c].

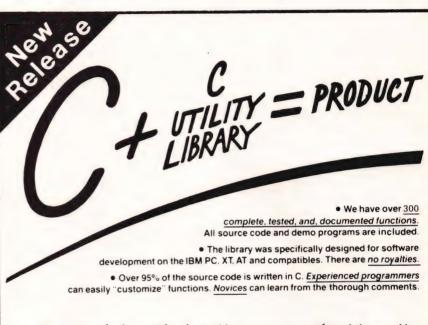
The standard Micro-PROLOG syntax simply uses lists. In this regard, it is somewhat like LISP. The list (labelused 0), which indicates the predicate name "labelused" applied to the argument "0," is an example of an atom. A clause is a list of atoms.

You add things to the data base in this way:

((labelused 0)) ((alphanumeric X) (alphabetic X)) ((alphanumeric X) (digit X))

This may seem confusing at first, but after a while it becomes more or less routine.

This minimal introduction to the Micro-PROLOG interpreter should be enough for you to gain some understanding of the VALGOL compil-



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er, but you will need a much more complete knowledge of Micro-PRO-LOG to understand all of the details.

VALGOLI

The language VALGOL I (very small ALGOL?) is a derivative of ALGOL-60. D. V. Schorre described it in 1964 as a sample language for the compiler-writing language META II. His paper⁴ was reprinted in *Dr. Dobb's Journal* (April 1980), which is where I found it. I had worked with VAL-GOL I in connection with the compiler-writing language Meta4 (in a SIG/M disk), so it seemed natural to use it for the same thing in Prolog.

VALGOL I has a few peculiarities. The keywords that usually are typeset in boldface here are preceded by a period (.begin, .end, .if, .then, .else, .until, .do, .integer). This also applies to the equals sign for comparing two expressions (.=). VALGOL I has only one data type, namely .integer, a 16-bit two's-complement number. The assignment statement is the reverse from the normal order (5 =: xassigns the value 5 to x). Arithmetic allows addition, subtraction, and multiplication, but there is no unary minus sign, so if you want - 2 you must write either 0 - 2 or 65534.

A more complete description of the syntax appears below in the description of the compiler.

Using the VALGOL I Compiler

The VALGOL I compiler is in a file called VALGOL.LOG. A typical use, to compile the short VALGOL program in the file P.VAL, is shown in Listing One (page 89). The command shown is in the standard Micro-PROLOG syntax. If the SIMPLE front end is also loaded, you could use the command:

is "P.VAL" compile "P.ASM")
The compiler prints out the input file
P.VAL on the console as it works. The
result of the compilation in the sample is P.ASM, included here as Listing
Four (page 96).

If you are familiar with the Intel mnemonics for the 8080, you may find it instructive to compare the VALGOL input with the assembly language output to see how the compiler works.

The VALGOL Definition

Take a look at the compiler in Listing Two (page 90). I have repeated part of the compiler using the SIMPLE syntax in Listing Three (page 96). SIMPLE is unsuitable for the complete listing for several reasons. In a few places in the compiler, I have used "meta-variables," which are not supported by SIMPLE. A SIMPLE listing also would not keep the comments and the formatting, making it confusing to read. So try to understand instead the list notation used for the standard syntax of Micro-PROLOG in Listing Two.

The lists preceded by a ? are to be executed immediately when they are loaded; the others are to be stored in the data base. Because the predicate symbol (/ *) is a special one that is always true, a list in the file of the form

?((/ * anything goes here))
will serve as a comment. (This is a
clumsy feature of Micro-PROLOG.)

The compiler listing is in two parts. The first part is independent of the particular syntax of VALGOL I and could be used equally well with many other programming languages. The second, below a horizontal line, is a VALGOL-specific part. In one sense, the part above the line implements an interpreter that runs the compiler described below the line. Let's examine a portion of the VALGOL-specific part of the code.

The message line, like all these lines, becomes a statement in the Prolog data base. The predicate symbol of the statement is "message," and the single argument is the string enclosed in quotation marks. The compiler will look for a message fact in the data base and print it out when a compilation starts.

The next line is the syntax line. This tells the compiler that the primary unit of syntax is the program.

The next statement is a description of a program. First comes the keyword begin. When this is matched, seven opening lines are sent to the output file. (I have put the VALGOL syntax description on the left-hand side of the page and the ASM output description on the right-hand side. A VALGOL I compiler for another processor would

keep the same left-hand side and change the right-hand side.) Then an opt-declaration (optional declaration, see below) is followed by one or more statements separated by semicolons. The multiple predicate allows matching of its arguments zero or more times. Finally comes the keyword end. When this is matched, the final JMP is inserted in the output file, followed by the four subroutines and the stack space.

The opt-declaration could be a declaration followed by a semicolon, or (if that cannot be found) it could be empty, which matches the input file automatically. Inclusion of the "empty" alternative is a way to allow for no declaration. A declaration is the keyword integer, followed by one or more identifiers separated by commas. For each identifier, the output file reserves a two-byte storage location.

A statement is one of the following: (1) An I/O statement, either

edit(expression, 'string')

which will send (expression) spaces and then the (string) to the console, or

print

which will send an end-of-line to the console.

(2) An assignment statement:

expression =: variable

(3) A loop:

.until expression .do statement

A value of zero for the expression is considered false, and a nonzero value is true.

(4) A conditional statement:

.if expression .then statement .else statement

The .else is not optional.

(5) A block, which consists of the keyword begin, followed by an optional declaration, followed by zero or more statements separated by semicolons, followed by the keyword

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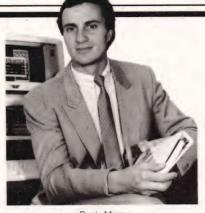
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.end. The null statement

.begin .end

is allowed.

The following test for equality is allowed:

expression .= expression

This has value 1 if true and 0 if false. Expressions can be built up from variables, numbers, the operations +, -, *, and parentheses. Notice how the syntax near the end of Listing Two is arranged to take into account the precedence of these operations.

The Language-Independent Part

Now let's look at the first part of Listing Two (part of this is repeated in SIMPLE syntax as Listing Three). When Micro-PROLOG attempts to verify a compile atom, it looks first in the data base for a message fact; if it finds one, it verifies the assertion (P x), which is always true but which has the side effect of printing x on the console. The assertion PP is also always true and starts a new line at the console. The compile atom continues as it prints $X \rightarrow Y$, where X is the first argument (presumably the filename of the VALGOL input file) and Y is the second argument (the ASM output file).



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Next the compiler deletes all facts about "labelused" (which may be left from a previous compile) and starts over with (labelused 0). It does something similar with "last-shown," which keeps track of how much of the file has been shown on the console.

Then the compiler opens the input file X and adds a fact to the data base so that later it can find the name of the input file. Similarly, it opens the output file Y. It skips white-space characters in the input file, starting at the beginning and ending at location Z1 (x in Listing Three). Then it finds the syntax fact to tell it what to start compiling with and feeds that to the Q routine, which carries out the actual compile.

Finally, the compiler closes the files and prints out a concluding message. If compilation fails for some reason, control goes to the second compile clause, and it prints out the error message.

Now look at Q. This short routine implements the recursive-descent compile, according to the code in the last half of the compiler. The first two arguments are the before and after file positions for both the input and output files. Whenever Q has to backtrack, the files automatically are rewound to the appropriate point for another try.

Notice the use of the variable Z (the third argument of Q) as the predicate symbol in a search of the data base. This is called a "metavariable" in Micro-PROLOG: the name of the predicate to be verified is supplied as a variable, not as a constant. This powerful feature of Micro-PROLOG may not be available in other Prologs. The X1 after the vertical line, which indicates the rest of the list, is another meta-variable. The remainder of this half of the code comprises various subroutines for use in the compile.

This is a very brief description of only a small part of this compiler. A complete understanding requires a greater familiarity with the many features and quirks of the Micro-PROLOG package.

Conclusion

The compiler shown in Listing Two is

written in the standard syntax of Micro-PROLOG. As such, it should run under any version of Micro-PROLOG. I would be interested in hearing from readers who get it to work under other versions. (Of course, the output would still be in 8080 assembly language.)

This compiler runs rather slowly. The common belief is that interpreters are slow, and this is an interpreter within an interpreter. The 10-line sample compilation in Listing One takes about 30 sec on my 4 MHz Z80. Because I wrote the compiler primarily as a programming exercise, I have not been overly concerned with a remedy for the speed problem. Probably the bottleneck occurs in the input routines that process one character at a time. For a faster compiler, routines such as match, id, or number would be recoded in assembly language. The Micro-PROLOG manual has instructions on how to link such new built-in functions in assembly code with Micro-PROLOG.

References

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- 3. Lewis Carroll, *Symbolic Logic*, 4th edition, 1896 (Reprinted by Dover Publications, 1958).
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Compiler in Prolog (Text begins on page 84) Listing One

Use of the compiler

```
B>PROLOG LOAD VALGOL
micro-PROLOG 3.1 for RML 380/480Z 29/03/84
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&&.?((compile "P.VAL" "P.ASM"))
VALGOL I compiler - translates VALGOL to ASM P.VAL -> P.ASM
.begin
.integer x;
0 =: x;
.until x .= 15 .do
      .begin
      edit (1+14*x-x*x, '+');
      print:
      x+1 =1 x
      · end
· end
** Compilation of "P,VAL" complete **
.TD.3
BOASM P.BBZ
CP/M ASSEMBLER - VER 2.0
01F7
001H USE FACTOR
END OF ASSEMBLY
BOLDAD P
FIRST ADDRESS 0100
LAST ADDRESS 01F6
BYTES READ
              0.089
RECORDS WRITTEN 02
B) F
type p.val
.begin
integer x ;
0 =: x ;
.until x .= 9 .do
        begin
        edit ( x*x + 1 , '+' ) ;
        print;
        x + 1 =: x
        end
erid
```

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End Listing One

(Listing Two begins on next page)

A>p

+

+

Compiler in Prolog (Listing Continued, text begins on page 84) Listing Two

VALGOL.LOG

```
?(( /x
   VALGOL . LOG
   Compiler for VALGOL I -- ver, 1.2
   Written in Micro-PROLOG.
   (Tested with Micro-PROLOG version 3.1 for CP/M-80)
   Translates VALGOL I to ASM-compatible 8080 assembly language.
   Uses file SEEKs for backtracking.
         written bg: G. A. EDGAR
                                       status: public domain
   0.3
            September 9, 1984
            September 16, 1984
November 1, 1984
   1.0
                                       output for ASM
   1.1
                                       display input
           November 10, 1984
                                       version for DDJ
   1.2
   Usage - in SIMPLE syntax:
         &.is("FOO.VAL" compile "FOO.ASM")
          - in standard syntax:
         &.?((compile "F00.VAL" "F00.ASM"))
))
?(( /* ----- Language independent part -----))
?(( /* compile: input X, output Y))
((compile X Y)
    (message x) (P x) (PP) (P X "->" Y) (PP) (PP)
    (KILL labelused) (ADDCL ((labelused 0)) )
(KILL last-shown) (ADDCL ((last-shown (-1|-1))) )
    (OPEN X) (KILL infile) (ADDCL ((infile X)) )
(erase Y) (CREATE Y) (KILL outfile) (ADDCL ((outfile Y)) )
    (skip ((0|0)|(0|0)) Z1 ) (syntax z) (Q Z1 Z z)
    (CLOSE X) (CLOSE Y)
    (PP) (PP ** Compilation of X complete **))
((compile X Y)
    (PP) (PP ** Syntax error **)
    (CLOSE X) (CLOSE Y))
((C X Y)
    (compile X Y))
?(( /* Q: find it in the language-specific database))
((Q X Y Z)
    (Z | X1) (sequential X Y | X1))
((sequential X X))
((sequential X Y (z|Z)|y)
    (z X X1|Z) / (sequential X1 Y|y))
?(( /* out: send a line to outfile))
((out (91|z1) (91|z2) | X)
(outfile Z) (SEEK Z z1) (outx Z | X)
(outx Z "~M~J") (SEEK Z z2))
((outx Z))
((outx Z X | x)
    (W Z (X)) / (outx Z | x))
?(( /* match: the input matches string Z))
    (STRINGOF Z1 Z) / (matchx X X2 Z1) (skip X2 Y))
((matchx X Y (x|Z))
    (inchar X X1 x1) (EQ x x1) / (matchx X1 Y Z))
((matchx X X ()))
?(( /* empty: matches automatically))
((empty X X))
?(( /* multiple: match the following zero or more times))
((multiple X Y|Z)
    (once X X1|Z) / (multiple X1 Y|Z))
((multiple X X|Z))
((once X X))
((once X Y (z|Z)|9)
    (z X X1|Z) / (once X1 Y|y))
?(( /* label: generate a new label))
((label X X Y)
    (labelused Y1) (SUM Y1 1 Y) (KILL labelused)
    (ADDCL ((labelused Y)) ))
```

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Compiler in Prolog (Listing Continued, text begins on page 84) Listing Two

```
?(( /* string: match a string quoted within characters x))
((string X Y x Z)
    / (inchar X X2 x1) (EQ x x1) / (stringx X2 X3 x Z1) (skip X3 Y) (STRINGOF Z1 Z))
 ((stringx X Y x ())
    (inchar X Y x1) (EQ x x1) / )
 ((stringx X Y x (g | Z))
    (inchar X X1 y) / (stringx X1 Y x Z))
?(( /* id: match an identifier))
((id X Y Z)
     (idx X X2 Z1) (skip X2 Y) (STRINGOF Z1 Z))
 ((idx X Y (x|Z))
    (inchar X X1 x) (alphabetic x) / (alphanum X1 Y Z))
 ((alphanum X Y (x|Z))
     (inchar X X1 x) (alphanumeric x) / (alphanum X1 Y Z))
((alphanum X X ()))
?(( /* number: match a number))
((number X Y Z)
     / (numberx X X2 Z1)
    (NOT EQ Z1 ()) (skip X2 Y) (STRINGOF Z1 Z))
 ((numberx X Y (x|Z))
    (inchar X X1 x) (digit x) / (number x X1 Y Z))
((numberx X X ()))
((alphanumeric x)
    (alphabetic x))
((alphanumeric x)
    (digit x))
((alphabetic x)
(LESS "@" x)(LESS x "["))
((alphabetic x)
(LESS "\" x)(LESS x "C"))
((digit x)
     (LESS "/" x)(LESS x ":"))
?(( /* skip: skip over characters listed as "skipchar"))
((skip X Y)
    (inchar X X1 x) (skipchar x) / (skip X1 Y))
((skip X X))
((skipchar " "))
((skipchar "~I"))
((skipchar "~J"))
((skipchar "~M"))
?(( /* inchar: input one character, show on console first time))
((inchar (y1|z1) (y2|z1) x)
(infile Y) (SEEK Y y1) (RDCH Y x) (SEEK Y y2) (last-shown y3)
    (IF (before 93 92)
((P x) (KILL last-shown) (ADDCL ((last-shown 92)) ))
      ()))
?(( /* before: compare two file positions ))
((before (y1|z1) (y1|z2))
    / (LESS z1 z2))
((before (y1|z1) (y2|z2))
    (LESS 91 92))
?(( /* erase: erase a file, no failure))
((erase Y)
    (ERA Y) /)
((erase Y))
?(( /* --- VALGOL specifics ----- ))
((message "VALGOL 1 compiler - translates VALGOL to ASM"))
((syntax PROGRAM))
((PROGRAM
    (match ".begin")
                                           "UCPM
                                  (out
                                                   EQU
                                                            0")
                                          "VBD0S
                                                            5")
                                  (out
                                                   EQU
                                           "VTPA
                                                            256")
                                  (out
                                                   EQU
                                          "VCR
                                  (out
                                                   EQU
                                                            13")
                                          "VLF
                                                            10")
                                  (out
                                                   EQU
                                  (out
                                                   ORG
                                                            VTPA")
                                  (out
                                                   LXI
                                                            SP, VSTACK")
    (Q OPT-DECLARATION)
    (Q STATEMENT)
```

(multiple
 (match ";")
 (Q STATEMENT))
(match ".end")

Cout	**	JMP	VCPM")
(out	"VMULT:	")	
(out	11	MOV	B,H")
(out	11	MOV	C, L")
Cout	11	XRA	A")
(out	11	MOV	H,A")
(out	11	MOV	L,A")
(out	11	MVI	A,16")
(out	"VMULT1:	PUSH	PSW")
(out	11	DAD	H")
(out	11	XRA	A")
(out	11	MOV	A,C")
(out	***	RAL")	
(out	11	MOV.	C,A")
(out	11	MOV	A,B")
(out	н	RAL")	
(out	11	MOV	B,A")
(out	**	JNC	VMULT2")
(out	**	DAD	D")
(out	"VMULT2:	POP	PSW")
(out	**	DCR	A")
(out	**	ORA	A")
(out	11	JNZ	VMULT1")
(out	11 -	RET")	
(out	"VEDIT:	")	
(out	11	MOV	A,H")
(out	11	ORA	L.")
(out	**	JZ	VEDIT1")
(out	**	MVI	A, (")
(out	**	CALL	VCPMOUT")
(out	11	DCX	H")
(out	11	JMP	VEDIT")
(out	"VEDIT1:	POP	H")
(out	"VEDIT2:	NOV	A,M")
(out	11	CF'I	0")
(out	**	INX	H")
(out	**	JZ	VEDIT3")
(out	14	CALL	VCPMOUT")

(Continued on next page)



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```
(out
                                                       JMF
                                                                VEDIT2")
                                     (out
                                            "VEDIT3:
                                                       PUSH
                                                                H")
                                     (out
                                                       RET")
                                     (out
                                              "VPRINT:")
                                                                A.VCR")
                                     (out
                                                       MVI
                                              11
                                                                VCPMOUT")
                                     Court
                                                       CALL
                                              31
                                                                A, VLF")
                                     (nut.
                                                       MUT
                                                                VCFMOUT")
                                     Court
                                                       CALL
                                                       RET")
                                     (out
                                              "VCPMOUT:")
                                     (out
                                              **
                                     (out
                                                       PUSH
                                                                H")
                                              **
                                     (out
                                                       MOV
                                                                E,A")
                                              **
                                                                C,2")
                                     (out
                                                       IVM
                                              11
                                                                VBDOS")
                                     (out
                                                       CALL
                                              11
                                     (out
                                                       POP
                                              ..
                                                       RET")
                                     (out
                                              11
                                                                60")
                                     (out
                                                       DS
                                            "VSTACK:
                                                       DW
                                                                0")
                                     (out
                                                       END") ))
                                     (out
 ((OPT-DECLARATION
     (Q DECLARATION)
     (match ";")))
((OPT-DECLARATION
     (empty)))
((DECLARATION
     (match ".integer")
                                     (label X1)
                                     (out
                                                       JMP
                                                                V" X1)
     (Q ID-SEQUENCE)
                                             "V" X1 ":") ))
                                     (out
((ID-SEQUENCE
     (Q IDENTIFIER)
     (multiple
         (match ",")
          (Q IDENTIFIER) )))
((IDENTIFIER
     (id Z)
                                            Z "V:
                                                               2") ))
                                    (quit.
                                                      DS
((STATEMENT
    (Q IO-STATEMENT)))
((STATEMENT
    (Q ASSIGNMENT-STATEMENT)))
((STATEMENT
    (Q UNTIL-STATEMENT)))
((STATEMENT
    (Q CONDITIONAL-STATEMENT)))
((STATEMENT
    (Q BLOCK)))
((BLOCK
    (match ".begin")
    (Q DECL-DR-ST)
    (multiple
         (match ";")
    (Q STATEMENT) )
(match ".end") ))
((BLOCK
    (match ".begin")
(match ".end") ))
((DECL-OR-ST
    (Q DECLARATION)))
((DECL-OR-ST
    (Q STATEMENT)))
((ID-STATEMENT
    (match "edit")
(match "(")
    (Q EXPRESSION)
    (match ",")
(string "'" Z)
                                    (out
                                                      CALL
                                                               VEDIT")
                                             11
                                                               (" Z "', 0")
                                    (out
                                                      DB
    (match ")") ))
((IO-STATEMENT
    (match "print")
                                    (out
                                                               VPRINT") ))
                                                      CALL
((CONDITIONAL-STATEMENT
    (match ".if")
                                    (label X1) (label X2)
    (Q EXPRESSION)
    (match ".then")
                                                               A,H")
                                    (out
                                                      MOV
```

```
(out
                                                     ORA
                                                              L")
                                                              V" X1)
                                   (out
                                                     JZ
    (Q STATEMENT)
                                                              V" X2)
    (match ".else")
                                                     JMP
                                   Court
                                            "V" X1 ";")
                                   (out
                                            "V" X2 ":") ))
    (Q STATEMENT)
                                   (out
((UNTIL-STATEMENT
    (match ".until")
                                   (label X1) (label X2)
(out """ X1 ":")
                                   (out
    (Q EXPRESSION)
                                                              A,H")
                                                     MOV
    (match ".do")
                                   (out
                                            11
                                   (out
                                                     ORA
                                                              V" X2)
                                            11
                                                     JNZ
                                   (out
                                                              V" X1)
    (Q STATEMENT)
                                    (out
                                                     JMP
                                            "V" X2 ";") ))
                                    (out
((ASSIGNMENT-STATEMENT
     (Q EXPRESSION)
     (match "=:")
                                                               " Z "V") ))
                                                    SHLD
     (id Z)
                                    (out
((EXPRESSION
     (Q EXPRESSION1)
     (Q OFT-RIGHT-SIDE)))
((OFT-RIGHT-SIDE
    (match ".=")
                                    (label X1) (label X2)
                                                               H")
                                    (out
                                                      POP
                                                               D")
     (Q EXPRESSION1)
                                    (out
                                                               A,L")
                                                      MOV
                                    (out
                                                               E")
                                                      SUB
                                    (out
                                                               V" X2)
                                    (out
                                                      .IN7
                                                      MOU
                                                               A, H")
                                    (out
                                                               D")
                                    (out
                                                      SEE
                                                               V" X2)
                                    (out
                                                      JNZ
                                                               H,1")
                                    (out
                                                      LXI
                                             11
                                                               V" X1)
                                    (out
                                                      JMF
                                             "V" X2 ":")
                                    (out
                                                     LXI
                                                               H,0")
                                    (out
                                            "V" X1 ";") ))
                                    (out
 ((OPT-RIGHT-SIDE
     (empty)))
 ((EXPRESSION1
     (Q TERM)
     (multiple
         (Q SIGNED-TERM))))
 ((SIGNED-TERM
                                                               H")
     (match "+")
                                                      PUSH
                                                               D")
     (Q TERM)
                                    (out
                                                      POP
                                                               D")))
                                                      DAD
                                    (out
 ((SIGNED-TERM
     (match "-")
                                                      PUSH
                                                               H")
                                    (out
                                                      POP
                                                               D")
     (Q TERM)
                                    (out
                                                               A,E")
                                    (out
                                                      MOV
                                    (out
                                                      SUB
                                                      MOV
                                                               L, A")
                                    (out
                                             11
                                                      MOV
                                                               A, D")
                                    (out
                                                               H")
                                    (out
                                                      SEE
                                                               H,A") ))
                                                      MOV
                                    (out
 ((TERM
     (Q FRIMARY)
     (multiple
         (match "*")
                                                               H")
                                    (out
                                                      PUSH
                                                               DIL
         (Q PRIMARY)
                                                      POP
                                                               VMULT") )))
                                                      CALL
 ((PRIMARY
                                                               " Z "V")))
                                    (out
                                                      LHLD
     (id Z)
 ((PRIMARY
     (number Z)
                                                      LXI
                                                               H," Z)))
                                    (out
 ((PRIMARY
      (match "(")
     (@ EXPRESSION)
(match ")") ))
```

End Listing Two

(Listing Three begins on next page)

Compiler in Prolog Listing Three

(Listing Continued, text begins on page 84)

Part of VALGOL.LOG in SIMPLE syntax

```
X compile Y if
      message (Z) and
      P (Z) and
     PP and
     PP and
      KILL (labelused) and
      add ((0 labelused)) and
      KILL (last-shown) and
      add (((-1|-1) last-shown)) and
     OPEN (X) and
KILL (infile) and
     add ((X infile)) and
erase (Y) and
CREATE (Y) and
      KILL (outfile) and
      add ((Y outfile)) and
      skip (((0|0) 0|0) x) and
      syntax (y) and
      00 (0 x z y) and
     CLOSE (X) and
CLOSE (Y) and
     PP and
PP (*** Compilation of X complete **)
X compile Y if
     PP and
     PP (** Syntax error **) and
     CLOSE (X) and CLOSE (Y)
X alphanumeric if
     X alphabetic
X alphanumeric if
     X digit
X alphabetic if
     @ LESS X and
     X LESS [
X alphabetic if
      LESS X and
     X LESS C
X digit if
     / LESS X and
X LESS :
                                          End Listing Three
```

Listing Four

P.ASM

VCPM VBDOS VTPA VCR VLF	EQU EQU EQU EQU ORG LXI JMP	0 5 256 13 10 VTFA SF,VSTACK V1
×V:	DS	2
V1:		
	LXI	H,0
	SHLD	×V
V2:		
	LHLD	×V
	PUSH	Н
	LXI	H,15
	POP	D
	MOV	A, L.
	SUB	E.
	JNZ	95
	MOV	A;H
	SBB	D
	JNZ	95
	LXI	H,1
	JMF.	V4
V5:		
	LXI	H,0
V4:		
	MOV	A,H
	ORA	L
	JNZ	V3
	LXI	H,1
	PUSH	Н

```
LXI
                    H.14
           PUSH
           LHLD
                    ×Ψ
           POP
                    UMUL T
           POP
                    D
           DAD
                    D
           PUSH
                    Н
          HLD
                    v.U
          PUSH
                    ы
          LHLD
                    хŲ
          POP
          CALL
                    VMULT
          POP
          MOU
                    A,E
          SUB
          мпи
                    L,A
          MOV
                    A,D
          SBB
                   H.A
          CALL
                   VEDIT
                   VERINT
          CALL
          LHLD
                   мV
          PHSH
          LXI
                   H, 1
          POP
                   D
          DAD
                   n
          SHLD
                   y.U
          JMF
                   42
 U3:
          JMP
                   VOPM
VMULT:
          MOV
          MOY
                   C,L
          XEA
          MOV
                   H.A
          MOY
                   L,A
          MUI
                   A. 16
VMULT1:
         PUSH
                   PSH
          DAD
                   н
          YRA
                   A
                   A,C
          MOU
          PAL
                   C,A
          MOV
          MOV
                   A,B
          EAL
          MOV
          JNC
                   VMULT2
          DAD
VMULT2:
         POP
                   P'SW
          DCR
                   Α
          ORA
          JNZ
                   UMULT1
          RET
VEDIT:
          MOV
                   A,H
          ORA
                   VEDIT1
          JZ
         MUI
         CALL
                   VCPMOUT
         DCX
          JMF
                   VEDIT
VEDIT1:
         POP
VEDIT2:
         MOV
                   A,M
         CPI
         INX
                   Н
         JZ
                   VEDIT3
         CALL
                   VCPMOUT
         JMP
                   VEDIT2
VEDITA: PUSH
VPRINT:
         MUT
                  A, YCR
         CALL
                  VCPMOUT
                  A, VLF
                  VCPMOUT
         CALL
         RET
VCPMOUT:
         PUSH
         MOU
                  E,A
         MUI
                  C,2
         CALL
                  VEDOS
         POP
                  Н
         RET
         DS
                  60
USTACK: DW
```

End Listings

DDJ Classifieds

Software

PCBTAM

Allows an IBM PC or compatible to perform BISYNC communication. PCBTAM is a gener-al purpose interrupt driven access method us-able by any Microsoft language. Requires IBM BSCA card and PC-DOS.

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SLK/F places an assembled or compiled program on a diskette with 4 different copyresistant features in such a way that it runs normally, but cannot be copied by backup programs such as COPYPC. The rest of the diskette is available as normal, and DOS may be added. Price \$150. Olive Branch Software 1715 Olive Street Santa Barbara, CA 93101 (805)569-1682

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Drive Alignment Test Program for CP/M 2.2/3.1 With Dysan 8" SSSD Diagnostic Disk \$65 check/MO postpaid Chandler Software, 273 W Shore Dr, Marblehead MA 01945 (617)631-4685

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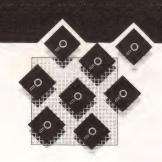
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Toolworks C/80 and Toolworks C/80 Mathpack, Version 3.1

Company: Software Toolworks, 15233 Ventura Blvd., Suite 1118, Sherman Oaks, CA 91403

Computer: Heath; Kaypro II, 4, and 10; Osborne 1 and the Executive; DEC VT-180 and Rainbow; Xerox 820 Price: Compiler, \$49.95; Mathpack, \$29.95 Circle Reader Service No. 101 Reviewed by D. C. Shoemaker

Few readers of *Dr. Dobb's Journal* are unaware of the contribution made to small computer system programmers by Ron Cain and his minimal C subset compiler. Over the years since its introduction, there have been many extensions and improvements, all building on his basic idea. One of the best and most complete versions is from Walt Bilofsky's Software Toolworks. First released in 1981, Version 3.1 is the most current and comes the closest to being a complete C compiler.

This software is a bargain at the price. The package comes in two parts, sold separately: the C compiler itself is \$49.95, and the C/80 Mathpack is \$29.95. There's a reason for the way the software is packaged and priced. Those just learning C probably have no need for the FLOAT and LONG data types that the Mathpack provides. Adding these features somewhat increases the size of the libraries and the assembled code. If you need them, Mathpack is a bargain-level entry into other more powerful capabilities given by the additional data types. Therefore, you don't have to pay for something you don't need, but you aren't limited to an entry-level compiler that can't do serious work.

C/80 is a complete implementation of the C language described by Brian Kernighan and Dennis Ritchie in *The C Programming Language* (Prentice-Hall, 1978) with the exception of the following features:

- FLOAT and LONG data types (available in the C/80 Mathpack)
- DOUBLE data type
- · typedef
- Arguments to #define macros
- · #line
- · Declarations within nested blocks
- · Bit fields

Whether or not these omissions are significant depends a great deal on what you want to do with the software. If you want to learn the language and get a whiff of programming in C, the C/80 package is sufficient. If you want to write some fast-running utility software and don't want to invest the time in assembly language, these omissions will cause no problems.

C/80 supports structures, statics, initialization, casts, compile time evaluation of constant expressions, and all the other C language features. It's worth mentioning some of the features that C/80 does provide, however, because they're often lacking in other subsets:

- Unix-style I/O redirection
- Conventional C I/O and string library
- Formatted and random access file I/O
- Dynamic storage allocation
- Runtime execution profile capability
- In-line assembly language capability

In addition, C/80 can generate source code compatible with Macro-80 and RMAC. Should you have neither, a serviceable assembler comes with the C/80 package. It works well and does the job with no fuss.

If you have a version of C/80 earlier than 3.0, here are the main changes in version 3.1:

- An expanded runtime library
- · ROMable code
- RMAC compatibility
- A menu-configurable compiler
- \ at the end of a line for continuation
- #ifneed for selective compilation
- True alloc/free
- Command line wildcard expansion
- CP/M files now written in 128-byte records
- File 0 always the terminal

The changes from version 3.0 to 3.1 are to correct minor bugs and speed code generation. Some new compiler switches have been added, and you are now prohibited from reading from and writing to the same open file. If you have one of the earlier versions of C/80, return the original disk to Software Toolworks with \$10.00, and they will give you the most recent version. I bought my first copy of C/80 in 1981 and have updated it twice.

Software Toolworks makes the C/80 package available in a variety of formats. Walt Bilofsky was one of the first to support Heath's in-house disk operating system HDOS, and he still does. However, the standard remains CP/M, and Software Toolworks provides disks for Heath, both hard and soft sector; Kaypro II, 4, and 10; Osborne I and the Osborne Executive; DEC's VT-180 and the Rainbow; the Xerox 820; and the standard 8-inch CP/M single-density format. There's

even a version for the Epson QX-10. Currently no version is offered for the IBM PC, PCjr, or any MSDOS, PCDOS, or ZDOS computer because the programs are written in 8080 code, but I expect that will change. Incidentally, I'm using the CP/M version on a Heath H120 running CP/M-85 with no trouble at all. Just order the soft-sector Heath version.

Using C/80 is simple. The hard part (as usual) is writing the original C language program. Then you call C/80 and let it compile the source into 8080 assembly language code. Let's use one of the test files on the distribution disk, HELLO.C. We do this by typing "C80 HELLO." The output file from C/80 can be modified if you wish. I'm not the greatest assembly language programmer, so I don't change the code, but someone more experienced might be able to tighten it up in some areas.

Next, call the assembler of your choice. I use the one that comes with the package. It's invoked as you might expect, by typing "AS HEL-LO." When the assembly is finished, you have an executable file called HELLO.COM. It will be rather large, due to the inclusion of pieces from the I/O and other libraries, but that's part of the tradeoff. The completed code executes relatively quickly. I don't have the capability to do much comparative benchmarking, but comparisons I've seen suggest that in some cases C/80 is a bit faster than BDS C and in others a bit slower. On balance they seem about equal, except that BDS C has some nonstandard features that get in the way of portability, and it costs twice what C/80 costs. BDS C doesn't provide as many formats, either.

Software Toolworks doesn't charge a license fee for the COM files created by C/80. This means that you're free to market whatever you write and compile as you see fit. Unlike some of the so-called "big boys" of the software industry, Bilofsky encourages programmers to market software using his tools.

A word about documentation: If you're a first-time C user, the manual that accompanies C/80 won't be too helpful. There are some examples in

the documentation that you can play with, and several source code files on the distribution disks, but that's it for introductory material. Several good books on the market can help you get a better grasp of the language. The documentation lists eight of the most common of these, and new books appear almost monthly.

The C/80 manual is 50 pages long with a three-page index. It has 16 chapters, with most of the attention paid to a language summary, a description of the I/O and runtime libraries, and a discussion on running the compiler. The manual includes a brief discussion of the internal features of the compiler and useful tricks to bend the compiler to your will. There is also a detailed list of compiler error messages, what they mean, and what to do about them.

The C/80 Mathpack requires C/80 version 3.0 or greater. If you've been working with an earlier verson of C/ 80, the Mathpack is a good reason to take advantage of the Software Toolworks' update policy. Mathpack comes on one disk with a short manual. The Mathpack adds true 32-bit LONG and FLOAT data types to the C/80 compiler and runtime library. It includes a program to patch the C/80 compiler to recognize LONG and FLOAT, a runtime library to perform 32-bit arithmetic, routines to convert between ASCII and floating-point representation, an augmented printf, and a transcendental library written in C.

In practical terms, this gives the C/80 a 32-bit signed number capability in the range -2,147,483,648 to 2,147, 483, 647. Floating-point numbers have 24-bit precision, which equates to about seven decimal digits, and an exponent range of 10 to the plus or minus 38th power.

Installing the Mathpack requires only that you run the modifying program CCONFIGE.COM. A menu will appear with a range of options. Choose option N and press RETURN. This changes FLOAT and LONG from not available to available. Now type a Y in response to the question about making changes permanent, and you're done. Everything else about C/80 remains as before, except





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that the size of the compiled and assembled code increases a bit.

The Mathpack manual is only 10 pages long with six chapters; it spends most of its time on installation and additions to the function library. You'll have to know how to make use of FLOAT and LONG before the Mathpack will do you much good.

In summary, if you want to learn C or if you already know a bit about the language and want an inexpensive but powerful implementation for your CP/M computer, my first choice would be the Software Toolworks C/80 and an introductory book. If you want an enhanced 32-bit capable C, add the Mathpack. For under \$80.00 you can't miss.

Disk Maker I

Company: New Generation Systems, 1800 Michael Faraday Drive, Suite 206, Reston, VA 22090 (703) 471-5598, (800) 368-3359 Operating System: CP/M 2.2 Price: \$1695.00 Circle Reader Service No. 103

Reviewed by Jim Kronman

Disk Maker I from New Generation Systems offers a practical solution to the virtual Tower of Babel that exists in the world of CP/M and MSDOS disk formats. Acting as a peripheral device in an S-100 system, Disk Maker I can format, read, write, and duplicate over 170 disk formats (at the time of this review—the total grows every month or two), allowing you to copy virtually any soft-sectored format and transfer disk files between various CP/M and MSDOS disk formats. An optional file transfer utility program transfers files from Dec-Mate II and Wang OIS word processing disk formats.

The standard Disk Maker I package consists of an S-100 disk controller board capable of supporting up to four disk drives, a 48 track-per-inch (tpi), double-sided 5¼-inch disk drive with power supply in a fan-cooled cabinet, and the Disk Maker software. Other disk drives for Disk Maker are a 96 tpi 5¼-inch drive, a

135 tpi 3½-inch drive, or a 96 tpi 1.2 Mb IBM PC/AT drive (as used in the IBM PC/AT) installed in the cabinet with the 48 tpi drive. A double-sided double-density (DSDD) 8-inch drive with its power supply in a separate cabinet is also available. Software options are the word processing transfer utilities and disk drive test software.

If you don't have an S-100 system. do not stop reading: New Generation Systems also offers a stand-alone Disk Maker II system. It is a 6 MHz, Z80, 64K CP/M 2.2 system that includes one DSDD 8-inch drive and one 48 tpi, DSDD 51/4-inch drive plus the same software provided with Disk Maker I. You can add the same options described for Disk Maker I, up to a total of four drives; a 10 Mb hard disk option is also available. Having reviewed the manual for the Disk Maker II (but not having used the equipment), I am reasonably confident that my remarks about Disk Maker I will be applicable to the operation of Disk Maker II as well.

The Disk Maker I controller uses the Western Digital 1795 controller chip, allowing the software to read or write disks in all 170-plus formats shown in the Table on page 101 (if you have the appropriate drives, of course). Disk Maker I cannot accommodate some disk formats such as Apple, NorthStar, Micropolis, Commodore, and Victor; these formats either are hard-sectored or depend on special tricks in the disk controller hardware.

When you have the optional 96 tpi 5¼-inch drive installed, the software allows you to set the 96 tpi drive as a read-only drive for a 48 tpi format; this allows you to transfer files from one 5¼-inch disk to another without having to do an intermediate copy. With the two 5¼-inch drives set to the same format, rapid disk duplication is possible using the DUPE utility program. New formats are added regularly, and New Generation Systems offers each upgrade to current users for only \$50.

Programs

The Disk Maker I software includes these programs:

DMINSTAL.COM—sets user-deter-

mined and system-dependent options DMSET.COM—loads Disk Maker BIOS into the system DMFORM.COM—formats disks WHATDISK.COM—determines exact format of IBM, Z-100, or Morrow disk

MC.COM—Mass Copy, a file copy program

DUBE COM—rapid track for track

DUPE.COM—rapid track-for-track copy program with optional verify DMCOMP.COM—rapid track-for-track compare program

TOMS.COM—CP/M < = > PCDOS file exchange utility

Various public domain utilities such as SWEEP and D normally are provided on the distribution disk as space permits. Two or more versions of DMSET and DMFORM are provided to accommodate various hardware peculiarities.

The optional software is:

DDD.COM—disk alignment program used with special disks

DEC2CPM—utility to transfer files from DecMate II word processor to CP/M

WANG2CPM—utility to transfer files from Wang OIS word processor to CP/M

Requirements

Disk Maker I is designed to operate under standard CP/M 2.2 with an 8080, 8085, or Z80 CPU. New Generation Systems recommends using it with a standard CP/M system only, but I run it with CP/M 2.2 and ZCPR, version 1, without any problems. The controller board and Disk Maker software normally use ports 98 to 9F (hex), but New Generation Systems provides software and hardware modification instructions to relocate the ports to other addresses if you have an unresolvable conflict in your system.

Although a 64K system is recommended for Disk Maker I, it will run in as little as 48K. The more disks you attach to the Disk Maker, the more memory the software requires. When you use the TOMS utility, a smaller TPA means that you may not be able to handle a full directory on an MSDOS disk; my 57K system will handle the maximum. Double-density

Apricot CP/M-86	3"(SSDD)	306K	Intertec HeadStart 3"	(SSDD)	372K
•		169K	Actrix Matrix	(DSDD)	
Access AMPRO	(SSDD)	188K	AMPRO	(DSDD)	386K
Balcones BRAVO	(DSDD)		Bondwell Model 14/16	(DSDD)	346K
BYAD	(SSDD)		Casio FP 1000/1100	(DSDD)	
Compuview CP/M-86					
Cromemco	(SSDD)		Cromemco C-10	(DSDD)	
Cromemco Davidge 1024	(DSDD)		DEC VT180	(SSDD)	
Digilog	(DSDD)		DEC VT180 Eagle I	(SSSD)	
Epson QX-10 CP/M	(DODD)	378K	Enson OY_10 MF	(DSDD)	
Fujitsu Micro 16s	(0300)	31/JK	Epson QX-10 MF Heath H-37	(SSDD)	
Heath H-37			Heath w/CDR Systems	(DSDD)	
Heath w/Magnolia	(0000)		HP 86/87/125	(DSDD)	
IBM PC (CP/M-86)	(2200)	15AV	IBM PC (CP/M-86)	(DSDD)	
ICL PC1 10,30,31,32	(0200)	1011	Insight Dev. IQ-120	(DSDD)	
KayPro II Lobo Max-80	(2200)	1611	KayPro 4/10		
Lobo May 90 25+k	(DCDD)	104K	Lobo Max-80 35tk	(SSDD) (DSSD)	
Lobo Max-80 35tk Microbee	(DSDD) (DSDD)		Microbee Molecular 9X	(DSDD)	
			Morrow Micro Dec III		
Morrow Micro Dec I/					
Morrow Micro Dec 11			MultiTech MIC-501	(SSDD)	
NCR Decision Mate V			NEC PC-8000 Series		
NEC PC-8000 Series			Osborne	(SSSD)	
Osborne DD/Executiv			Otrona Attache	(DSDD)	
PMC 101 (CP/M 3.0)	(0200)	386K	Sanyo 1000/1100/1150		
SD Sales	(2220)	/1K	SD Sales	(2200)	112K
Sharp 3540			SuperBrain		
SuperBrain	(DSDD)		Systel III	(DSDD)	
TeleVideo 802/3/6			TeleVid. 806 TurboDos		
TI Professional	(SSDD)		Toshiba T100	(DSDD)	
TRS-80 Mod I Omikro			TRS-80 Mod I Omikron		
TRS-80 Mod III MM		186K	TRS-80 Mod IV	(SSDD)	154K
TRS-80 IV Montezuma	(SSDD)	166K	Versa Floppy II Xerox 820	(DSDD)	278K
Xerox 820 Zenith Z-100	(SSSD)	82K	Xerox 820	(SSDD)	155K
Zenith Z-100	(SSDD)	148K	Zenith Z-100	(DSDD)	304K
Zenith Z-100 (orig)	(DSDD)	304K	Zorba AMPRO 96	(DSDD)	388K
ALTOS 586				(SSDD)	
	96 (DSDD)			(DSDD)	
	96(SSDD)			(DSDD)	
	96 (DSDD)		,	(DSDD)	
•	96(DSDD)			S(SSDD)	
	96 (DSDD)			(DSDD)	
	96(DSDD)			S(SSDD)	
9	96 (DSDD)		•	(DSDD)	
	96(SSDD)			S(SSDD)	
	96 (DSDD)		9	(DSDD)	
	96(DSDD)			(DSDD)	
	96 (DSDD)			(DSDD)	
	96(SSDD)			S(DSDD)	
	96 (DSDD)			S(SSDD)	
	96 (SSDD)			S(DSDD)	
	96 (DSDD)			S(DSDD)	
	96 (DSDD)			S(DSDD)	
Otrona Philips 3000	96 (DSDD)			5(DSDD) 5(DSDD)	628K 776K
Philips 3000	96 (SSDD)			(0300)	7701
		Tal	ole		

Disk Maker I Formats

(Continued on next page)

```
Sanyo 1200/50
                   96(DSDD) 620K
                                    Sanyo 2000
                                                        96(SSDD) 302K
Sanyo 4050
                   96(DSDD) 620K
                                    TeleVideo 1603
                                                        96(DSDD) 706K
Vector 4-S
                   96(DSDD) 712K
                                    Vector VSX
                                                        96(DSDD) 710K
                   8"(SSDD) 446K
                                                 (256b) 8"(SSDD) 482K
ALTOS
                                    CCS
           (1024b) 8"(SSDD) 596K
                                    Colonial Data SB80 8"(SSDD) 596K
CCS
Colonial Data SB80 8"(DSDD)1208K
                                                        8"(SSDD) 592K
                                    Columbia 1800
           (1024b) 8"(SSDD) 596K
                                                (1024b) 8"(DSDD)1192K
CompuPro
                                    CompuPro
CP/M SSSD Standard 8"(SSSD) 241K
                                    Delta Products 125 8"(DSDD)1212K
Harris 1685-50MFT
                   8"(SSDD) 596K
                                                        8"(DSDD) 968K
                                    Harris 1685-50MFT
Harris 1685-50MFT
                   8"(DSDD)1196K
                                    IBM 3740 TO Direct 8"(SSSD) 248K
IBM 5520 WP
                   8"(DSDD)1132K
                                    IBM 128b no skew
                                                        8"(SSSD) 241K
                   8"(SSDD) 482K
IBM 256b no skew
                                                        8"(SSDD) 484K
                                    Insight IQ-120
                                                        8"(SSDD) 496K
Megaflex Apple
                   8"(SSDD) 558K
                                    Molecular
NEC APC CP/M-86
                   8"(DSDD) 980K
                                    S. D. Sales (128b) 8"(SSDD) 464K
S. D. Sales (256b) 8"(SSDD) 476K
                                                 (128b) 8"(SSDD) 472K
                                    Tarbell
TRS-80 II,12/6 P&T 8"(SSDD) 596K
                                    TRS-80 II,12/6 P&T 8"(DSDD)1210K
TRS-80 Lifeboat
                   8"(SSDD) 482K
                                    TRS-80 Lifeboat
                                                        8"(SSDD) 596K
                                                        8"(SSDD) 482K
Vector 2800
                   8"(DSDD) 984K
                                    Zenith Z-100
                   8"(DSDD) 980K
                                                 MSDOS 3"(SSDD) 314K
Zenith Z-100
                                    ACT Apricot
HP-150
             MSDOS 3"(SSDD) 258K
                                    Intertec H-S MSDOS 3"(SSDD) 395K
IBM PC
             PCDOS1.1(SSDD) 156K
                                    IBM PC
                                                  PCDOS1.1(DSDD) 315K
IBM PC
             PCDOS2.0(SSDD) 175K
                                    IBM PC
                                                  PCDOS2.0(DSDD) 354K
IBM PC-AT
                                                          (DSDD) 354K
             PCDOS3.0(DSDD)1186K
                                    Kaypro 10
                                                  MSDOS
                      (DSDD) 354K
                                    Zenith
Wang
             MSDOS
                                                  ZDOS 1.1(DSDD) 315K
BurroughsB25 MSDOS 96(DSDD) 610K
                                    DEC Rainbow
                                                  MSDOS 96(SSDD) 384K
Eagle 1600
             MSDOS 96(DSDD) 785K
                                    Monroe
                                                  MSDOS 96(DSDD) 712K
NCR D-M "M"
             MSDOS 96(DSDD) 790K
                                    Otrona
                                                  MSDOS 96(DSDD) 712K
                                                  MSDOS 96(DSDD) 714K
Sanyo MBC
             MSDOS 96(DSDD) 792K
                                    Tandy 2000
                                                  MSDOS 8"(DSDD)1221K
             MSDOS 8"(SSSD) 239K
Lomas Data
                                    NEC APC
                                                  ZDOS 8"(SSSD) 245K
Standard
             MSDOS 8"(SSSD) 246K
                                    Zenith V1.1
```

NOTE: all formats 5 1/4" 48 tpi, CP/M-80/86 unless noted as:

3" - 3 1/2 inch format 8" - 8 inch format

96 - 96 tpi 5 1/4 inch format SS = Single Sided; DS = Double Sided SD = Single Density; DD = Double Density

Table Disk Maker I Formats

8-inch formats and the IBM PCAT drive require at least a 4 MHz system clock; otherwise, a 2 MHz clock should be sufficient.

Installation of the Disk Maker I hardware is as easy as plugging the disk controller board into an empty slot in your S-100 bus motherboard and connecting the disk cable between the disk cabinet and the controller board. A menu-driven installation program allows you to customize some features of the system to your hardware and personal taste; the installation process takes only a few minutes.

How It Works

Disk Maker I is simple to use. To format a disk, type DMFORM to invoke the format program, then choose a format by number from the on-screen menu. You can also invoke DMFORM by making a format choice on the command line; the program proceeds without showing you the menu. You can return to the menu if you want to change your selection. DMFORM will verify a disk after formatting if you set a permanent option with DMINSTALL or specify verification on the command line when you invoke DMFORM.

The DMSET program gives you access to the Disk Maker disk drives. You use DMSET the same way you use DMFORM: you can choose from a menu or go directly to work by specifying a format on the command line. You can set each Disk Maker drive to a different format. Every time you warm boot, the Disk Maker software displays the disk formats you have selected.

DMSET installs a special BIOS under your normal CP/M system; you can run any CP/M program that will fit in the somewhat reduced memory left for applications. Even though

there is less memory, I have found it adequate for running any of my utilities, dBASE II or my text editor (which does bidirectional scrolling through a disk file). You probably will notice the memory reduction the most when you run a spreadsheet program or any other application that uses all available memory but does not scroll data to and from disk, the way many text editors do.

Because of the radically different structure of MSDOS files, you need a special program called TOMS (TO MSDOS) to access individual disk files on a MSDOS disk. You cannot run another program while using TOMS; if you want a CP/M application to access a MSDOS data file, you first must transfer the file to a CP/M format. TOMS is like a small part of the MSDOS operating system, providing the commands DIR, COPY, TYPE, and ERASE. TOMS cannot deal with subdirectories, so all files must be in the root directory on a MSDOS disk coming into the system; all files written by TOMS are in the root directory.

Performance

I have been using Disk Maker I for well over a year. The capabilities of the system are impressive, but even more impressive is the support New Generation Systems provides. I was an early user and, as you might expect, everything did not start out working perfectly. One annoying problem was caused by the routing of the data cable in the disk drive cabinet. The cable was picking up noise from the power supply, which gave me errors when reading the inner tracks of the 48 tpi drive. I solved the problem by changing the routing of the cable at the suggestion of New Generation Systems, but in the meantime, I received a replacement 48 tpi drive that had been specially aligned and certified. I have always received courteous and competent assistance anytime I have had a reason to call New Generation Systems with a problem or a question. You can expect the same treatment. (I was dealing with New Generation Systems as just another user, before I had thought of doing this review.)

The Disk Maker I hardware con-

sists of off-the-shelf components integrated by New Generation Systems—nothing "razzle-dazzle" but obviously chosen carefully for quality and value. What makes the Disk Maker I package outstanding is the software and support. The programs perform well and are easy to use. The no-nonsense operator interface was designed for users who need to get things done quickly.

Although the software is not friendly in the current sense of having elaborate menus and on-line help fa-

cilities, each user prompt and program response is clear in its meaning. With one exception, which I will discuss later, all errors are trapped cleanly and appropriate error messages appear on the screen. The user interface is appropriate for the intended users of this package: software professionals, experienced computer users, and serious hobbyists with a need to transfer files between diverse disk formats. Operation is simple enough for a novice to use the system, too.



Evolution.

Now FoxBASE, the dBASE II source-compatible interpreter/compiler, is even better than before. Automatic 8087 co-processor support allows you to perform numeric computations with lightning speed. Fourteen-digit precision gives you 40% greater accuracy than dBASE II. The fact that FoxBASE is not copy protected means you can easily load it onto your hard disk. What's more, FoxBASE comes complete with a NO-RISK demo plan.

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FoxBASE is currently available on a wide range of machines: IBM-PC, IBM-PC/XT/AT, COMPAQ & IBM compatibles, TI Professional, DG Desktop, and DG MV-Series to name just a few. And it will soon be available on the Molecular and NCR Tower computers as well. Call or write today for more information.

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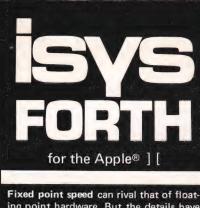
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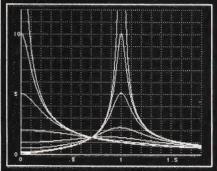
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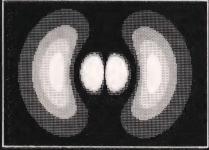
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Parallel Resonance with Damping BASIC 213 sec ISYS FORTH 27 sec



Hydrogen 3p Orbital Cross-section BASIC 492 sec ISYS FORTH 39 sec

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The Disk Maker I (and Disk Maker II) documentation is well organized and appropriate for the intended users. The *User's Manual* for Disk Maker I contains over 50 pages with a good table of contents in a threering binder. The table of contents is adequate for using the manual: no index is provided and none is needed. The manual, prepared with a word processor, is much cleaner in appearance than most manuals prepared this way. It is relatively easy to flip through the pages and spot whatever you are looking for.

New Generation Systems has assumed that you already know how to use CP/M and your computer. The manual will say "copy this file to another disk" rather than give a blow-by-blow description of how to use the file copy utility. There is a section titled "How To Run Disk Maker Without Reading The Manual" for the impatient and for the technical-minded a section on each program called "How xxxx Works" that explains the inner workings of the program.

I have validated Disk Maker's ability to format, read, or write disks in about 20 of the 170 available formats. I have used Disk Maker primarily to move files between an S-100 system and an Osborne I (double density) or between the S-100 system and a PCDOS system. Other than the read errors I encountered (now fixed), the Disk Maker hardware and software have performed flawlessly for me.

I have encountered a few instances where it has been necessary to bulk erase a disk and format it with Disk Maker I because, if I wrote on a disk already formatted by the receiving system, that system could not read the disk

Although Disk Maker I is easy to run, one enhancement would be welcome. The Disk Maker I software assumes you know what disk format you are dealing with. If you tell it you are feeding it a disk of format X and then give it a disk of format Y, you may or may not get an error message; the program you run could go merrily along without complaint, producing either garbage files or a disk that the target system cannot read. The Disk Maker software cannot identify posi-

tively that you have inserted the correct disk format in the drive being written to because no standard means of identification exists for a blank formatted disk; even if you determine the sector size, sectors per track, and so forth, you still cannot deduce the directory size and other characteristics needed for positive identification.

WHATDISK enables you to determine which of several similar formats a disk might be, if you already know that the disk is for IBM PCDOS, Heath/Zenith Z-100, or Morrow Micro Decision. A generalized WHATDISK program to identify any disk format would be very useful. Such a program is impossible to write, however, for the above reasons.

What You Pay

The basic Disk Maker I, which includes the controller board, one 48 tpi, DSDD 5¼-inch drive, and the Disk Maker software is \$1695. The 96 tpi, DSDD drive is an additional \$395. The 8-inch drive and cabinet for Disk Maker I is \$849. The 3½-inch drive costs \$295, and the PCAT drive is \$495. The word processing transfer utilities package is \$295, and the disk testing software is \$150 plus \$40 each for the 48 and 96 tpi Dysan test disks. You also can purchase the Disk Maker I controller and software with no drives.

The basic Disk Maker II system is \$3395 for the stand-alone 6 MHz Z80B system with one DSDD 8-inch drive, one 48 tpi, DSDD 5¼-inch drive, and CP/M 2.2; you only need to add a terminal. The option prices are the same as for Disk Maker I, and a second DSDD 8-inch drive costs \$600.

Are these prices reasonable? It depends on what you need. Programs for the Kaypro, IBM PC, Morrow, and other computers that allow you to read, write, and format a number of 5¼-inch formats cost less than \$100, but don't ask them to recognize an 8-inch disk drive, a PCAT drive, or a 3½-inch drive. At the other end of the spectrum are systems that claim less capability than Disk Maker II and cost over \$5000. Disk Maker I and Disk Maker II offer outstanding versatility and performance for the money.



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IN TERNATION AL

Conclusion

Disk Maker I is a solid product backed by a competent and cooperative company that operates as if it wants to remain in business for a long time. If you are a software developer, software distributor, user group, large office, or serious hobbyist with the need to exchange files between computers with various disk formats, you should seriously consider Disk Maker I or Disk Maker II as an answer to your needs.

AMPRO Little Board and Bookshelf Computers

Company: AMPRO Computers, Inc., 67 East Evelyn Ave., Mountain View, CA 94041 (415) 962-0230.

Price: \$349

Circle Reader Service No. 105 Reviewed by Richard Conn

The heart of the AMPRO Bookshelf Computer® is the AMPRO Little Board®, a full-featured single-board computer that measures only 5¾-inch by 7¾-inch and is designed for mounting on the back of a standard 5¼-inch minifloppy disk drive. Although it is small in size, the AMPRO Little Board is big in features: the Little Board contains all of the hardware necessary to support a conventional CP/M environment.

The Little Board is based around a 4 MHz Z80A microprocessor and the associated Zilog Z80 family of support chips. On board this small computer is:

- A 4 MHz Z80A microprocessor
- · 64K of dynamic RAM
- One 4K 2732-type EPROM
- A Z80A CTC (Counter/Timer Circuit)
- A Z80 DART (Dual Asynchronous Receiver/Transmitter)
- A parallel output port (based on Dtype latches)
- A Western Digital 1770 Floppy Disk Controller Chip

The AMPRO Bookshelf Computer is an AMPRO Little Board computer housed in an attractive case with one or two 51/4-inch floppies, built-in

power supply, two 25-pin EIA RS-232C connectors, one Centronics connector, and a connector for tagging on up to two more 5¼-inch floppies. An ON/OFF switch and a RESET switch are available on the front panel.

The AMPRO Little Board Computer

Hardware

The Little Board has two possible memory configurations, which depend on the setting of an EPROM-enable bit in the Board Control Register. Enabling the EPROM establishes the following memory configuration:

Address (Hex) Memory Element

0000 - 0FFF 2732 EPROM 1000 - 7FFF 2732 EPROM duplicated on each 4K group

8000 - FFFF RAM

If the EPROM is disabled, all memory from 0 to 0FFFFH is enabled as RAM.

A Z80 DART and RS-232C line drivers provide the serial input/output ports. The DART is an asynchronous device, providing for baud rates up to 38,400 on its A channel and 9600 on its B channel in the Little Board configuration. The Little Board does not implement all of the RS-232C signals on its two serial channels. Only the following signals are provided:

- Serial Data Output (RS-232C pin 3)
- Serial Data Input (RS-232C pin 2)
- Request to Send Handshaking Output (RS-232C pin 5)
- Clear to Send Handshaking Input (RS-232C pin 20)

Both channels of the DART are wired as DCE (Data Communications Equipment), which is the complement of DTE (Data Terminal Equipment)—the wiring of the RS-232C connector on computer terminals. Notably missing from this list is the Data Carrier Detect signal. You may use the Clear to Send signal in place of Data Carrier Detect.

The parallel port is an output-only

port, wired in a Centronics-standard configuration. The Z80 CTC on the Little Board provides four counter/timers for use in conjunction with the hardware and software of the computer.

The Western Digital 1770 Floppy Disk Controller chip used in the Little Board design can work with just under 400K per disk on double-sided, 48 tracks-per-inch (TPI) drives and just under 800K per disk on double-sided, 96 TPI drives.

Software

The AMPRO Little Board is supplied with one copy of CP/M 2.2, which uses the ZCPR3 Command Processor in place of the CP/M CCP. Although only the ZCPR3 Command Processor, in a less than maximum configuration, is provided with the Little Board, you can obtain a more complete ZCPR3 implementation from Echelon (see below).

The ZCPR3 configuration distributed with the Little Board provides CP/M 2.2 compatibility while simultaneously presenting the following additional features:

- Multiple commands per line, separated by semicolons, enabling command lines like DIR;ERA
 *.BAK;DIR
- Automatic command search path that searches through directories in the following sequence when looking for COM files: (1) current disk, current user; (2) current disk, user 0; (3) disk A, current user; (4) disk A, user 0; (5) disk A, user 15; and (6) current disk, user 15
- Four-element shell stack, so shells other than the ZCPR3 Command Processor can act as the user interface to the system
- Several built-in commands, including: GET—load file anywhere in memory; JUMP—call subroutine anywhere in memory; and LIST—print file on printer
- Extended directory references over normal CP/M: D:—reference disk (e.g., TYPE B:MYFILE-.TXT); U:—reference user area (e.g., DIR 5:); and DU:—reference disk and user area (e.g.,

See articles in Dr. Dobb's. Computer Language, Byte, and other such magazines for more information on ZCPR3. The authorized agent for ZCPR3 is Echelon, Inc., 101 First St., Los Altos, CA 94022 (415) 948-3820. The ZCPR3 RCP/M and BBS are also available to you at (415) 489-9005.

The nine standard CP/M utility programs (ED, DDT, PIP, etc.) and thirteen additional utility programs are supplied with the Little Board. Included in this list is MULTIDSK, which allows the user to dynamically select one drive to support any one of over 40 different 51/4-inch floppy disk formats, and 48TPI, which allows the user to read 48 TPI disks on a 96 TPI drive.

One nice option that AMPRO gives the customer is that of purchasing the source code to several key programs at a reasonable price. The customer may purchase the Technical Support Software package, which includes the source files for MULTIDSK, the BIOS, and the BOOT.

Pricing

The AMPRO Little Board computer costs \$349. This price includes the single-board computer and all of the software listed above. An extra \$50 buys the Technical Support Software package.

The AMPRO Bookshelf Computer

Hardware

As mentioned above, the AMPRO Bookshelf Computer consists of an AMPRO Little Board, one or two 51/4inch minifloppy disk drives, a built-in power supply, two 25-pin EIA RS-232C connectors, one Centronics connector, one connector for attaching up to two external minifloppy disk drives, and ON/OFF and RESET switches on the front panel. The Bookshelf is available in the following models:

Model	Configuration
121	One double-sided,
	48 TPI drive (400K)
122	Two double-sided,
	48 TPI drives (800K)
141	One double-sided,

96 TPI drive (800K) Two double-sided, 96 TPI drives (1600K)

Software

142

All of the software provided with the AMPRO Little Board is also provided with the AMPRO Bookshelf Computer. In addition, a ZCPR3 shell designed specifically for use on the AM-PRO computer is provided (in object code only). FRIENDLY is a screenoriented tool that provides a directory of files to the user of the current disk and allows him or her to perform a variety of operations on the files displayed. By simply moving a pointer to a desired file and pressing a few keys, a FRIENDLY user can copy a file, delete a file, view a file on the console, print a file on the printer, run a program, mark a file for later delete or copy with a group, perform a sequence of commands on the file based upon a preprogrammed menu, and perform many more operations.

FRIENDLY is easy to use, usually requiring only a few minutes to learn, and it removes casual computer users from the CP/M command environment, relieving them of the necessity of learning how to use CP/M in order to use the computer.

A second software package provided with the AMPRO Bookshelf is T/ MAKER, a product of T/MAKER Company. T/MAKER provides a screen-oriented, memory-based editor, text formatter, electronic spreadsheet, bar graph generator, and file management system in one integrated package. T/MAKER will run under conventional CP/M, CP/M enhanced by the ZCPR3 Command Processor, or FRIENDLY. Several articles have already been published on T/MAKER, and you can obtain further information by writing to T/ MAKER Company, P.O. Box 6430, Falls Church, VA 22046.

Documentation

The documentation provided with the AMPRO Little Board is the Little Board User's Manual, a mainly hardware manual that describes the Little Board in great detail. It is well written and clear if you have a background in digital electronics.

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No documentation on CP/M or the standard CP/M programs is provided. The first pages of the manual, however, give pointers to where to obtain this documentation. Also, no documentation other than a brief overview of ZCPR3 is provided. Again, the manual gives pointers to Echelon.

If you buy the AMPRO Bookshelf Computer, you get the *Little Board User's Manual* as well as documentation on FRIENDLY and T/MAKER. The documentation on FRIENDLY and T/MAKER is oriented to computer users having some limited experience with CP/M.

General Impressions and Comments

I have used the AMPRO Bookshelf for several months now. I started with the Model 122, which had two 400K, 48 TPI drives, and am now using the Model 142, which has two 800K, 96 TPI drives. I find its clean design, speed, and reliability to be excellent. Given my background in CP/M and digital electronics, I found all of the documentation on the Little Board/Bookshelf to be readable and understandable.

The Bookshelf is a nice computer for any user, especially with the FRIENDLY shell as a front end. Although it is a good choice for the first-time computer user, as with any computer, you must overcome a learning curve before you can expect effective use of the computer.

The AMPRO BIOS performs well. The speed of the disks with this BIOS is nice: the disks are faster than most 5¼-inch disk systems I have observed and many 8-inch disk systems I have used. At 800K per disk with the Model 142 (96 TPI drives), I find the disk capacity also to be quite reasonable.

Overall, the AMPRO Bookshelf and AMPRO Little Board are good computers, but there are a few drawbacks for some applications:

- (1) Lack of documentation on CP/M and ZCPR3. However, AMPRO supplies pointers to where you can obtain such documentation, at additional cost.
- (2) Limitation of serial I/O ports to only two serial RS-232C ports and one parallel port. Nevertheless, if

your needs call for no more than a terminal, modem, and printer (as many popular computers are configured today), the AMPRO computers are adequate.

(3) Lack of hard disk support and expandability. The Little Board does not support hard disks or other such devices in its current form. You may buy, however, the Little Board SCSI/PLUS Adapter for \$99. This adapter "piggy-backs" on the Little Board and allows you to attach a Winchester disk drive, slave processor and device boards (perhaps for I/O), and other facilities. This board, with some effort, may eliminate drawbacks 2 and 3.

I am excited about the Little Board and the Bookshelf for their applications in several environments. As a stand-alone personal computer with limited I/O resources and disk space, these computers are good choices. The SCSI/PLUS Adapter promises to expand their capabilities.

For embedded computer applications in which 4K of code is enough to run the application program, the Little Board provides a nice, inexpensive self-contained computer. The 4K 2732 EPROM can be programmed for the application, and 32K of RAM is available for data. The two RS-232C ports are there, as well as the parallel output-only port. No disks would be needed for this type of application. For embedded computer applications with disks, the AMPRO Little Board would "piggy-back" on a 51/4-inch 96 TPI minifloppy and feed off of the floppy's power supply. This would provide a very small embedded computer with 800K of disk and 64K of RAM.

As a slave computer to another computer, the AMPRO Bookshelf with its two disks could act as a background batch processor, communications controller (for a BBS), printer spooler, or other such intelligent and flexible slave device.

Overall, I like the AMPRO Little Board and AMPRO Bookshelf and intend to continue using my Bookshelf for a variety of slave computer and stand-alone applications (such as computer assistance for talks and presentations). I recommend it with some reservation to first-time computer users and with no reservation to embedded applications designers and more experienced computer users.

BetterBASIC, Version 1.1 Company: Summit Software, P.O. Box 99, Babson Park, Wellesley, MA 02157 (617) 235-0729

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Price: BetterBASIC \$199; 8087 Math Module \$99; Runtime System \$250

Circle Reader Service No. 107 Reviewed by Matthew Trask

Among the reasons that Summit Software gives for purchasing BetterBA-SIC are full 640K memory support, separately compiled program modules, language extensibility, window support, 8087 support, and incremental compilation. Although these reasons would be persuasive in the case of another programming language, I question whether they will induce many programmers to shell out \$199, the cost of BetterBASIC, when some form of GW BASIC (aka BASICA) is provided at no charge with most IBM type computers these days and an excellent Pascal compiler can be purchased for less than fifty dollars. In order to help programmers make a decision on this purchase, I will discuss the unique features of this programming system, benchmark its performance, and examine the difficulties of converting programs from GW BASIC to BetterBASIC.

Installation

Although the package can be run right out of the box by using its default configuration, you will probably want to configure it to suit your hardware environment. B.COM is the executable portion of BetterBASIC and it uses the file B.CNF to determine which of the other modules to load for operation. Table 1 (page 110) is a listing of all the modules that are provided on the BetterBASIC disk and Table 2 (page 110) shows the default configuration. An ASCII editor can be used

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Extensions	
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Mastering Forth (additional copies)	\$18
Thinking Forth by Leo Brodie	16
Forth-83 International Standard	
Rochester Bibliography, 2nd ed	15
1984 Rochester Conference	25
1984 J1 of Forth Appl. & Res. 2(2)	15
1983 FORML Conference	25



12077 Wilshire Blvd., #506 Los Angelès, CA 90025 (213) 821-4340 to add or delete modules and create new configurations. For example, users that don't have a graphics card may wish to delete GRAPHICS.IBM in order to free up memory for program use. I replaced FILE.DOS with FILE2.DOS in order to take advantage of DOS 2.x subdirectories and added SYSCALL.IBM in order to use the SHELL command and BIOS/DOS calls. You may specify the use of alternative configurations when starting the program by typing B-MYCON-

FIG/c where MYCONFIG is the name of the optional configuration file.

A Tandy 2000 version of the program is also available. The modules with the extension .IBM are all replaced with equivalent modules that end with .TDY.

Startup and Operation

After a sign-on message and copyright notice you will see the first major difference between BetterBASIC and GW BASIC: instead of the usual

"60891 Bytes free" message, a 512K machine will display "Left:260304 bytes." All available system memory is used as BetterBASIC's workspace. This allows much larger programs to be written. BetterBASIC supports most of GW BASIC's commands, including the on-screen editing keys that are used on the IBM PC. Table 3 (page 111) contains a complete listing of GW BASIC commands that are different or not supported and a list of new commands that are found in BetterBASIC.

Every statement that you enter is checked for syntax and then compiled. If you make a mistake when entering a program line, an error message will be given immediately. For example:

40 PRINT (1 + 2 * 3)	
	^
")" Expected	

If the statement is entered correctly, it is compiled to virtual-machine code rather than being interpreted at runtime, thus giving a large speed improvement over the GW BASIC interpreter.

As in Pascal and C, all variables are declared at the beginning of the program. The exception to this is the AUTODEF command, which allows automatic declaration of a variable by the first assignment of a value. AUTODEF defaults to ON, but if you turn it off, the compiler will catch misspelled variable names. The data types that are supported in BetterBA-SIC are byte, integer, real, string (up to 32,767 characters), array, pointer, and structure. Structures are identical to Pascal's record data type and may contain any other data type. Arrays can contain byte, integer, real, and string data as well as structures and other arrays, thus allowing arrays of arrays, arrays of structures, and structures of arrays.

A BetterBASIC programming session superficially resembles one in GW BASIC in that lines are numbered and most of the command keywords are identical. When saving to disk, however, you must specify the file's extension because .BAS is not assumed by BetterBASIC. At the

В	COM	35312	1-03-85	1:00a
В	DEF	27020	1-03-85	1:00a
B	CNF	142	1-03-85	1:00a
MATH	BCD	8112	1-03-85	1:00a
CONSOLE	IBM	21392	1-03-85	1:00a
MAIN		3840	1-03-85	1:00a
FILE	DOS	19056	1-03-85	1:00a
GRAPHICS	IBM	10512	1-03-85	1:00a
PLAY	IBM	3136	1-03-85	1:00a
EVENT	IBM	7744	1-03-85	1:00a
FILE2	DOS	21072	1-03-85	1:00a
CHAIN	MOD	2704	1-03-85	1:00a
SYSCALL	IBM	3520	1-03-85	1:00a
COMM	BAS	8484	1-03-85	1:00a
PLIST	BAS	2542	1-03-85	1:00a
SECTOR	BAS	6702	1-03-85	1:00a
STRUC	BAS	2980	1-03-85	1:00a
SHELL	CNF	175	1-03-85	1:00a
CHAIN	SIZ	11	1-03-85	1:00a
READ	ME	126734	1-03-85	1:00a
		20 File(s) 102	4 bytes free	

Table 1
Directory listing of the BetterBASIC diskette showing the many modules that make up the Better BASIC system.

MODULES=MATH.BCD
MODULES=CONSOLE.IBM
MODULES=MAIN
MODULES=FILE.DOS
MODULES=GRAPHICS.IBM
MODULES=EVENT.IBM
MODULES=PLAY.IBM
STATUS=ON

Table 2
The default B.CNF configuration file.

end of a session you can exit to DOS with the SYSTEM command or by typing BYE.

Unique Features of BetterBASIC

The best feature of BetterBASIC is the use of true procedures with variables of local scope that exist only inside the procedure. Global variables can be used by declaring them as EX-TERNAL in the procedure and providing the actual declaration at a level outside the procedure. A procedure can be declared as RECURSIVE if its name is declared as EXTERNAL inside the procedure. GOSUB and RE-TURN are both supported, but as you

gain experience with BetterBASIC, they will probably fall by the wayside in favor of these Pascal-like, parameterized procedures. In addition to passing arguments by value or reference, you may specify optional and/ or default arguments.

One particularly distinctive aspect of BetterBASIC is the ANY ARG declaration. This allows a procedure or function to determine what type of data was passed to it at runtime with the following functions:

TYPE() extracts the type of datum

a datum

SIZE() extracts the size of a datum

For example, A=TYPE(FOO) will assign the following values to A:

- 0 if FOO is a byte 1 if FOO is an integer if FOO is a real 3 if FOO is a string if FOO is a structure 4
- 10 + N if FOO is an array of above type N

DIM() extracts the dimensions of | If TYPE(A) returns 3 for string you

GW BASIC Statements Not Supported in BetterBASIC

BLOAD	BSAVE	CDBL	CSNG	CVD
CVI CVS	CONT	DEFDBL	DEFINT	
DEFSTR	DEF FN	DEF USR	EQV	ERASE
ERL FIELD	FRE ANTA (S).	GET	IMP	
MERGE	MKD	MKI	MKS	MOTOR
ON PEN	ON STRIG	PEN	PMAP	PUT
RESUME	STICK	STRIG	TROFF	TRON
USR VARPTR	VIEW	WEND	WINDOW	

New BetterBASIC Statements

ANY ARG	ASH HANDAR SEED	AUTODEF	BYE
BYTE - 1 2000	BYTE ARG	BYTE ARRAY	BYTE ARRAY STRUC
BYTEPTR	CHECK	CODE	CODE?
COLOR BORDER	COMPRESS	CONSTANT	DEFINE WINDOW
DEL\$	DO END DO	DO IF	DO UNTIL
DYNAMIC	EDIT proc	ERROR	EXIT
EXIT n LEVELS	EXTERNAL or EXT	FRAME WINDOW	HEADER
INPUT FROM	INS\$	INTEGER or INT	INT ARG
INT ARRAY	INT ARRAY STRUC	INT FUNCTION	INT PTR
INTERRUPT	INTERRUPT PROC	KEYWORD ARG	LINE\$
LIST ALL	LIST ARGS	LIST PROCS	LOWER\$
MAIN	MAKE MODULE	MAKE PROGRAM	OFFSET
ON INTERRUPT	PRECISION	PROCEDURE	PUBLIC
		or PROC	
READCHR	READCHR FROM	READLINE	READLINE FROM
REAL	REAL ARRAY	REAL ARRAY	REAL ARG
		STRUC	
REAL FUNCTION	REAL PTR	REPEAT	REPEAT IF
RESTORE SCREEN	RESULT =		SAVE MODULE
SAVE SCREEN			SH
	SPAN		STR ARG
	STR ARRAY STRUC		STRPTR
STRUCTURE	UPPER\$	WHILE DO	XREF

Table 3 A comparison of BetterBASIC and GW BASIC Keywords

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can determine its allocated size (not length) with SIZE(A). If FOO is a structure, DIM(A) will return the number of fields and TYPE(FOO(N)) will return the type of field N. This flexibility of optional/ambiguous parameter passing can be a tremendous improvement over Pascal's rigid requirements.

BetterBASIC has a concept called Procedure Families in which more than one procedure or function can have the same name with the only distinction being the parameter list. This allows BetterBASIC to select which one to use based on the type of argument that is used. As an example, the two procedures that follow can be used to give informative error messages on integer input, rather than GW BASIC's cryptic "Redo from start."

PROCEDURE:IntegerInput
INT ARG Value
10 INPUT Value
20 PRINT "Your integer is",
Value

PROCEDURE:IntegerInput.a STRING ARG SomeString 10 PRINT SomeString, "is not a valid integer, please try again"

In this example, IntegerInput.a will be used any time a valid integer is not entered when IntegerInput is called. This technique can also be used to assign additional functionality to BetterBASIC's reserved words.

Functions are similar to procedures in that they share all the same data types. The difference is that they are used in expressions to return a value that is used in the expression. The last statement of a function must be RESULT= and the value that is to be returned.

Both procedures and functions can use KEYWORD ARGuments—a concept that is similar to Pascal's user-enumerated data types. If an argument is declared as

KEYWORD ARG:Color \RED\GREEN\BLUE

then the procedure will accept either RED, GREEN, or BLUE as valid data

for Color.

BetterBASIC has a full complement of flow-controlling block structures. In addition to the usual FOR...NEXT, IF..THEN, and WHILE...REPEAT, BetterBASIC has DO UNTIL..REPEAT, DO..REPEAT, DO ITIMES..REPEAT, DO..END DO, DO..REPEAT, DO IF..REPEAT IF, and more. GOTOs are implemented in a way that could placate even Dijkstra—they cannot be used to enter or exit a control block. The only way out of a control structure is via the EXIT statement, and then only to the statement that immediately follows the block.

Variables can be declared as absolute by adding a segment and offset after the declaration. BYTE ARRAY-(4000):VideoBuffer [&hB000:0] is an absolute reference to the IBM's 2000 character monochrome video refresh buffer. This technique can be used to access the BIOS communication area in low memory for equipment determination.

The BetterBASIC language can be extended by adding your own procedures and functions to the language as keywords. The MAKE MODULE command will convert all procedures and functions currently in memory to a module that can be added to the B.CNF configuration file. Like C language function libraries, useful procedures can be distributed to other BetterBASIC users as modules with a good degree of source code protection because the source code cannot be derived from a module. Summit Software is currently selling an 8087 module and expects to offer an interface module for the SoftCraft Btrieve system sometime this spring.

Programs can be prepared for standalone execution with the MAKE PROGRAM command and the optional Runtime System. The Runtime System consists of RUN.COM, RUN.S\$\$, and MKEXE.COM. RUN.COM will execute any program that has been prepared with MAKE PROGRAM and MKEXE.COM appends the RUN.\$\$\$ runtime library to a program file for stand-alone execution.

Assembly language support is pretty straightforward with a very good example given in Appendix G of

the manual. The keyword CODE is used instead of BLOAD in order to load an assembler module into memory at runtime. CODE? can be used to report the absolute load address if debugging is necessary. The assembly language procedure is then invoked by CALL procedurename(arglist) just as in GW BASIC. An assembly language module can contain one or more procedures with a Symbol Table at the beginning that defines the entry point for each procedure in the module. The excerpt from the Better-BASIC manual in the Listing (page 116) will give the flavor of assembly language interfacing.

Also worthy of note are the XREF command and the debug window. XREF generates a symbol cross-reference of all user-declared identifiers and the line numbers on which they appear. Advanced programmers may wish to use the DOS debug program to test programs that are developed in BetterBASIC. If they start with DEBUG B and then execute BetterBASIC inside DEBUG, the [Ctrl-PgUp] keys will exit to the debug prompt.

Current Version

The current version of BetterBASIC is 1.1. I received the upgrade from 1.0 to 1.1 during the review. New features supported include CHAINing and CALLing modules as overlays, communication between overlays via disk, SYStem calls for access to BIOS and DOS functions, and a SHELL command that can be used to execute programs (such as COMMAND-.COM) from within BetterBASIC. The new XMEM keyword allows data structures such as arrays up to the limits of system memory, not just 64K as in v1.0. Also, the release notes describe bug fixes that were applied to this new version.

Summit Software informs me that all registered users will be provided with free updates to v1.1, with a copy of the new documentation on the disk and an option to purchase printed documentation for \$15. I also received test versions of the MATH1-BNY and MATH2-BNY (single and double precision binary math) that are significantly faster than MATH.BCD, but less precise. By the

time you read this, both modules should be included in the standard BetterBASIC system in addition to the BCD math module.

Benchmarks

I compared the performance of BetterBASIC and GW BASIC by running Rich Malloy's four BASIC benchmark programs that I downloaded from the Byte bulletin board. These tests separate I/O from computation to give fairly accurate representations of speed. With one exception (see Table 4, page 114), BetterBASIC was the better performer by far. The exception was in the floating point calculations, where the BCD math module was about 30% slower than GW BASIC. The new single precision binary math module ran about twice as fast.

Round-off error can be of concern in business and financial programming. Although MATH1.BNY showed no error in this benchmark (compared to GW BASIC's -1.788 E-07 error), it is probably well worth the slower speed of MATH.BCD to have guaranteed zero error in financial calculations.

The Runtime System, unlike the BASCOM compiler, will not show an improvement in execution speed. This is because BASCOM generates native 8086 code while MKEXE and RUNCOM only provide a stand-alone BetterBASIC environment to run the virtual-machine code that is produced by the MAKE PROGRAM command.

These benchmarks were run on an IBM PC with 512K of RAM, 10Mb hard disk, and a monochrome display. I have also successfully run BetterBASIC on a Leading Edge PC/XT, a PCjr, and a Hyperion PC with no difficulty.

GW BASIC Program Conversion

To put it bluntly, there is no easy conversion from GW BASIC to BetterBASIC. There is a simple procedure outlined at the end of the manual that involves editing an ASCII version of the original. The keywords SOURCE and ENDFILE are added at the start and end of the file for compatibility with BetterBASIC's LIST ALL ASCII storage format. In addition, variable



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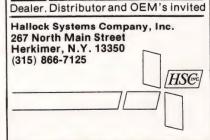
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declarations must be made at the start of the file. If you don't declare the variables, they will be defined on use, but all integers will be defined as reals with the resultant loss of speed and precision that comes from the use of the real data type.

I was able to convert with no difficulty trivial programs such as the Byte benchmarks, and one fairly lengthy musical program came over without much modification. However, large programs such as PC-TALK or RBBS-PC would be easier to rewrite from scratch with BetterBASIC than they would be to convert. This is because of major philosophical differences in the use of procedures and the use of structures for file I/O rather than FIELDs in an I/O buffer. Conceptually, it would probably be easier to translate a Pascal or C program than an existing GW BASIC program.

Refer to Table 3 for a complete summary of the differences between the two BASICs.

The Documentation

If there are two words that describe the BetterBASIC documentation, they are massive and thorough. The manual is an IBM-style 5½ by 8-inch slip-cased binder with over 550 pages! It is broken up into seven sections: General Information, Introduction to BetterBASIC, Quick Reference, Syntax Visuals, Appendices, and two new additions for v1.1—Chain/Overlays and Syscalls.

The syntax visuals are comparable to the IBM BASIC manual with one

or more pages in alphabetical order to describe each command and show examples of proper usage. The introductory chapter is very well done with an excellent tutorial. It is geared to beginners, but will provide a refresher for experienced BASIC programmers. It includes in the lesson on recursion a discussion of static versus dynamic storage of variables that will be useful to many microcomputer programmers. The presentation of variable scope and side effects in the procedure section will be helpful to those programmers whose only previous experience is with other BASICs. Appendices include an ASCII code table, keyboard scan codes, interrupts, character sets, module usage, module defined statements, use of Assembler, error codes, and GW BA-SIC conversion hints.

User Support

Because Summit Software is still a small company, any phone calls in search of support will be handled by one of the program's developers. This kind of direct access can be wonderful, especially when problems involve the more complex aspects of the system. There is no charge for phone support for registered users and Summit can be reached during business hours.

Complaints

As in all new products, there is still room for improvement. My main gripe has to do with errors in the manual. Until I received my v1.1 upgrade, I could find no reference to the PUB-LIC keyword that is necessary for pre-

paring to MAKE MODULEs. There also seems to be some confusion when conceptual words from other languages are used interchangeably with BetterBASIC keywords; structure and record are used in this fashion, although record is never defined to mean the same as structure.

Some of the error codes that the compiler can generate are similar to the warnings of other compilers. I'd like to see the compiler add some of the missing parentheses that it can detect, rather than just notifying the programmer. Also, you cannot specify a program on the command line to be executed by BetterBASIC. This precludes the use of B SETCLOCK to read your clock/calendar in an AUTOEXEC.BAT file.

There is no provision made for tracing program execution, as TRON/TROFF are not supported. I hope the people at Summit are listening and provide a trace that, unlike GW BASIC's primitive version, can also show variable contents, not just line numbers.

My final complaint has to do with price—there is a hidden cost that a potential developer must keep in mind. If you plan to market the software you develop with BetterBASIC, you will need to purchase the Runtime System for an additional \$250 so your customers will be able to run your program without the necessity of purchasing their own copy of BetterBASIC.

Conclusions

So who will find this package useful?

	BetterBASIC	GW BASIC
BENCH1.BAS—write 64K bytes out to a file	14.6	43.3
BENCH2.BAS—read 64K bytes in from a file	10.4	29.1
BENCH3.BAS—5000 floating point operations	92.9(BCD)	69.4
	37.2(BN)	Y)
BENCH4.BAS—calculate all primes from 1 to 700	0 35.2	208.1

Table 4
Performance Benchmarks (All times in seconds)

Probably not the C wizards and Forth gurus of the world. They will feel restrained by the line numbering and the lack of mysterious, syntactic constructions. Pascal programmers will feel right at home with procedures that have local variables and statements like READLINE. I think this package will find its place mostly among programmers who are currently developing vertical market (i.e., business) software in BASIC.

A large percentage of software that is created for vertical markets, such as insurance agencies, medical practices, video rental clubs, and accounting firms, is written in some form of the BASIC language. One reason that is often given for this disproportionate popularity is that BA-SIC is available in some form on most personal computers, thus providing a greater market when the software is ported to a new machine. However, because of the widespread availability of Pascal and C compilers for micros in recent years, I think this reason is less important than the fact that much vertical market software is not developed by trained professional programmers. Often an accountant/ car salesman/real estate broker/ farmer who has learned a little BA-SIC on a personal computer realizes a need for some industry-specific piece of software. This combination of industry expertise and programming experience can result in a very successful program.

In the words of Ivar Wold, Summit's president, these people are faced with the "Pascal Dilemma"they intend to learn Pascal or C but haven't found the time for it yet. With this package, a BASIC programmer will be able to write better. more maintainable code without the learning curve that is associated with learning a new language from scratch. After using BetterBASIC, it will be easy to pick up Pascal with very little effort, as all of Pascal's concepts can be found in BetterBA-SIC. However, BetterBASIC may provide just the excuse never to have to learn a new language again.

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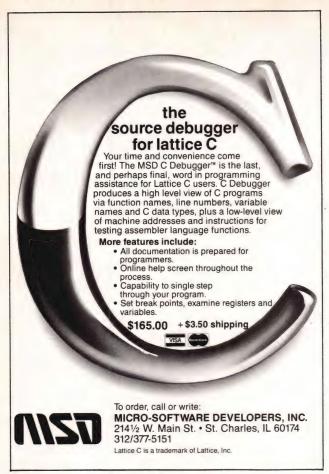
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Better Basic Listing (Text begins on page 108)

```
Sample assembly language interface to a BetterBASIC program
        org
:--- define the Symbol Table
        db
sym1
               sym2-$
                                ; link to next symbol table entry
                'EXCHANGE'
        db
                                ; name of the Procedure
        dw
                                :fill word ( MUST be here !!)
               headeri
                               ;pointer to Procedure header
sym2
       db
               sym3-$
                               :link to next symbol table entry
               'FILL'
                               :name of the Procedure
        db
        dw
               0
                               :fill word ( MUST be here !!)
        dw
               header2
                               :pointer to Procedure header
sym3 db
           0
                                :Symbol Table Terminator
       even
                                :headers must be on
                                : WORD boundary
header1 dw
               154h
                                :MUST be 154h
               exchange
                               ;pointer to actual asm procedure
               2
                                ;number of Arguments
        div
                                ;type of argument 0 (Integer)
        dw
               1
       dw
               1
                                ;type of argument 1 (Integer)
               154h
header2 dw
                               :MUST be 154h
               fill
                              pointer to actual asm procedure
       dw
               2
                               :number of Arguments
        dw
        dw
               3
                               type of argument 0 (String)
               1
        dw
                               ;type of argument 1 (Integer)
; These are the procedures proper
exchange
               proc far
                             ;MUST be a far proc
                                : the EXCHANGE code
exchange
              endp
fill
               proc far
                                ; the FILL code
fill
               endp
```



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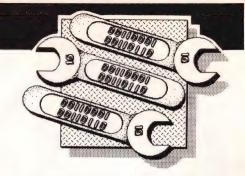
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16-BIT SOFTWARE TOOLBOX



by Ray Duncan

68000 Square Root Routine

Jim Cathey of Spokane, Washington, writes: "To help [eliminate] the dearth of 68000 programming goodies you were mentioning, here is an integer square root program. It takes only about 10–11 times as long to execute as a divide instruction. I found this algorithm a few years back in EDN I thought it was a nifty trick, and saved a clipping for when I needed it. Two muls instructions, one add.l, and this subroutine implement a true 16-bit vector length subroutine, in less than 2000 cycles (250 microsec, at 8 MHz)!

"The original program (and my first attempt) had a bug, in that the intermediate variables were half the size of the input data. This works until the last trip through the loop, where you may have overflow errors that could ruin the accuracy. For speed, the main loop could be unrolled using word intermediates for the first 15 passes, and long on the last loopequivalent. This should save some time if it's a problem. Of course, if speed is really a problem, one of the approximations for vector length you presented a while back would be many times faster." See Listing One (page 122) for the source code for Jim's 68000 square root routine.

MacFeedback

Jim Howell of San Jose, California, a regular correspondent to *DDJ*, writes: "I am writing regarding your recent rather disparaging comments (*DDJ*, December 1984, #98) on the Macintosh. I believe it is much too early for you to be forecasting the demise of the Mac. So 'only' 200,000 Mac's were delivered in its first year. How

many IBM PC's were delivered during its first year? And have you forgotten that even a year after the IBM PC came out, people were still complaining about the lack of software for it? As for comparing the Mac's 'lackluster sales' with the Lisa, I would guess that there are already more Macs than Lisas.

"One point I will agree on is the difficulty of developing software for the Mac. If Apple really wants lots of software for it, they need to make the required technical documentation available (and at a reasonable price). Development tools (compilers and assemblers) need to be able to run using a single Mac. There was an ad in Infoworld recently for a Mac assembler 'that requires no other hardware or software.' I don't know much about the availability of other language processors for the Mac, though I suspect (and hope) that most of them will be usable with only a single Mac. As for 'weak, bug-ridden' high-level languages, again consider the IBM PC. That's probably a fair description of many of the language processors that were available for the IBM PC during its first year. IBM's Pascal compiler, for example, is atrocious.

"I also agree that a 68000 assembler should be able to run with 128K and one disk drive, but comparing that to the DRI 8080 assembler that ran in 32K is very unfair. 8080 assembly language is much simpler than that of the 68000. The 68000 has more addressing modes, more instruction formats, and its assembly language has a more complex syntax. Also, the DRI 8080 assembler must have been a bare-bones, absolute assembler. A bare-bones 68000 assembler could be written to run on a 64K CP/M system (but probably not on a 32K system). A similar assembler should run, therefore, on a 128K Mac. However, I would assume that Apple does not want to put out just a bare-bones assembler. If, for example, the Apple assembler generates object code that can be linked with separately compiled or assembled modules, this would add considerable complexity to the assembler.

"While a Mac assembler should be able to run in 128K, I don't think it's a big problem if it does not. Anyone doing serious software development on the Mac will have to have 512K (the next step after 128K) and two disk drives, even if some of the tools run in 128K. After all, how many developers of IBM PC software have only 128K in their machines? And even fewer have only one disk drive.

"I will be like the other readers who requested 68000 material and not submit any 68000 code. Like most of these readers (probably), I am interested in the 68000, but do not actually have a 68000 system. Therefore, I also do not have any 68000 code to submit. With the attitudes expressed in your column about the 68000 (and about Intel's 80286), one wonders whether you would want to include any 68000 code or material anyway.

"Finally [in reference to another column on identifying IBM PC environments], location F000:FFFEH on my Corona contains 0FEH, just like the PC/XT. Apparently, they want software to think it's running on an XT, just in case a hard disk happened to be attached (yes, I do own an IBM compatible, though I like my 6809 system better). The AT&T computer that we use at work has 00 (zero) at that location (the date in the ROM is 05/03/84)."

This letter from Jim neatly illustrates the polarization that the Mac has induced within the programming community. There is a certain class of people who are dazzled by Apple's "insanely great" publicity and will forgive the Macintosh anything. There is another group, among whom I guess I must number myself, who find Apple's pretentious advertising and grandiose claims a little ridiculous, considering that almost everything that's good about the Mac's user interface was "borrowed" directly from the Xerox Star that has been around for ten years. Speaking of pretentious, see the recent Playboy interview with Steve Jobs for some amusing pronouncements, especially about us programmers over 30 years old.

The fact is that everybody would find the sales of 200,000 Macs in the first year to be very remarkable, if Apple hadn't spouted off all those projections about how it was going to build a Mac every 15 seconds in their new super MacFactory. To argue that the Mac has already sold more machines than the Lisa is not what you'd call a devastating rejoinder, since Apple has already thrown in the towel on the Lisa and reincarnated it as the Macintosh XL.

As for the Macintosh assembler, I feel that I'm on very solid ground with my previous comments, having written several assemblers myself. The fact that the instruction set of the 68000 is powerful and extensive cuts both ways: of course it means that you have to worry about assembling more complex syntax, but it also means that you have a much more powerful language in which to write the assembler. Unless, of course, you are so bull-headed as to insist on writing your assembler in a high-level language like Pascal, which is completely unsuited to the job. Then you do end up with monstrosities like the Apple Macintosh Assembler or the Microsoft 8086 Macro Assembler. The proof of my contention has been conveniently provided by the product "MacASM" from Mainstay of Agoura Hills, California. This is an integrated full screen editor, macro assembler, and resource compiler for the Macintosh that runs nicely in 128K and costs only \$125.00. "MacASM" is not copy protected and supports multiple

drives including hard disks. Mainstay can be reached at (818) 991-6540.

MSDOS Device Drivers

Stan Mitchell of San Jose, California, writes: "I thought I would pass along a tool that comes in handy when working with MSDOS device drivers. It depends upon a useful property of the NUL device driver. Unlike other devices, it is embedded in the DOS module (IBMDOS.COM for the PC) and its device header is at the head of the device chain. By using

the next device pointer in successive headers, the complete chain can be revealed. It is interesting that if any installed devices are present, the NUL header points to the first of these. When installed devices are exhausted, the link continues with the resident devices (contained in IBM-BIO.COM for the PC).

"The only 'trick' in the program is using the OPEN function to return the device header pointer. This is an undocumented feature of MSDOS."

Readers, please note that the format of the reserved area in the file

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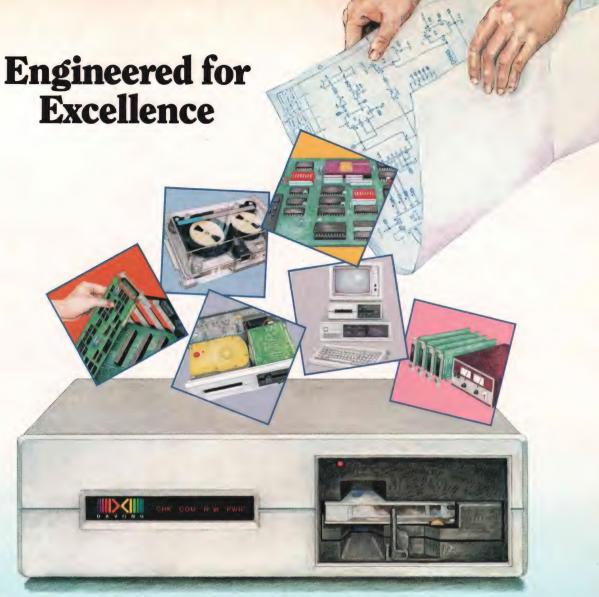
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control block is different under MSDOS 2.0 than it is under version 3.0. Stan's C program and the assembly language source for the _peek function are provided as Listing Two (page 122) and Listing Three (page 125).

Microsoft Assembler Bug of the Month

Gregor Owen of Port Jefferson Station, New York, writes: "I don't know if anyone's still counting, but here's my contribution for Microsoft Assembler bug of the month. In the assembly below, the code es:lodsb produces an error; however, the code below it, mov ax,es:[si+1], produces an error too. If you comment out the es:lodsb, both errors disappear—so superior to the old-fashioned kind of assembler, where frequently the removal of defective code only removes errors in the vicinity of the defect.

"At this point, I don't really know if es:lodsb represents a legal operation. I don't happen to have an Intel book at this location, and since every 8086/88 instruction has a unique set of addressing modes, it's difficult to get any kind of feel for what's allowed. Needless to say, the IBM/Microsoft assembler manual is not helpful. It's true that if one comments out es:lodsb and assembles lods byte ptr es:[si+0] both errors disappear, but I don't think anybody who's used the Microsoft assembler for any short while would take any comfort from that.

"And while we're on the subject, notice the expressive elegance and beauty of the phrase

lods byte ptr es: [si + 0]

typical of the language Intel has provided for us. The si+0, I concede, is something I've been doing ever since I managed to assemble some kind of complicated expression that had [si] in it—producing code that referenced [si+0FFFFH] and didn't generate an assembler error. Of course I probably don't have to use si+0 in every expression, but who wants to spend the time to find out? That's the beauty of the language: every programmer, in order to produce stuff that works at

all, will develop a private dialect containing much myth and legend, thus making the source even more incomprehensible than it is anyway.

"My personal theory about 8086/ 88 assembly language is that what we have here is a marketing coup on the part of Intel. They had a ridiculous processor, with the traditional inept Intel approach to registers and addressing. Somehow, they sensed it would be awfully embarrassing to expose this thing in all the stark simplicity of their 'lxi h,7'-type assembler [for the 8080 among other processors]. So they commissioned the finest, most convoluted, Pascalized minds in the software industry to come up with an assembler so grand and structured and incredibly intricate that no one would ever notice how awful the thing was for which it was assembling. Judging by the results, they seem to have succeeded.

"And those of us who labor in the vineyards of the information revolution can't be all too high-horsey about this: after all, the Intel/Microsoft language makes it almost impossible for those pesky amateurs to figure anything out, thus preserving the world of 8086/88 assembler for us, the elect: people paid to spend the necessary endless hours figuring out how to use the language and avoid the Microsoft bugs. A sort of 'programmer's employment project' for the new age."

Gregor's program is provided as Listing Four (page 126). My uneducated guess, in this case, is that the Microsoft Assembler interprets the es: portion of the statement es:lodsb as the definition of a label named es. It then gives you the error message "symbol already different type," since the name es is already hardwired into the symbol table as an assembler directive. All the other errors follow from the ensuing type conflicts.

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mov bx,0ffffh

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mov ax,[bx]

The system dies immediately. The same sequence of instructions will run just fine on an 8086 or 8088-based machine. What is happening here?

It turns out that on the 80286, accessing a word operand at offset 0FFFFH, i.e., a segment wrap, causes

a hardware interrupt 13H. Alas (as Jerry Pournelle would say), this interrupt is already used by the IBM PC ROM BIOS for the disk driver. So when your program accidentally generates a segment wrap fault, it traps to the disk driver with nonsense parameters in the registers and probably writes garbage all over your nice

hard disk. This kind of phenomenon, when we move a previously happy program from an IBM PC to a PC/AT and watch it go to heaven, helps us while away those rainy afternoons.

DDJ

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16-Bit Toolbox (Text begins on page 118) Listing One

```
Integer Square Root (32 to 16 Bit)
   (Exact method, not approximate)
   Call with:
          DO.L = Unsigned number
  Returns:
          D0.L = SQRT (D0.L)
          D1.L <> 0 if not exact root
  Uses:
          D1-D4 as temporaries ---
          D1 = Error term
          D2 = Running estimate
          D3 = High bracket
          D4 = Loop counter
  Notes:
          Result first in DO.W, but is valid in longword.
          Takes from 1480 to 1832 cycles (including RTS).
          (Word version is from 548 to 660 cycles).
lsgrt
          move.w
                     #15,d4
                               ; Loop count (bits-1 of result)
          moveq
                     #0,d1
                               ; Result in D1
          moveq
                     #0,d2
sgrtl
          asl.1
                     #1,d0
                               ; Get 2 leading bits at a time and
          roxl.1
                     #1,d1
                                into Error term for extrapolation.
                               ; (Classical method, easy in binary)
          asl.1
                     #1,d0
          rox1.1
                     #1,d1
          asl.1
                     #1,d2
                               ; Running estimate * 2
          move.1
                    d2,d3
          asl.l
                     #1,d3
          cmp.1
                    d3,d1
          bls
                    sqrt2
                               ; New error term > 2* running est.?
                     #1,d2
          addq.1
                               ; Yes, we want 1 bit then.
          addq.1
                     #1,d3
                               ; Fix up new error term.
          sub.1
                    d3,d1
sqrt2
          dbra
                    d4, sqrtl
                               ; Do all 16 bit-pairs.
                               ; Returns answer in DO.W
          movel
                    d2, d0
          rts
```

End Listing One

Listing Two

"C" source code for Stan Mitchell's program to dump the device driver chain.

```
1
2
     * dd_dump.c
                     program for displaying device driver chain
3
                     for DOS 2.00, 2.10, 3.00
5
     * by stan mitchell
                     november 21,1984
6
                     Lattice C v. 1.04
     7
8
9
    #define void int
10
    #define byte char
    #include "stdio.h"
11
12
```

(Continued on page 124)



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16-Bit Toolbox (Listing Continued, text begins on page 118) Listing Two

```
13
        /* DOS function calls */
        #define OPENFCB
14
15
        #define CLOSEFCB 0x10
16
        #define VERSION 0x30
17
18
        extern int _peek();
19
20
                DEVHDR
        struct
21
22
                            nxthdr_off; /* doubleword ptr to next device hdr in chain */
                 unsigned
23
                            nxthdr_seg;
                 unsigned
24
                                         /* type of device driver */
                 unsigned
                            attr:
25
                 unsigned
                            strat;
                                         /* device strategy entry point */
                                         /* interrupt entry point */
26
                 unsigned
                            introt:
27
                            dname[8];
                                         /* device name */
28
                 ):
29
30
        struct
               FCB
31
32
                            drive;
                                          /* drive designator */
                 bute
33
                            fname[11]:
                                          /* file or device name */
                 char
34
                                          /* current blk (set to 0 by OPENFCB) */
                            curblk:
                 unsigned
35
                 unsigned
                            recsiz;
                                          /* logical record size (set to 0x80 by OPENFCB) */
36
                                          /* file size in bytes */
                 lone
                            fsize:
37
                 unsigned
                            date;
                                          /* creation or last update date */
                            sys_rsv[10]; /* fields reserved for DOS */
38
                 bute
39
                 byte
                            bset[5];
                                          /* relative record numbers */
40
                 );
41
42
       struct
                RSV2 X
43
44
                                          /* creation or last update time */
                 unsigned
                            time;
45
                            attribute;
                                          /* device or file attribute */
                 byte
                                          /* offset address of device header */
46
                            dhdr_off;
                 unsigned
47
                 unsigned
                            dhdr_seg;
                                          /* segment address of device header */
                                          /* unknown usage */
48
                            unk[3];
                 bute
49
                 );
50
51
        struct RSV3 X
52
53
                 unsigned
                            time;
                                          /* creation or last update time */
                            attribute;
54
                 unsigned
                                          /* device or file attribute */
55
                 unsigned
                            dhdr_off;
                                          /* offset address of device header */
56
                 unsigned
                            dhdr_seg;
                                          /* segment address of device header */
57
                            unk[2];
                                          /* unknown usage */
                 byte
58
                 );
59
60
        struct DEVHDR
                                           /* device header */
                         device;
61
        struct FCB
                          device_fcb;
                                           /* standard FCB for device */
62
        struct
                 RSV2 X
                          *do52x;
                                           /* reserved field definitions DOS 2.x */
63
        struct
                RSV3_X
                         *dos3x;
                                           /* reserved field definitions DOS 3.x */
65
        main()
66
67
        int
                  dev_off, dev_seg, i, j;
68
69
        initfcb(0, "NUL
                              "); /* set up standard FCB for NUL device */
70
        if (bdos(OPENFCB,&device_fcb) & 0xff) /* open the device */
71
72
                 printf("Unable to open device\n");
73
                 exit(1);
74
75
        if ((bdos(VERSION, 0) & 0xff) == 3)
76
                 ( /* the reserved fields are allocated differently in DOS 3.x */
77
                 dos3x=(struct RSV3_X *) &device_fcb.sys_rsv[0];
78
                 dev_off=dos3x->dhdr off;
79
                 dev_seg=dos3x->dhdr_seg;
80
81
        else if ((bdos(VERSION,0) & 0xff) == 2)
82
                 ( /* ... than they were in DOS 2.x */
                 dos2x=(struct RSV2_X *) &device_fcb.sys_rsv[0];
83
84
                 dev_off=dos2x->dhdr_off;
                 dev_seg=dos2x->dhdr_seg;
```

```
86
87
        else
                 ( /* forget it for DOS 1.10 or earlier */
88
                 printf (" DOS 2.00 or newer required\n");
89
99
                 exit(1):
91
92
93
        printf(" Device driver chain .... \n");
94
        printf("\n");
95
         /* display the current DOS chain of device drivers */
96
97
        printf(" ptr type name
                                            strategy ptr interrupt ptr \n");
98
99
100
           printf("%04x:%04x ",dev_seg,dev_off); /* device header ptr */
101
           if (dev off == 0xffff) break;
                                                  /* if last in chain, offset=0xffff */
102
           /* move the device header into the data segment structure "device" */
103
            _peek(dev_seg,dev_off,&device,sizeof(device));
194
           printf(" 7.04x ", device.attr);
                                                  /* display the device attribute word */
105
            if ((device.attr & 0x8000) == 0)
                                                  /* if a block device ... */
106
                  for (j=0;j<=3;j++) printf("%02x",device.dname[j]); /* there is no device name */
107
108
                 for (j=0;j<=7;j++) printf("%c",device.dname[j]); /* otherwise display device name */
199
110
                           7.04x:7.04x
                                           *,dev_seg,device.strat); /* strategy entry point */
111
            printf("7.04x:7.04x \n", dev_seg, device.intrpt); /* "interrupt" entry point */
112
113
            dev_seg=device.nxthdr_seg; /* set up ptr to next device header */
            dev off=device.nxthdr_off; /* and loop back to display it */
114
115
116
        )
117
118
        /* initialize standard FCB */
119
120
                  initfcb(drv,name)
        byte
121
                  dry:
122
         char
                  name[];
123
          (
124
125
126
          device fcb.drive=drv; /* drive designation: default drive=0, A=1, etc. */
127
          for (i=0;i(=10;i++) device fcb.fname[i]=name[i]; /* device or file name */
          for (i=0;i<=4;i++) device_fcb.bset[i]=0; /* fields not zeroed by open call */
128
129
                                                                                  End Listing Two
139
```

Listing Three

Assembly language source for "peek" library function, used in Stan Mitchell's device driver dump program.

```
PAGE
                            PAGE
                                 62,132
                                 peek
                      void _peek(segment,offset,buffer,nbytes)
                            unsigned segment; /# segment portion of memory addr */
                            unsigned offset; /* offset portion of memory addr */
                                  *buffer; /* local memory buffer (in data segment) */
                            unsigned obytes; /* number of bytes to transfer */
                            Lattice C assembly language interface convention followed
                               (ES=DS on entru)
                      = 0004
                      EXTRA
                            EQU
                      PGROUP GROUP
                                 PROG
                            SEGMENT BYTE PUBLIC 'PROG'
0000
                            PUBLIC PEEK
                            ASSUME CS: PGROUP
```

(Continued on next page)

16-Bit Toolbox (Listing Continued, text begins on page 118) **Listing Three**

0000		PEEK	PROC	NEAR	
0000	55	_	PUSH	BP	;
0001	8B EC		MOV	BP, SP	,
0003	1E		PUSH	DS	,
0004	56		PUSH	SI	,
0005	57		PUSH	DI	,
0006	51		PUSH		,
			1	•	,
				ource seq	ment
0007	8E 5E 04		MOV		PTR [BP+EXTRA]
			:	,	THE LEW LANDING
			inet s	ource off	set
999A	8B 76 96				PTR [BP+EXTRA+2]
			1	O1, HOND	THE CONTENTION 23
				estinatio	n offset in ES
000D	8B 7E 08		MOV		PTR [BP+EXTRA+4]
			1	DI, WOND	TIN CONTEXTINATES
				ute count	for transfer
0010	8B 4E 0A		MOV		PTR [BP+EXTRA+6]
			1	ONTHORD	THE EDITENTION
				the hutes	into local buffer
0013	F3/ A4		REP	MOVSB	Into local pariet
			-	110100	
0015	59		POP	CX	
9916			POP		;
0017				SI	*
0018			POP	DS	
9019			POP	BP	5
	C3		RET	LA .	3
991B	~	PEEK	ENDP		i
001B		PROG	ENDS		
VVID		1 1100	END		
			CNU		

End Listing Three

Listing Four

```
Gregor Owen's Microsoft Assembler Bug of the Month. Assemble this brief program for some entertaining error messages, then comment out the line "es:lodsb" and assemble it again.
```

```
microwiff segment
```

assume cs:microwiff,ds:microwiff,es:microwiff

microwimp:

```
es:lodsb
lods byte ptr es:[si+0]
mov ax,es:[si+1]

microwiff ends
end microwimp
```

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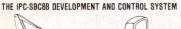
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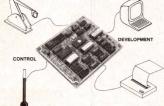


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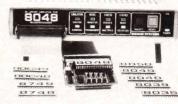
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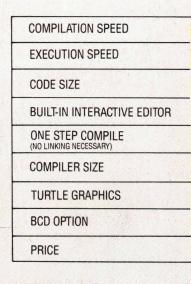
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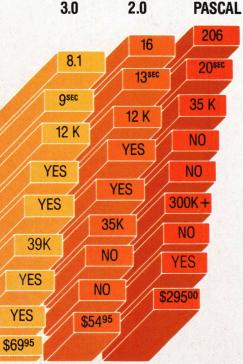
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